54AC139,54ACT139

54AC139 54ACT139 Dual 1-of-4 Decoder/Demultiplexer



Literature Number: SNOS086



54AC139 • 54ACT139 Dual 1-of-4 Decoder/Demultiplexer

General Description

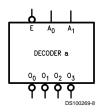
The 'AC/'ACT139 is a high-speed, dual 1-of-4 decoder/ demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually-exclusive active-LOW outputs. Each decoder has an active-LOW Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the 'AC/'ACT139 can be used as a function generator providing all four minterms of two variables.

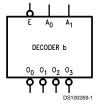
- Multifunction capability
- Two completely independent 1-of-4 decoders
- Active LOW mutually exclusive outputs
- Outputs source/sink 24 mA
- 'ACT139 has TTL-compatible inputs
- Standard Military Drawing (SMD)
 - 'AC139: 5962-87623
 - 'ACT139: 5962-87553

Features

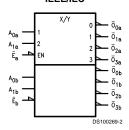
■ I_{CC} reduced by 50%

Logic Symbols





IEEE/IEC

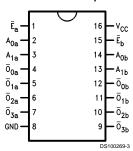


Pin Names	Description
A ₀ , A ₁	Address Inputs
Ē	Enable Inputs
$\overline{O}_0 - \overline{O}_3$	Outputs

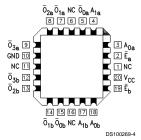
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Connection Diagrams

Pin Assignment for DIP and Flatpak



Pin Assignment for LCC



Functional Description

The 'AC/'ACT139 is a high-speed dual 1-of-4 decoder/ demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs (A₀-A₁) and provides four mutually exclusive active-LOW outputs $(\overline{O}_0 - \overline{O}_3)$. Each decoder has an active-LOW enable (\overline{E}) . When \overline{E} is HIGH all outputs are forced HIGH. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the 'AC/'ACT139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in , and thereby reducing the number of packages required in a logic network.

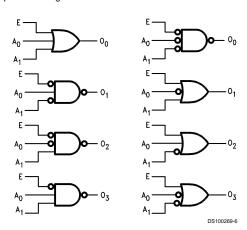


FIGURE 1. Gate Functions (Each Half)

Truth Table

	Inputs		Outputs				
Ē	A _o	A ₁	O _o	Ō₁	\overline{O}_2	\overline{O}_3	
Н	Х	Х	Н	Н	Н	Н	
L	L	L	L	Н	Н	Н	
L	Н	L	Н	L	Н	Н	
L	L	Н	Н	Н	L	Н	
L	Н	Н	Н	Н	Н	L	

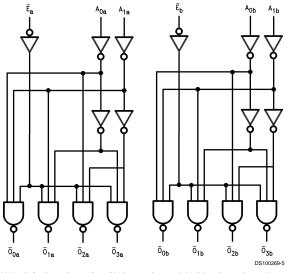
H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

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Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage (V_{CC}) -0.5V to +7.0V DC Input Diode Current (IIK) $V_1 = -0.5V$ -20 mA $V_I = V_{CC} + 0.5V$ +20 mA DC Input Voltage (V_I) -0.5V to $V_{\rm CC}$ + 0.5V DC Output Diode Current (I_{OK}) $V_{O} = -0.5V$ -20 mA $V_O = V_{CC} + 0.5V$ +20 mA DC Output Voltage (V_O) -0.5V to $V_{\rm CC}$ + 0.5V DC Output Source or Sink Current (I_O) ±50 mA DC V_{CC} or Ground Current per Output Pin (I_{CC} or I_{GND}) ±50 mA

Storage Temperature (T_{STG})

-65°C to +150°C

Junction Temperature (T_J)

CDIP 175°C

Recommended Operating Conditions

Supply Voltage (V_{CC})

 $\begin{tabular}{lll} 'AC & 2.0V to 6.0V \\ 'ACT & 4.5V to 5.5V \\ Input Voltage (V_I) & 0V to V_{CC} \\ Output Voltage (V_O) & 0V to V_{CC} \\ \end{tabular}$

Operating Temperature (T_A)

Minimum Input Edge Rate $(\Delta V/\Delta t)$

'AC Devices

 $\rm V_{IN}$ from 30% to 70% of $\rm V_{CC}$

V_{CC} @ 3.3V, 4.5V, 5.5V 125 mV/ns

Minimum Input Edge Rate $(\Delta V/\Delta t)$

'ACT Devices

 V_{IN} from 0.8V to 2.0V

 V_{CC} @ 4.5V, 5.5V 125 mV/ns

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT® circuits outside databook specifications.

DC Characteristics for 'AC Family Devices

			54AC		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	−55°C to		
			+125°C		
			Guaranteed		
			Limits		
V_{IH}	Minimum High Level	3.0	2.1		V _{OUT} = 0.1V
	Input Voltage	4.5	3.15	V	or V _{CC} – 0.1V
		5.5	3.85		
V_{IL}	Maximum Low Level	3.0	0.9		V _{OUT} = 0.1V
	Input Voltage	4.5	1.35	V	or V _{CC} – 0.1V
		5.5	1.65		
V _{OH}	Minimum High Level	3.0	2.9		I _{OUT} = -50 μA
	Output Voltage	4.5	4.4	V	
		5.5	5.4		
					(Note 2)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		3.0	2.4		I _{OH} = -12 mA
		4.5	3.7	V	I _{OH} = -24 mA
		5.5	4.7		$I_{OH} = -24 \text{ mA}$
V _{OL}	Maximum Low Level	3.0	0.1		I _{OUT} = 50 μA
	Output Voltage	4.5	0.1	V	
		5.5	0.1		
					(Note 2) V _{IN} = V _{II} or V _{IH}
		3.0	0.50		I _{OL} = 12 mA
		4.5	0.50	V	I _{OL} = 24 mA
		5.5	0.50		I _{OL} = 24 mA
I _{IN}	Maximum Input	5.5	±1.0	μA	$V_I = V_{CC}$, GND
	Leakage Current			1	

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DC Cha	racteristics for 'AC	Family De	evices (Continued))	
			54AC		
Symbol	Parameter	V _{cc} (V)	T _A = -55°C to +125°C	Units	Conditions
			Guaranteed Limits		
loup	(Note 3) Minimum Dynamic	5.5	50	mA	V _{OLD} = 1.65V Max

-50

80.0

mΑ

μΑ

5.5

Note 2: All outputs loaded; thresholds on input associated with output under test.

Note 3: Maximum test duration 2.0 ms, one output loaded at a time.

Maximum Quiescent

Output Current

Supply Current

Note 4: I_{IN} and I_{CC} @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V V_{CC} .

 I_{CC} for 54AC @ 25°C is identical to 74AC @ 25°C.

I_{OHD}

 I_{CC}

DC Characteristics for 'ACT Family Devices

			54ACT		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	–55°C to		
			+125°C		
			Guaranteed		
			Limits		
V_{IH}	Minimum High Level	4.5	2.0	V	V _{OUT} = 0.1V
	Input Voltage	5.5	2.0		or V _{CC} – 0.1V
V _{IL}	Maximum Low Level	4.5	0.8	V	V _{OUT} = 0.1V
	Input Voltage	5.5	0.8		or V _{CC} – 0.1V
V _{OH}	Minimum High Level	4.5	4.4	V	I _{OUT} = -50 μA
	Output Voltage	5.5	5.4		
					(Note 5)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	3.70	V	$I_{OH} = -24 \text{ mA}$
		5.5	4.70		$I_{OH} = -24 \text{ mA}$
V_{OL}	Maximum Low Level	4.5	0.1	V	I _{OUT} = 50 μA
	Output Voltage	5.5	0.1		
					(Note 5)
					$V_{IN} = V_{IL}$ or V_{IH}
		4.5	0.50	V	I _{OL} = 24 mA
		5.5	0.50		I _{OL} = 24 mA
I _{IN}	Maximum Input	5.5	±1.0	μA	$V_{I} = V_{CC}$, GND
	Leakage Current				
I _{CCT}	Maximum I _{CC} /Input	5.5	1.6	mA	$V_I = V_{CC} - 2.1V$
	(Note 6)				
I_{OLD}	Minimum Dynamic	5.5	50	mA	V _{OLD} = 1.65V Max
I _{OHD}	Output Current	5.5	-50	mA	V _{OHD} = 3.85V Min
I _{cc}	Maximum Quiescent	5.5	80.0	μA	V _{IN} = V _{CC}
	Supply Current				or GND

Note 5: All outputs loaded; thresholds on input associated with output under test.

Note 6: Maximum test duration 2.0 ms, one output loaded at a time.

Note 7: I_{CC} for 54ACT @ 25°C is identical to 74ACT @ 25°C.

V_{OHD} = 3.85V Min

 $V_{IN} = V_{CC}$

or GND

			54	AC		
Symbol	Parameter	V _{cc} (V) (Note 8)	T _A = -55°C to +125°C C _L = 50 pF		Units	Fig. No.
			t _{PLH}	Propagation Delay	3.3	1.0
	A_n to \overline{O}_n	5.0	1.0	11.0		
t _{PHL}	Propagation Delay	3.3	1.0	12.5	ns	
	A_n to \overline{O}_n	5.0	1.0	10.0		
t _{PLH}	Propagation Delay	3.3	1.0	14.5	ns	
	\overline{E}_{n} to \overline{O}_{n}	5.0	1.0	11.0		
t _{PHL}	Propagation Delay	3.3	1.0	12.5	ns	
	\overline{E}_{n} to \overline{O}_{n}	5.0	1.0	10.0		

Note 8: Voltage Range 3.3 is 3.3V ±0.3V Voltage Range 5.0 is 5.0V ±0.5V

AC Electrical Characteristics

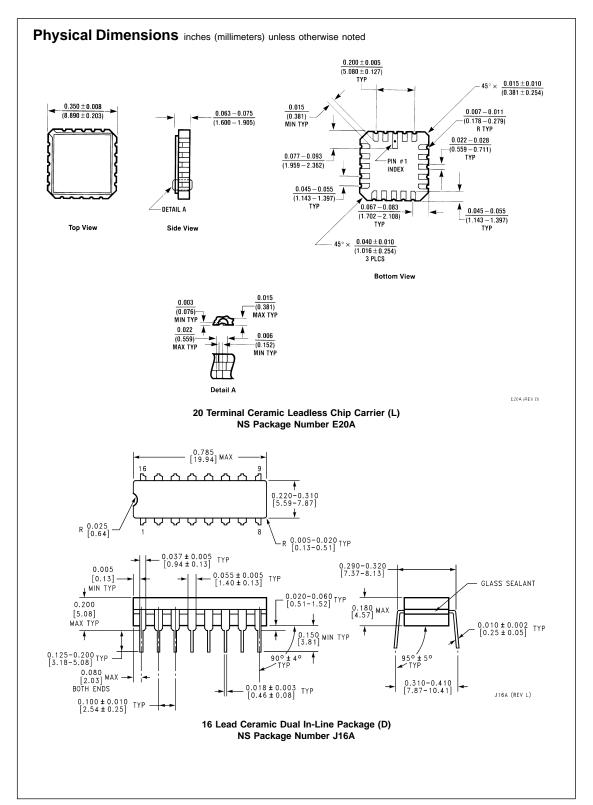
Symbol	Parameter	V _{CC} (V) (Note 9)	54ACT T _A = -55°C to +125°C C _L = 50 pF		Units	Fig. No.
			Min	Max		
t _{PLH}	Propagation Delay A_n to \overline{O}_n	5.0	1.0	12.0	ns	
t _{PHL}	Propagation Delay A _n to O n	5.0	1.0	11.0	ns	
t _{PLH}	Propagation Delay E _n to O _n	5.0	1.0	12.5	ns	
t _{PHL}	Propagation Delay E _n to O _n	5.0	1.0	12.0	ns	

Note 9: Voltage Range 5.0 is 5.0V ±0.5V

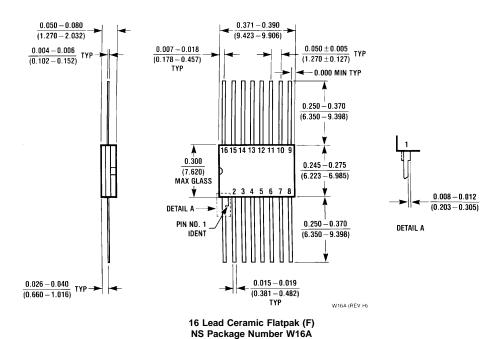
Capacitance

Symbol	Parameter	Тур	Units	Conditions	
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = OPEN	
C _{PD}	Power Dissipation	40.0	pF	$V_{CC} = 5.0V$	
	Capacitance				

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Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



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