54F280,74F280

54F280 9-Bit Parity Generator/Checker



Literature Number: SNOS183A



54F/74F280 9-Bit Parity Generator/Checker

General Description

The 'F280 is a high-speed parity generator/checker that accepts nine bits of input data and detects whether an even or an odd number of these inputs is HIGH. If an even number of inputs is HIGH, the Sum Even output is HIGH. If an odd number is HIGH, the Sum Even output is LOW. The Sum

Odd output is the complement of the Sum Even output.

Features

■ Guaranteed 4000V minimum ESD protection

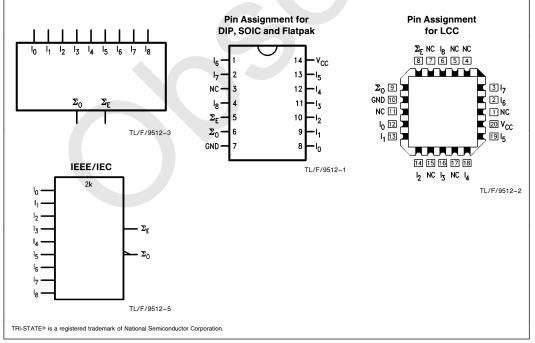
Commercial	Military	Package Number	Package Description
74F280PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line
	54F280DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line
74F280SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F280SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ
	54F280FM (Note 2)	W14B	14-Lead Cerpack
	54F280LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

Logic Symbols

Connection Diagrams



Unit Loading/Fan Out

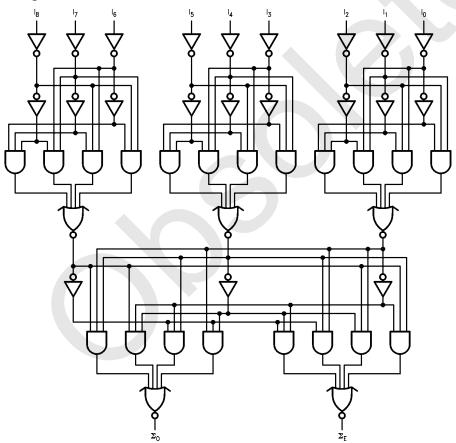
		54F/74F				
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}			
I ₀ -I ₈	Data Inputs	1.0/1.0	20 μA/-0.6 mA			
Σ_{O}	Odd Parity Output	50/33.3	-1 mA/20 mA			
Σ_{E}	Even Parity Output	50/33.3	−1 mA/20 mA			

Truth Table

Number of	Outputs				
HIGH Inputs I ₀ -I ₈	Σ Even	Σ Odd			
0, 2, 4, 6, 8	Н	L			
1, 3, 5, 7, 9	L	Н			

H = HIGH Voltage Level L = LOW Voltage Level

Logic Diagram



TL/F/9512-4

Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 $\begin{array}{lll} \text{Storage Temperature} & -65^{\circ}\text{C to} + 150^{\circ}\text{C} \\ \text{Ambient Temperature under Bias} & -55^{\circ}\text{C to} + 125^{\circ}\text{C} \\ \text{Junction Temperature under Bias} & -55^{\circ}\text{C to} + 175^{\circ}\text{C} \\ \text{Plastic} & -55^{\circ}\text{C to} + 150^{\circ}\text{C} \\ \end{array}$

V_{CC} Pin Potential to

Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$)

 $\begin{array}{lll} \text{Standard Output} & -0.5 \text{V to V}_{CC} \\ \text{TRI-STATE} \odot \text{Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$

Current Applied to Output in LOW State (Max) twice the rated I_{OL} (mA) ESD Last Passing Voltage (Min) 4000V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature

Supply Voltage

Military + 4.5V to + 5.5V Commercial + 4.5V to + 5.5V

DC Electrical Characteristics

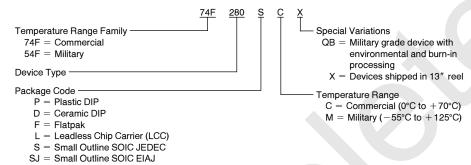
Symbol	Parameter .		54F/74F			Units	v _{cc}	Conditions	
Symbol			Min	Тур	Max	Oilles	VCC	Conditions	
V_{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal		
V_{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V_{CD}	Input Clamp Diode Vo	oltage			-1.2	V	Min	$I_{IN} = -18 \text{ mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	٧	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
I _{IH}	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V_{ID}	Input Leakage Test	74F	4.75			٧	0.0	$I_{\text{ID}} = 1.9 \mu\text{A}$ All Other Pins Grounded	
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
I _{IL}	Input LOW Current			-0.6	mA	Max	V _{IN} = 0.5V		
los	Output Short-Circuit C	-60		-150	mA	Max	V _{OUT} = 0V		
Іссн	Power Supply Curren		25	38	mA	Max	V _O = HIGH		

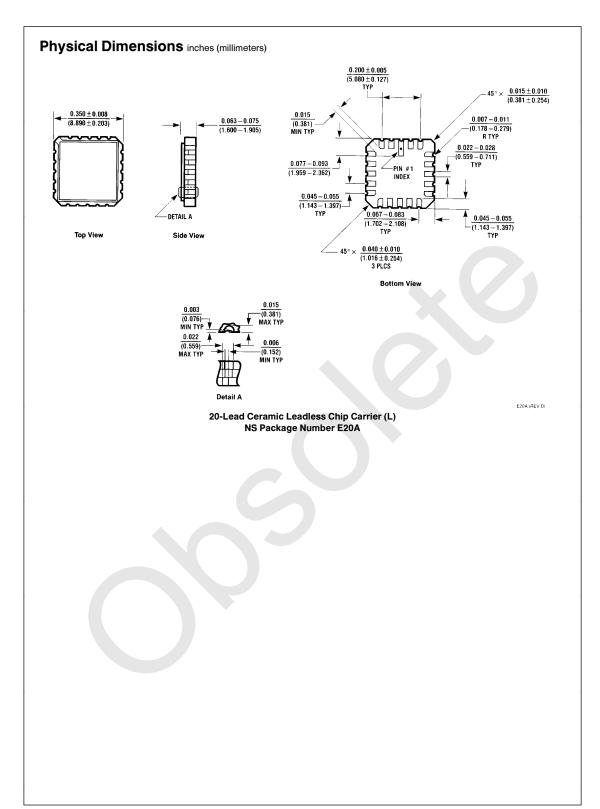
AC Electrical Characteristics

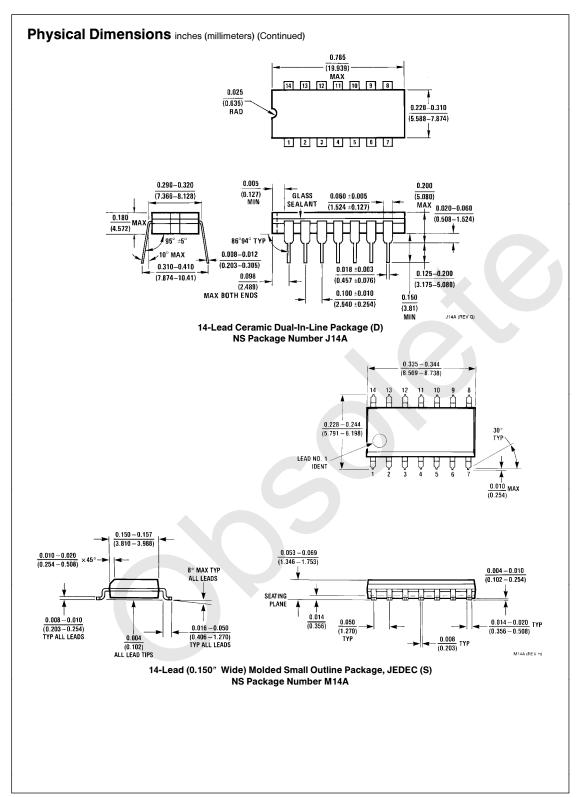
		74F			54F		74F		
Symbol Parameter		$\begin{aligned} \textbf{T}_{\textbf{A}} &= +25^{\circ}\textbf{C} \\ \textbf{V}_{\textbf{CC}} &= +5.0\textbf{V} \\ \textbf{C}_{\textbf{L}} &= \textbf{50}~\textbf{pF} \end{aligned}$			$ extsf{T}_{ extsf{A}}, extsf{V}_{ extsf{CC}} = extsf{Mil} \ extsf{C}_{ extsf{L}} = extsf{50 pF}$		T _A , V _{CC} = Com C _L = 50 pF		Units
		Min	Тур	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay I_n to Σ_E	6.5 6.5	10.0 11.0	15.0 16.0	6.5 6.5	20.0 21.0	6.5 6.5	16.0 17.0	ns
t _{PLH}	Propagation Delay I_n to Σ_O	6.0 6.5	10.0 11.0	15.0 16.0	5.0 6.5	20.0 21.0	6.0 6.5	16.0 17.0	ns

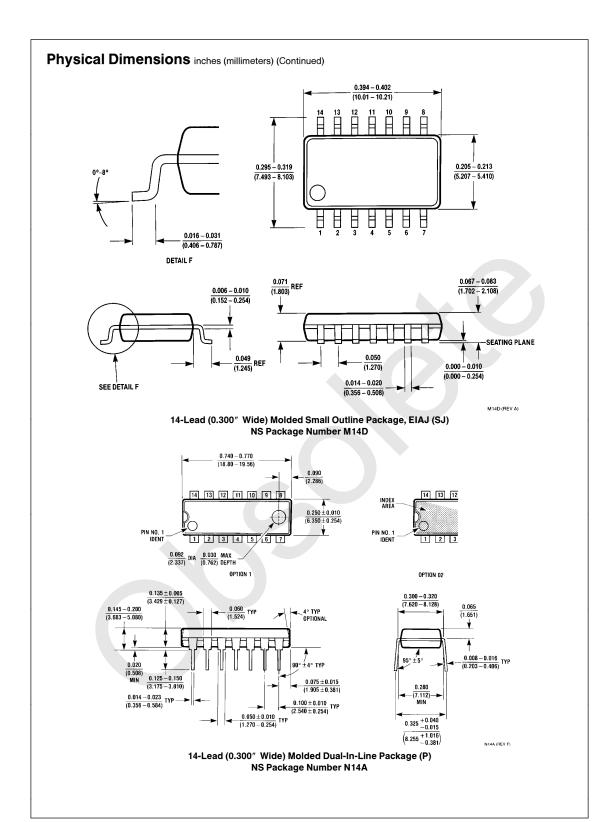
Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

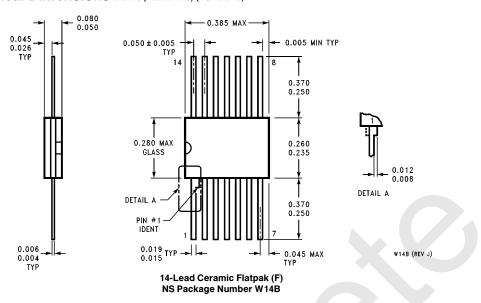








Physical Dimensions inches (millimeters) (Continued)



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