54LS10,DM54LS10,DM74LS10

54LS10 DM54LS10 DM74LS10 Triple 3-Input NAND Gates



Literature Number: SNOS277A



54LS10/DM54LS10/DM74LS10 Triple 3-Input NAND Gates

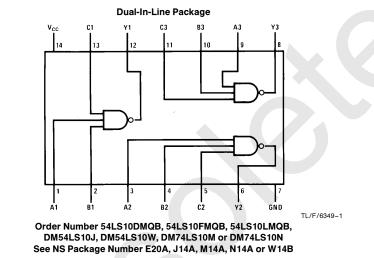
General Description

Features

This device contains three independent gates each of which performs the logic NAND function.

 Alternate Military/Aerospace device (54LS10) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Connection Diagram



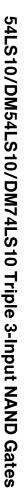
Function Table

$\mathbf{Y} = \mathbf{ABC}$							
	Inputs		Output				
Α	в	С	Y				
X X	Х	L	н				
X	L	х	н				
L	Х	х	н				
н	н	Н	L				

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level



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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM54LS and 54LS	-55°C to +125°C
DM74LS	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS10			DM74LS10			Units
Cymbol	i arameter	Min	Nom	Max	Min	Nom	Max	onnto
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current			-0.4			-0.4	mA
I _{OL}	Low Level Output Current			4			8	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

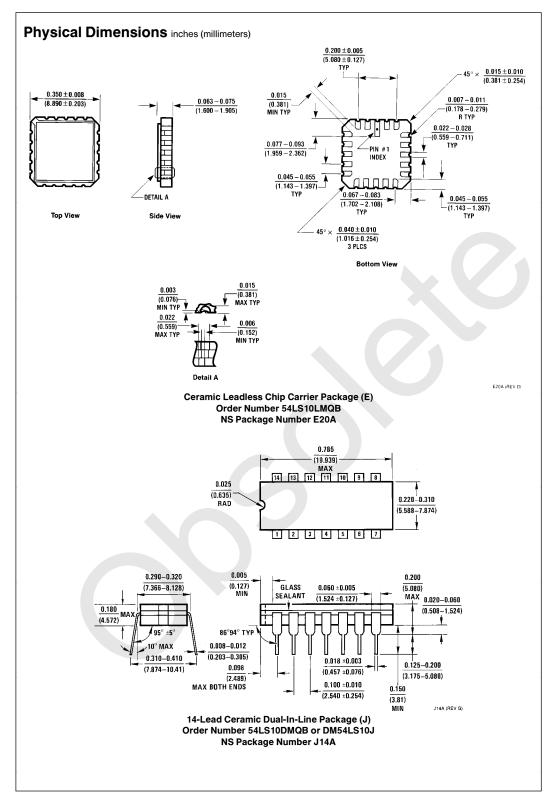
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min$, $I_{I} = -18 \text{ mA}$				-1.5	V
V _{OH}	High Level Output	$V_{CC} = Min, I_{OH} = Max, V_{IL} = Max$	DM54	2.5	3.4		v
	Voltage		DM74	2.7	3.4		
V _{OL}	Low Level Output	$\label{eq:V_CC} \begin{split} V_{CC} &= \text{Min}, \text{I}_{OL} = \text{Max}, \\ V_{IH} &= \text{Min} \end{split}$	DM54		0.25	0.4	v
Volta	Voltage		DM74		0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$	DM74		0.25	0.4	
l _l	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.36	mA
los	Short Circuit	V _{CC} = Max	DM54	-20		- 100	mA
	Output Current	(Note 2)	DM74	-20		- 100	
I _{CCH}	Supply Current with Outputs High	V _{CC} = Max			0.6	1.2	mA
ICCL	Supply Current with Outputs Low	V _{CC} = Max			1.8	3.3	mA

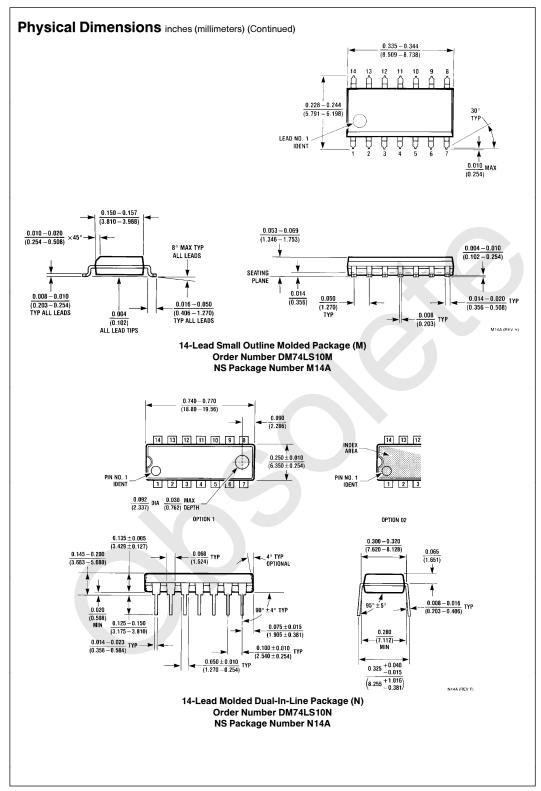
Switching Characteristics at V_{CC} = 5V and T_A = 25°C (See Section 1 for Test Waveforms and Output Load)

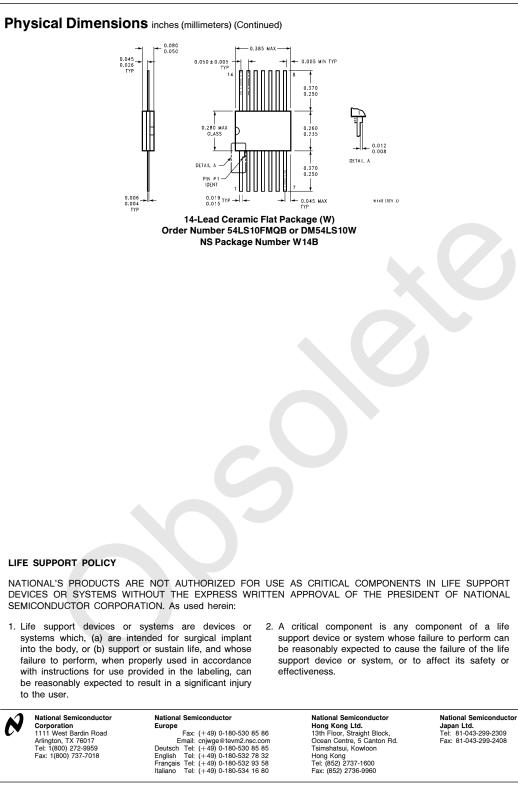
	Parameter	$R_L = 2 k\Omega$				
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Мах	
t _{PLH}	Propagation Delay Time Low to High Level Output	3	10	4	15	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	3	10	4	15	ns
Note 1: All typicals	s are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.	•		•		

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.









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