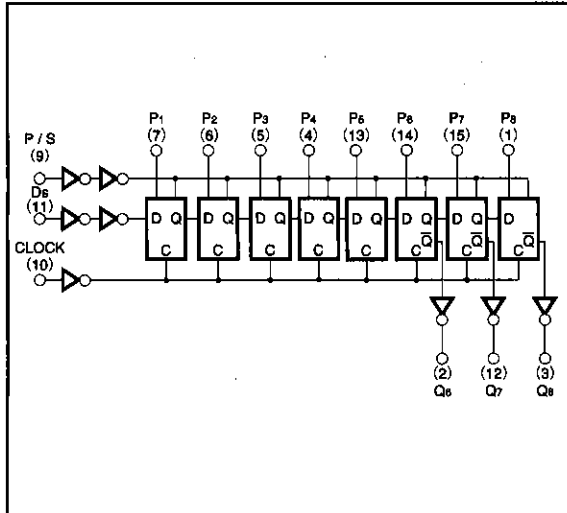


# 8-bit static shift register

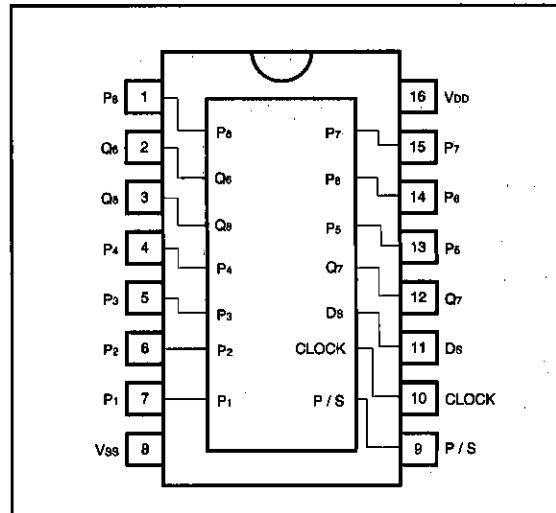
## BU4021B/BU4021BF

The BU4021B and BU4021BF are 8-bit static shift registers consisting of 8 register cells, each of which has parallel input. Control of the parallel/serial control input (P/S) enables serial input/serial output with clock synchronization, as well as parallel input/serial input conversions.

● Logic diagram



● Block diagram



● Truth table

Serial operation

t	CLOCK	Ds	P/S	Q <sub>6</sub> (t=n+6)	Q <sub>7</sub> (t=n+7)	Q <sub>8</sub> (t=n+8)
n	$\bar{f}$	L	L	0	?	?
n+1	$\bar{f}$	H	L	1	0	?
n+2	$\bar{f}$	L	L	0	1	0
n+3	$\bar{f}$	H	L	1	0	1
	$\bar{f}$	X	L	Q <sub>6</sub>	Q <sub>7</sub>	Q <sub>8</sub>

Parallel operation

CLOCK	Ds	P/S	D <sub>m</sub>	Q <sub>m</sub> *
$\bar{f}$	X	H	L	L
$\bar{f}$	X	H	H	H

X: Don't care  
\*: Q<sub>6</sub>, Q<sub>7</sub>, and Q<sub>8</sub> are external

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V <sub>DD</sub>	-0.3~18	V
Power dissipation	P <sub>d</sub>	1000 (DIP) , 500 (SOP)	mW
Operating temperature	T <sub>opr</sub>	-40~85	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C
Input voltage	V <sub>IN</sub>	-0.3~V <sub>DD</sub> +0.3	V

## ● Electrical characteristics

DC characteristics (unless otherwise noted,  $T_a=25^\circ\text{C}$ ,  $V_{SS}=0\text{V}$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit	V <sub>DD</sub> (V)	Conditions
"H" input voltage	V <sub>IH</sub>	3.5	—	—	V	5	—
		7.0	—	—		10	
		11.0	—	—		15	
"L" input voltage	V <sub>IL</sub>	—	—	1.5	V	5	—
		—	—	3.0		10	
		—	—	4.0		15	
"H" input current	I <sub>IH</sub>	—	—	0.3	μA	15	V <sub>IH</sub> =15V
"L" input current	I <sub>IL</sub>	—	—	-0.3	μA	15	V <sub>IL</sub> =0V
"H" output voltage	V <sub>OH</sub>	4.95	—	—	V	5	I <sub>o</sub> =0mA
		9.95	—	—		10	
		14.95	—	—		15	
"L" output voltage	V <sub>OL</sub>	—	—	0.05	V	5	I <sub>o</sub> =0mA
		—	—	0.05		10	
		—	—	0.05		15	
"H" output current	I <sub>OH</sub>	-0.16	—	—	mA	5	V <sub>OH</sub> =4.6V
		-0.4	—	—		10	V <sub>OH</sub> =9.5V
		-1.2	—	—		15	V <sub>OH</sub> =13.5V
"L" output current	I <sub>OL</sub>	0.44	—	—	mA	5	V <sub>OL</sub> =0.4V
		1.1	—	—		10	V <sub>OL</sub> =0.5V
		3.0	—	—		15	V <sub>OL</sub> =1.5V
Quiescent supply current	I <sub>DD</sub>	—	—	20	μA	5	V <sub>I</sub> =V <sub>DD</sub> , GND
		—	—	40		10	
		—	—	80		15	

## ●Electrical characteristics

Switching characteristics (unless otherwise noted, Ta=25°C, Cl=50pF)

Parameter	Symbol	Min.	Typ.	Max.	Unit	V <sub>DD</sub> (V)	Conditions	Measurement Circuit
Output rise time	t <sub>RLH</sub>	—	180	—	ns	5	—	Fig.1
		—	90	—		10		
		—	65	—		15		
Output fall time	t <sub>THL</sub>	—	100	—	ns	5	—	Fig.1
		—	50	—		10		
		—	40	—		15		
"L" to "H" propagation delay time CLOCK to Q, P/S to Q	t <sub>PLH</sub>	—	400	—	ns	5	—	Fig.1
		—	170	—		10		
		—	115	—		15		
"H" to "L" propagation delay time CLOCK to Q, P/S to Q	t <sub>PHL</sub>	—	400	—	ns	5	—	Fig.1
		—	170	—		10		
		—	115	—		15		
Setup time	t <sub>su</sub>	—	150	—	ns	5	—	Fig.1
		—	50	—		10		
		—	30	—		15		
Minimum clock pulse width	t <sub>w</sub> (CLK)	—	150	—	ns	5	—	Fig.1
		—	75	—		10		
		—	40	—		15		
Maximum clock frequency	f (CLK) Max.	—	3.0	—	MHz	5	—	Fig.1
		—	6.0	—		10		
		—	8.0	—		15		
Maximum clock rise/fall time	t <sub>r</sub> (CLK) t <sub>f</sub> (CLK)	—	—	15	μs	5	—	Fig.1
		—	—	5.0		10		
		—	—	4.0		15		
Minimum P/S control pulse width	t <sub>w</sub> (P/S)	—	150	—	ns	5	—	—
		—	75	—		10		
		—	40	—		15		
Input capacitance	C <sub>IN</sub>	—	5	—	pF	—	—	—

● Measurement circuit

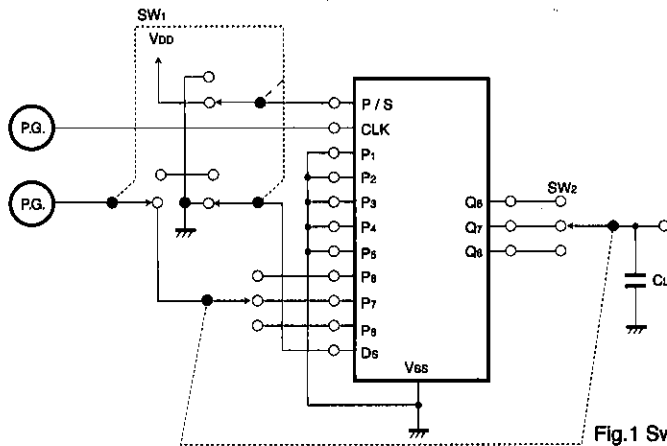


Fig.1 Switching characteristics measurement circuit

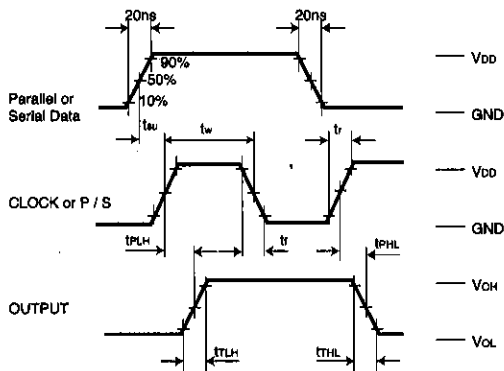


Fig.2 Switching characteristics waveform

● Electrical characteristic curve

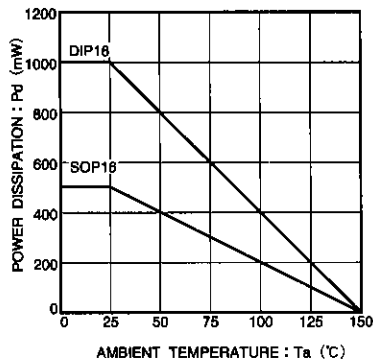
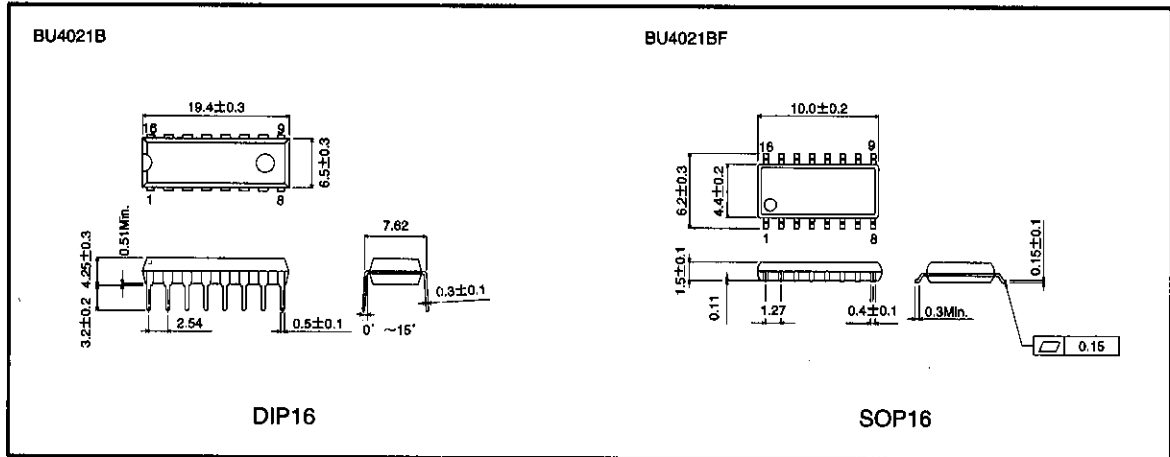


Fig.3 Power dissipation - ambient temperature characteristic

● External dimensions (Units: mm)



# Series Standard

## BU4000B

The BU4000 Series are CMOS ICs featuring low voltage and low power consumption. The wide range of operating power supply voltages is compatible with the general-purpose 4000B Series, and when a 5V power supply voltage is used, the LS-TTL IC can be driven directly.

These ICs are available in SOP and SSOP packages as well as the standard DIP package.

●Features

- 1) Low power consumption.
- 2) Wide range of operating power supply voltages.
- 3) High input impedance.
- 4) High fan-out.
- 5) Direct drive of 2 L-TTL inputs and 1 LS-TTL input.

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V <sub>DD</sub>	18 *1	V
Input voltage	V <sub>IN</sub>	-0.3~V <sub>DD</sub> +0.3	V
Power dissipation *2	P <sub>d</sub>	Please refer to specifications for individual package	mW
Storage temperature	T <sub>stg</sub>	-55~150	°C

\*1 For the BU4XXXBC type, V<sub>DD</sub> = 20 V.

\*2 The values for the SOP and SSOP packages are the values when mounted on a glass epoxy PCB (50 mm x 50 mm x 1.6 mm).

●Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V <sub>DD</sub>	3~16 *	V
Input voltage	V <sub>IN</sub>	0~V <sub>DD</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C

\* For the BU4XXXBC type, V<sub>DD</sub> = 3 to 18 V.

●Electrical characteristic curves

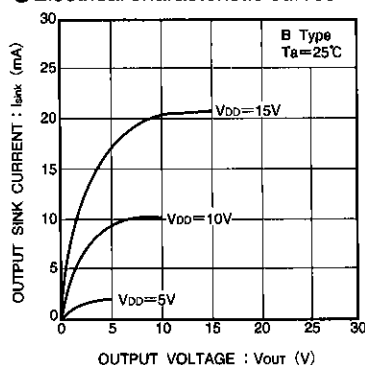


Fig.1 Output sink current - output voltage characteristic

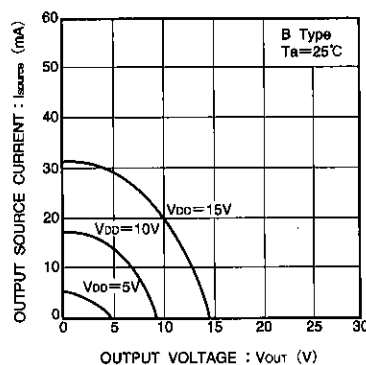


Fig.2 Output source current - output voltage characteristic

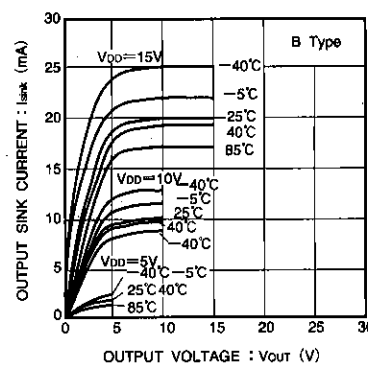


Fig.3 Output SINK current - output voltage characteristic

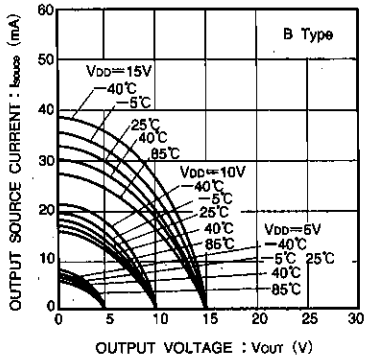


Fig.4 Output source current - output voltage characteristic

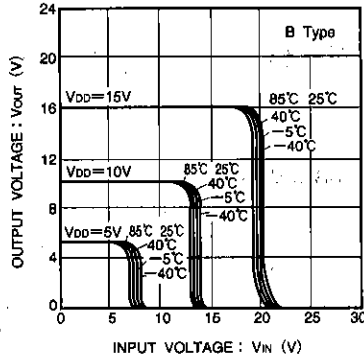


Fig.5 Output voltage - input voltage characteristic

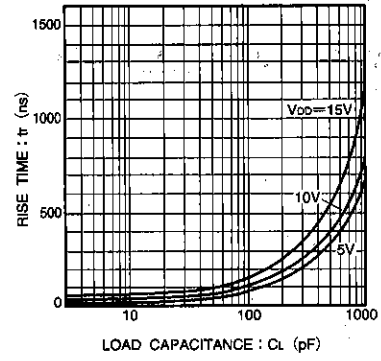


Fig.6 Rise time - load capacitance characteristic

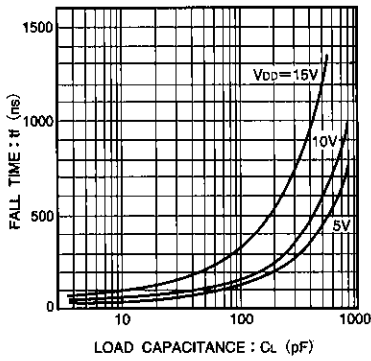


Fig.7 Fall time - load capacitance characteristic

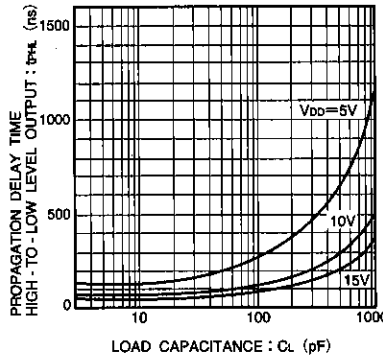


Fig.8 "H" to "L" propagation delay time - load capacitance characteristic

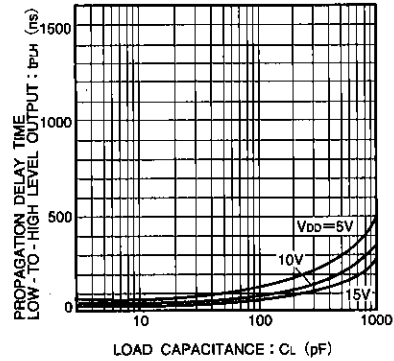


Fig.9 "L" to "H" propagation delay time - load capacitance characteristic

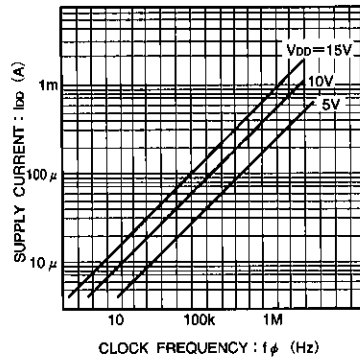


Fig.10 Supply current - clock frequency characteristic

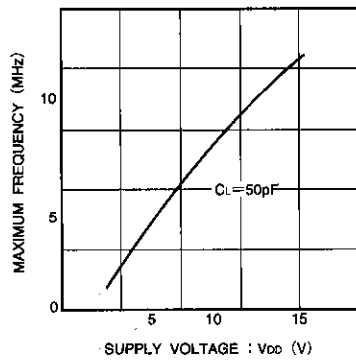


Fig.11 Maximum clock frequency - power supply voltage characteristic

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