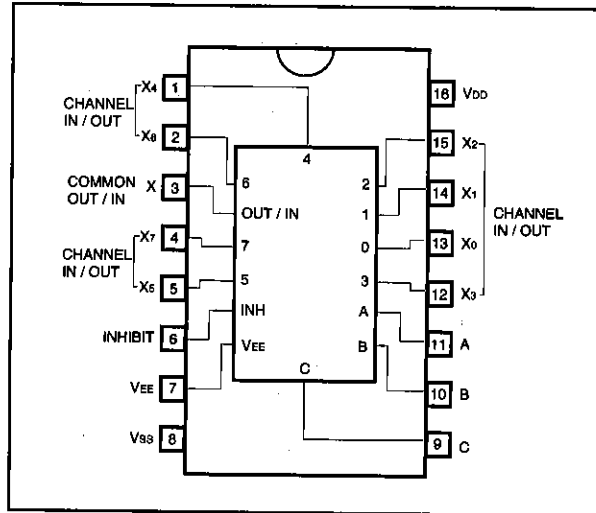


8-channel analog multiplexer/ demultiplexer

BU4051BC/BU4051BCF/BU4051BCFV

The BU4051BC, BU4051BCF and BU4051BCFV are analog multiplexers/demultiplexers which use three-input digital signals for control via an 8-channel analog switch. These products feature high on/off output voltage ratio and low crosstalk between analog switches.

●Block diagram

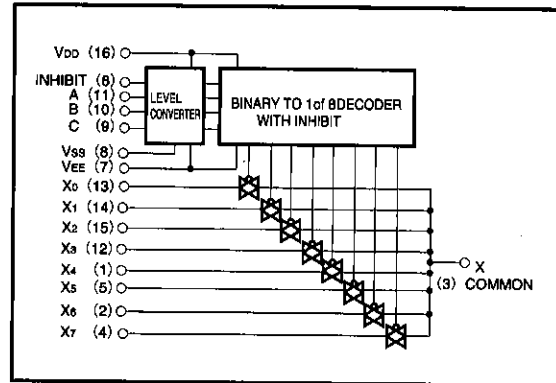


●Truth table

INHIBIT	A	B	C	ON SWITCH
L	L	L	L	X0
L	H	L	L	X1
L	L	H	L	X2
L	H	H	L	X3
L	L	L	H	X4
L	H	L	H	X5
L	L	H	H	X6
L	H	H	H	X7
H	X	X	X	NONE

X : Don't Care

●Logic diagram



BU4000B series

CMOS logic

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	$V_{DD}-V_{EE}$	-0.3~20	V
Power dissipation	Pd	1000 (DIP), 500 (SOP), 400 (SSOP)	mW
Operating temperature	Topr	-40~85	°C
Storage temperature	Tstg	-55~150	°C
Input voltage	V_{IN}	-0.3~ $V_{DD}+0.3$	V

●Electrical characteristics

DC characteristics (Ta=25°C, $V_{EE}=V_{SS}=0V$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	V _{DD} (V)	Conditions
High-level input voltage	V_{IH}	5.5	—	—	V	5	—
		7.0	—	—		10	
		11.0	—	—		15	
Low-level input voltage	V_{IL}	—	—	1.5	V	5	—
		—	—	3.0		10	
		—	—	4.0		15	
High-level input current	I_{IH}	—	—	0.3	μA	15	$V_{IH}=15V$
Low-level input current	I_{IL}	—	—	-0.3	μA	15	$V_{IL}=0V$
RON resistance	R_{ON}	—	—	950	Ω	5	—
		—	—	250		10	
		—	—	160		15	
RON resistance deflexion	ΔR_{ON}	—	25	—	Ω	5	—
		—	10	—		10	
		—	5	—		15	
Channel OFF leakage current	I_{OFF}	—	—	0.3	μA	15	—
		—	—	-0.3		15	
Quiescent supply current	I_{DD}	—	—	5	μA	5	$V_i=V_{DD}$ or GND
		—	—	10		10	
		—	—	15		15	

Switching characteristics (Ta=25°C, VEE=VSS=0V, RL=1kΩ, CL=50pF)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions		Measurement Circuit
						VDD (V)		
Propagation delay time CHANNEL IN→OUT	tPLH tPHL	—	15	45	ns	5	—	Fig.4
		—	8	20		10		
		—	6	15		15		
Propagation delay time CONT→OUT	tPHZ, tPLZ tPZH, tPZL	—	70	550	ns	5	—	Fig.5, 6
		—	90	240		10		
		—	70	160		15		
Propagation delay time INHIBIT→OUT	tPHZ, tPLZ tPZH, tPZL	—	170	450	ns	5	—	Fig.5, 6
		—	90	210		10		
		—	70	160		15		
Max. propagation frequency	fMax.	—	15	—	MHz	5	VEE=−5V*1	Fig.7
Feedthrough	FT	—	0.7	—	MHz	5	VEE=−5V*2	Fig.7
Sinewave distortion	D	—	0.02	—	%	5	VEE=−5V*3	Fig.7
Input capacitance (control)	Cc	—	5	—	pF	—	—	—
Input capacitance (switch)	Cs	—	10	—	pF	—	—	—

- *1 VIN=5VP-P Sinewave, frequency that enables 20 log10 VOUT/VIN=−3dB
- *2 VIN=5VP-P Sinewave, frequency that enables 20 log10 VOUT/VIN=−50dB at Channel off
- *3 VIN=5VP-P Sinewave

● Measurement circuits

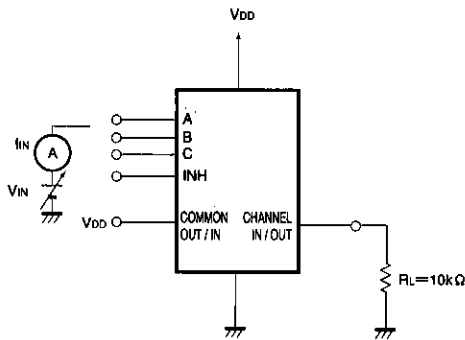


Fig. 1 Input voltage, current

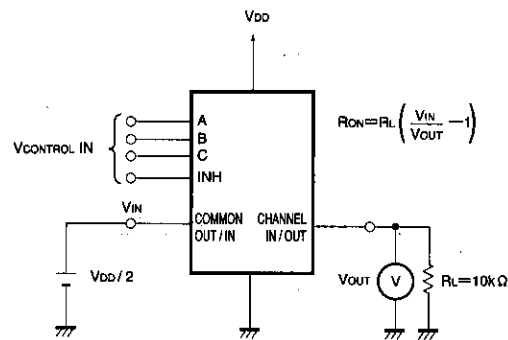


Fig. 2 ON resistance, ON resistance deviation

● Measurement circuits

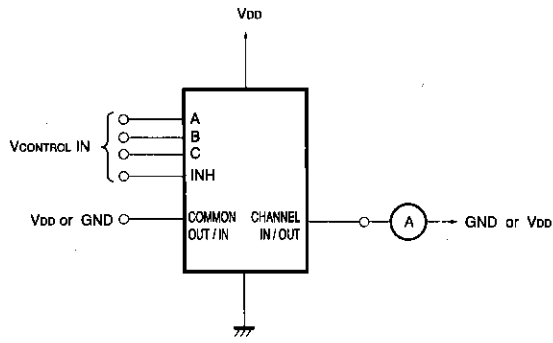


Fig. 3 OFF - channel leakage current

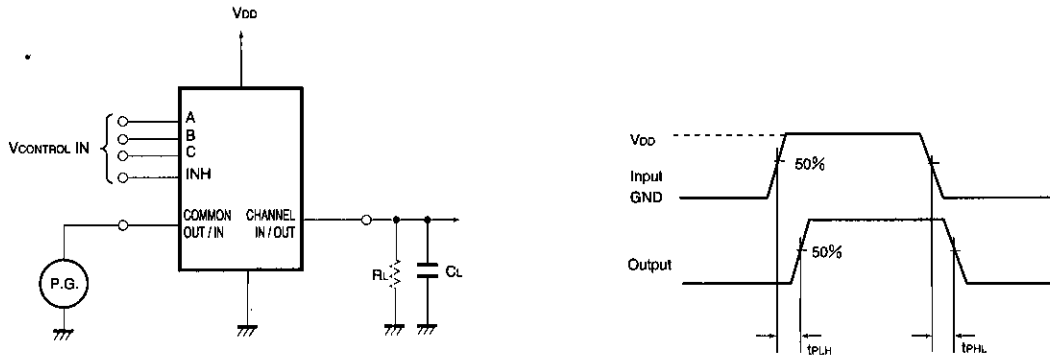


Fig. 4 Propagation delay time (Switch IN to OUT)

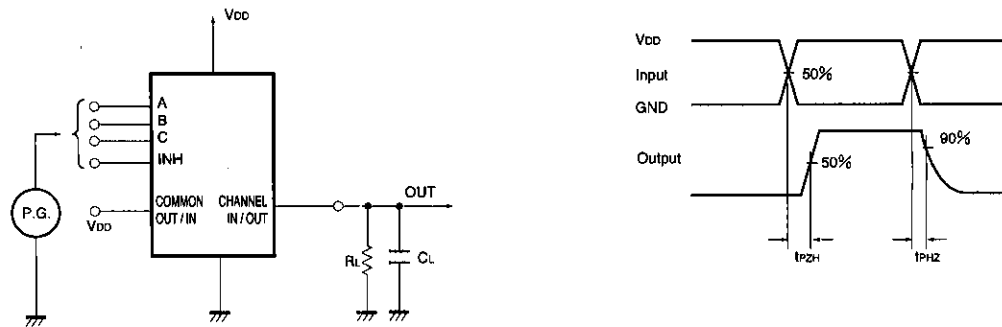


Fig. 5 Propagation delay time (CONT, INH to OUT)

● Measurement circuits

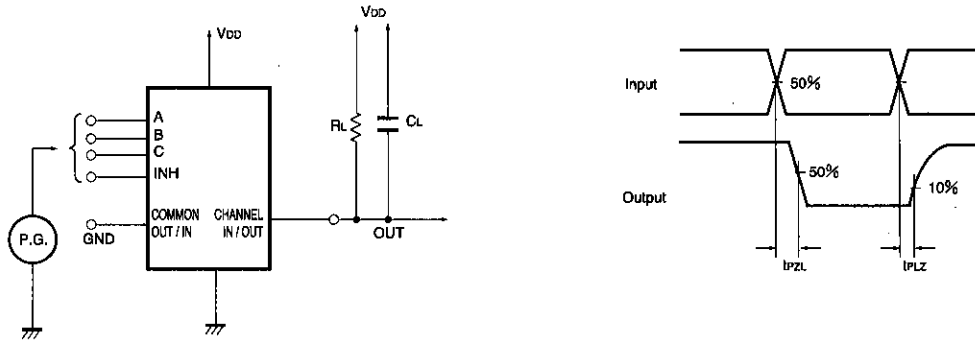


Fig. 6 Propagation delay time (CONT, INH to OUT)

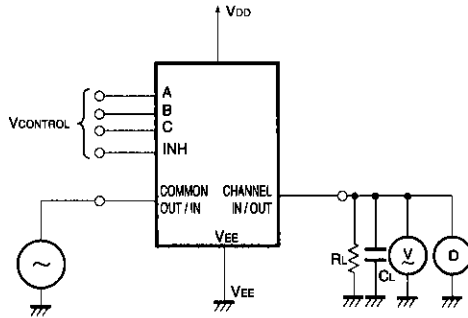


Fig. 7 Maximum propagation frequency, feedthrough attenuation, sinewave distortion

● Electrical characteristic curve

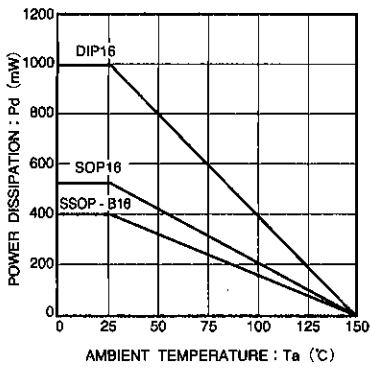
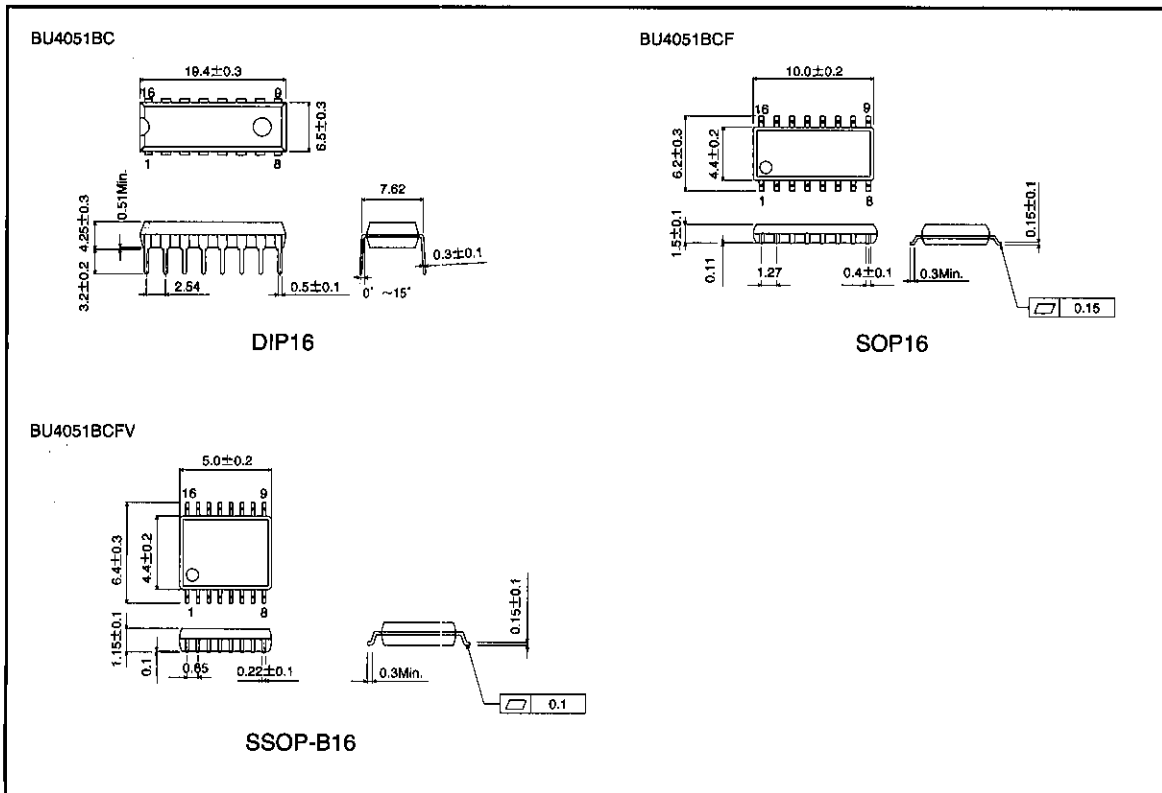


Fig. 8 Power dissipation - ambient temperature characteristic

●External dimensions (Units: mm)



Series Standard

BU4000B

The BU4000 Series are CMOS ICs featuring low voltage and low power consumption. The wide range of operating power supply voltages is compatible with the general-purpose 4000B Series, and when a 5V power supply voltage is used, the LS-TTL IC can be driven directly.

These ICs are available in SOP and SSOP packages as well as the standard DIP package.

●Features

- 1) Low power consumption.
- 2) Wide range of operating power supply voltages.
- 3) High input impedance.
- 4) High fan-out.
- 5) Direct drive of 2 L-TTL inputs and 1 LS-TTL input.

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V _{DD}	18 *1	V
Input voltage	V _{IN}	-0.3~V _{DD} +0.3	V
Power dissipation *2	P _d	Please refer to specifications for individual package	mW
Storage temperature	T _{stg}	-55~150	°C

*1 For the BU4XXXBC type, V_{DD} = 20 V.

*2 The values for the SOP and SSOP packages are the values when mounted on a glass epoxy PCB (50 mm x 50 mm x 1.6 mm).

●Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V _{DD}	3~16 *	V
Input voltage	V _{IN}	0~V _{DD}	V
Operating temperature	T _{opr}	-40~85	°C

* For the BU4XXXBC type, V_{DD} = 3 to 18 V.

●Electrical characteristic curves

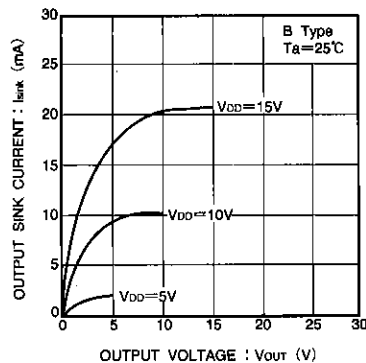


Fig.1 Output sink current - output voltage characteristic

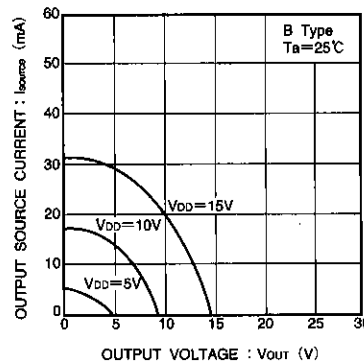


Fig.2 Output source current - output voltage characteristic

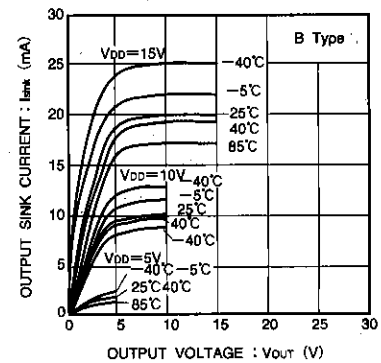


Fig.3 Output SINK current - output voltage characteristic

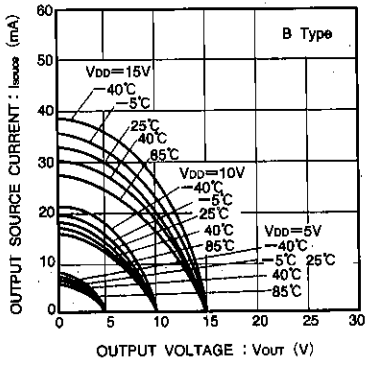


Fig.4 Output source current - output voltage characteristic

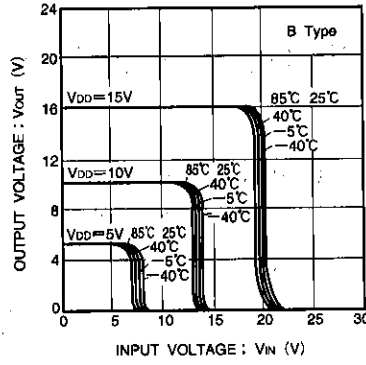


Fig.5 Output voltage - input voltage characteristic

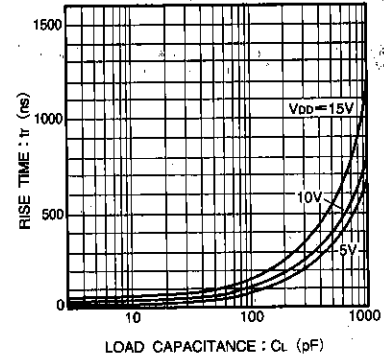


Fig.6 Rise time - load capacitance characteristic

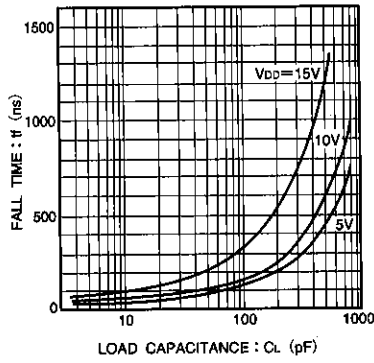


Fig.7 Fall time - load capacitance characteristic

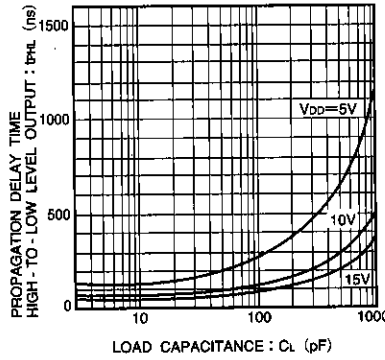


Fig.8 "H" to "L" propagation delay time - load capacitance characteristic

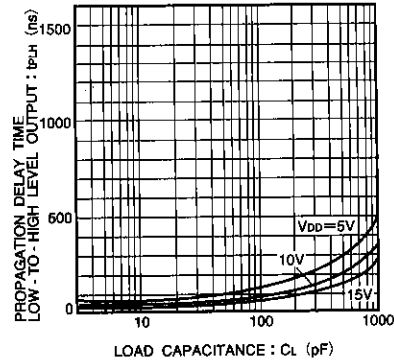


Fig.9 "L" to "H" propagation delay time - load capacitance characteristic

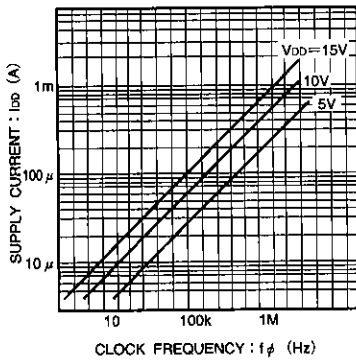


Fig.10 Supply current - clock frequency characteristic

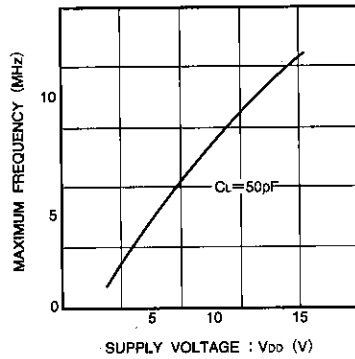


Fig.11 Maximum clock frequency - power supply voltage characteristic

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