

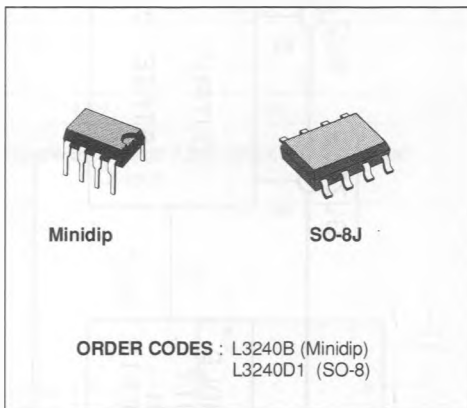
ELECTRONIC TWO-TONE RINGER

- LOW CURRENT CONSUMPTION, IN ORDER TO ALLOW THE PARALLEL OPERATION OF 4 DEVICES
- INTEGRATED RECTIFIER BRIDGE WITH ZENER DIODES TO PROTECT AGAINST OVER-VOLTAGES
- LITTLE EXTERNAL CIRCUITRY
- TONE AND SWITCHING FREQUENCIES ADJUSTABLE BY EXTERNAL COMPONENTS
- INTEGRATED VOLTAGE AND CURRENT HYSTERESIS
- BRIDGE OUTPUT CONFIGURATION

DESCRIPTION

L3240 is a monolithic integrated circuit designed to replace the mechanical bell in telephone sets, in connection with an electro acoustical converter. The device can drive either directly a piezo ceramic converter (buzzer) or a small loudspeaker. In this case a transformer is needed. The two tone frequencies generated are switched by an internal oscillator in a fast sequence and made audible across output amplifiers in the transducer ; both tone frequencies and the switching frequency can be externally adjusted.

The supply voltage is obtained from the AC ring si-

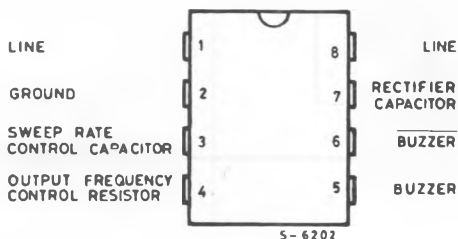


gnal and the circuit is designed so that noise on the line or variations of the ringing signal cannot affect the correct operation of the devices.

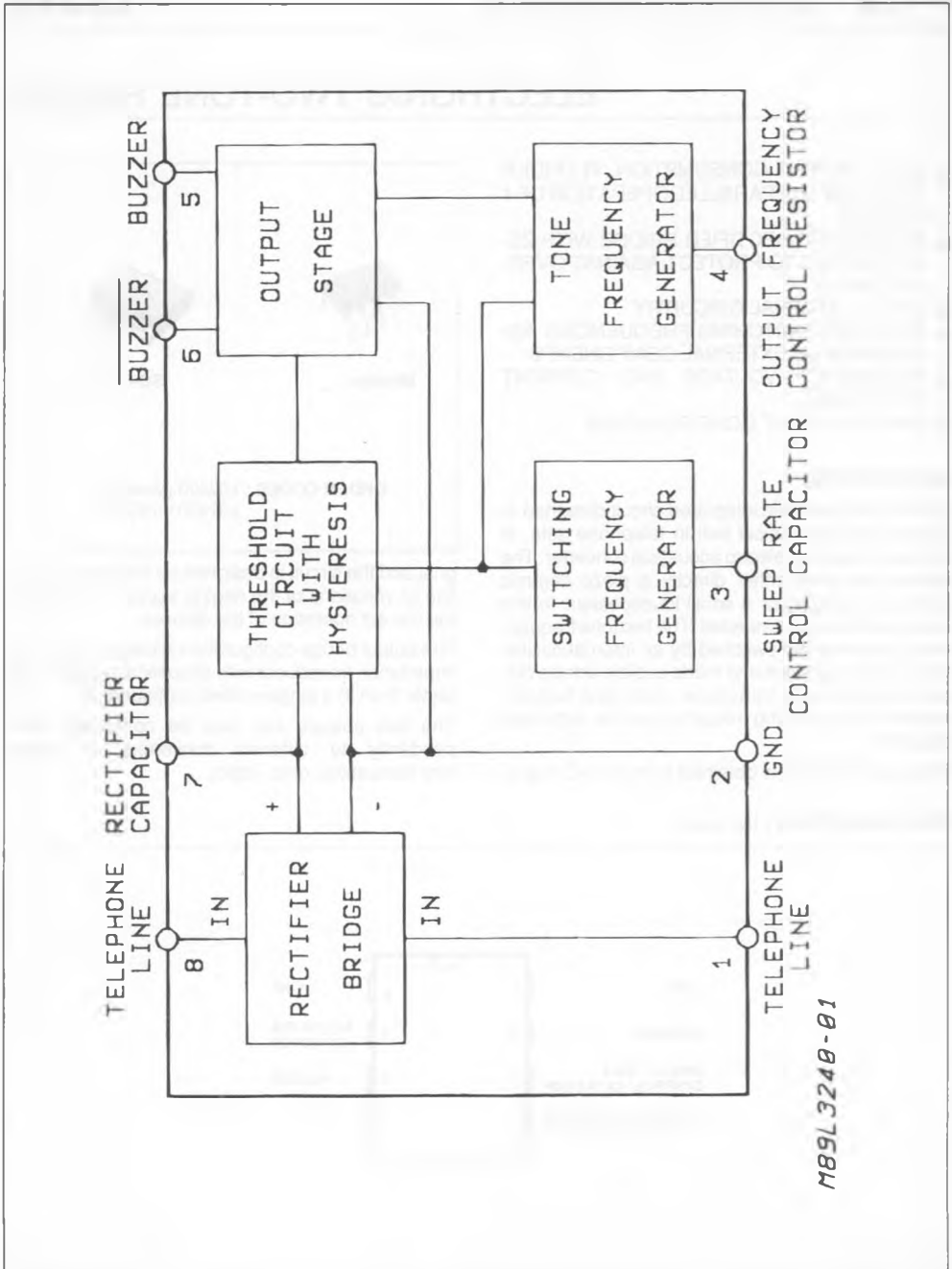
The output bridge configuration allows to use a high impedance transducer with acoustical results much better than in a single ended configuration.

The two outputs can also be connected independently to different converters or actuators (acoustical, opto, logic).

PIN CONNECTION (top view)



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{AB}	Calling Voltage (f = 50 Hz) Continuous	120	V _{rms}
V _{AB}	Calling Voltage (f = 50 Hz) 5s N/10s OFF	200	V _{rms}
DC	Supply Current	30	mA
T _{OP}	Operating Temperature	- 20 to + 70	°C
T _{stg}	Storage and Junction Temperature	- 65 to + 150	°C

THERMAL DATA

R _{th j-amb}	Thermal Resistance Junction-ambient	Max	100	°C/W
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Figure 1 : Test Circuit.

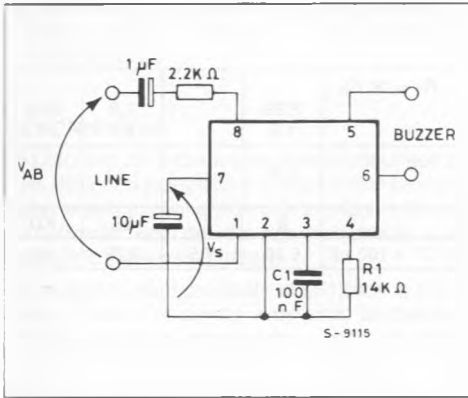
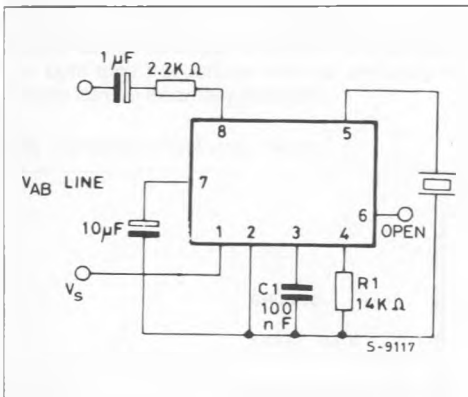


Figure 3 : Application Compatible with LS1240 (single ended output).



$$R_1 = \frac{3.56 \times 10^4}{F_1 \text{ (HZ)}} \times (1 - 0.16 \times \ln \frac{F_1}{2543})$$

$$f_2 = 0.725 f_1$$

$$f_{\text{SWEEP}} = \frac{750}{C_1 \text{ (nF)}}$$

Figure 2 : Typical Application with Balanced Output.

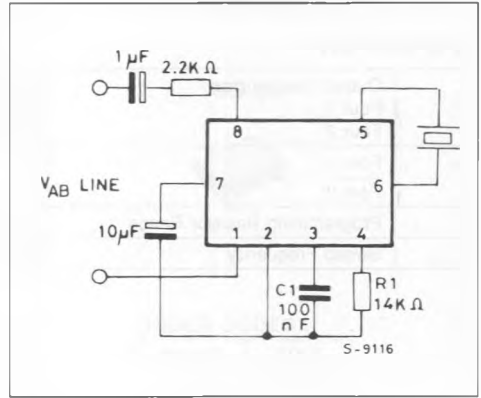
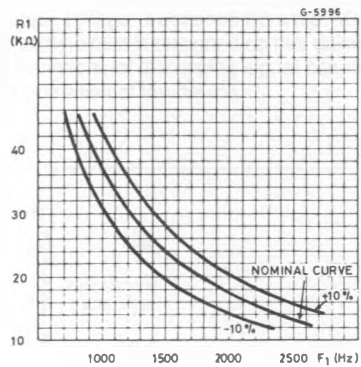


Figure 4 : F₁ Out vs. R₁.



ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$; $V_s =$ applied between pins 7-2 ; otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_s	Supply Voltage				26	V
I_B	Current Consumption Without Load (pins 8-1)	$V_s = 16.5$ to 29.5 V		1.5	1.8	mA
V_{ON}	Activation Voltage		12		13.5	V
V_{OFF}	Sustaining Voltage		7.8		9.3	V
R_D	Differential Resistance in OFF Condition (pins 8-1)		6.4			$K\Omega$
V_{OUT}	Output Voltage Swing			$V_s - 5$		V
I_{OUT}	Short Circuit Current (pins 5-6)	$V_s = 20$ V		35		mA
V_s	Voltage Drop Between Pins 8-1 and Pins 7-2			3		V

AC OPERATION

	Output Frequencies Fout 1 Fout 2	$V_s = 26$ V $R_1 = 14$ $K\Omega$ $V_s = 0$ V $V_s = 6$ V	2,29 1.6		2.8 2.1	KHz
	Fout 1 Fout 2		1.33		1.43	
	Programming Resistor Range		8		56	$K\Omega$
	Sweep Frequency	$R_1 = 14$ $K\Omega$ $C_1 = 100$ nF	5.25	7.5	9.75	Hz