Monolithic Linear IC LA6339M No.1156B SANYO High-Performance Quad Comparator

The LA6339M is a high-performance quad comparator that is capable of operating from a single power supply over a wide range of 2V to 36V. Because of its excellent input characteristics and low power, it can be very conveniently applied to multisignal parallel comparator circuits that require high-density assembly.

### Features

• Wide supply voltage range (Single supply: 2.0 to 36.0V,

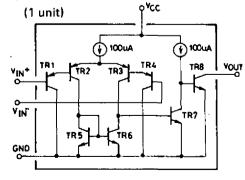
- dual supplies:  $\pm 1.0$  to  $\pm 18.0$ V)
- · Wide common-mode input voltage range (0 to V<sub>CC</sub>-1.5V)
- · Open collector output enabling wired OR
- · Small current dissipation (0.8mA/VCC=5V, RL=∞) and low power
- 'Mini flat package enabling compactness of sets

Maximum Ratings/Ta=25°C		•	unit
Maximum power supply voltage	VCC max	36	v
Differential input voltage	VID	36	v
Common-Mode input voltage range	VICM	-0.3~+36	v
Allowable power dissipation	P <sub>đ</sub> max	330	mW
Operating temperature	Topr	-30~+85	°C
Storage temperature	Τ <sub>stg</sub>	-55~+125	°C

# Operating Characteristics/T<sub>2</sub>=25°C, V<sub>CC</sub>=5V

perating characteristics 18-20 C, ACC-04		1 631					
			circuit	min	typ	max	unit
Input offset voltage	VIO		1		±2	±5	mν
Input offset current	10		2		±5	±50	nA
Input bias current	IB		3		25	250	nA
Common-mode input voltage range	VICM			0	VC	<u> </u>	V
Current dissipation	ICC	RL≓∞	4		0,8	2	mA
Voltage gain	'VG	RL=15kΩ	5		200		V/mV
Response time		V <sub>RL</sub> =5V, R <sub>L</sub> =5.1kΩ	6		1.3		μs
Output sink current	ISINK	V <sub>IN</sub> –=1V, V <sub>IN</sub> +=0V, Vo≦1.5V	7	6	16		mA
Output saturation voltage	VOL	V <sub>IN</sub> =1V, V <sub>IN</sub> +=0V, ¹ <sub>SINK</sub> ≦3mA	8		0.2	0.4	V
Output leak current	LEAK	V <sub>IN</sub> -=0V, V <sub>IN</sub> +=1V, V <sub>0</sub> =5V	9		0.1		nA

### **Equivalent Circuit**

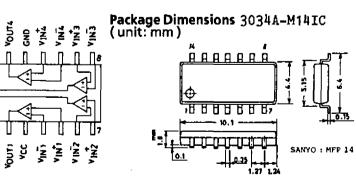




VOUT3

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Test

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### **Test Circuits**

1. Input offset voltage

2. Input offset current

₹<sub>R2</sub>

,<sup>v</sup>cc

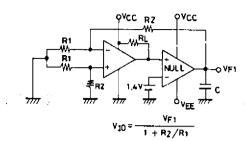
ovcc

VF2

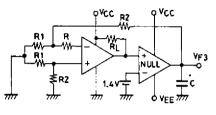
∍ <sub>۳€</sub> ک

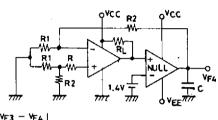
NHI

VF2 - VF1 R(1+R2/R1)

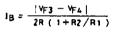


3. Input bias current

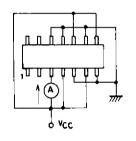


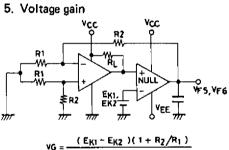


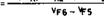
I<sub>10</sub> =



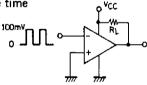
4. Current dissipation

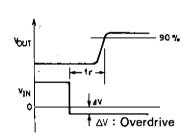






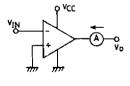
6. Response time

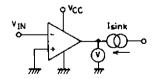




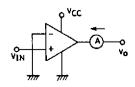
8. Output saturation voltage

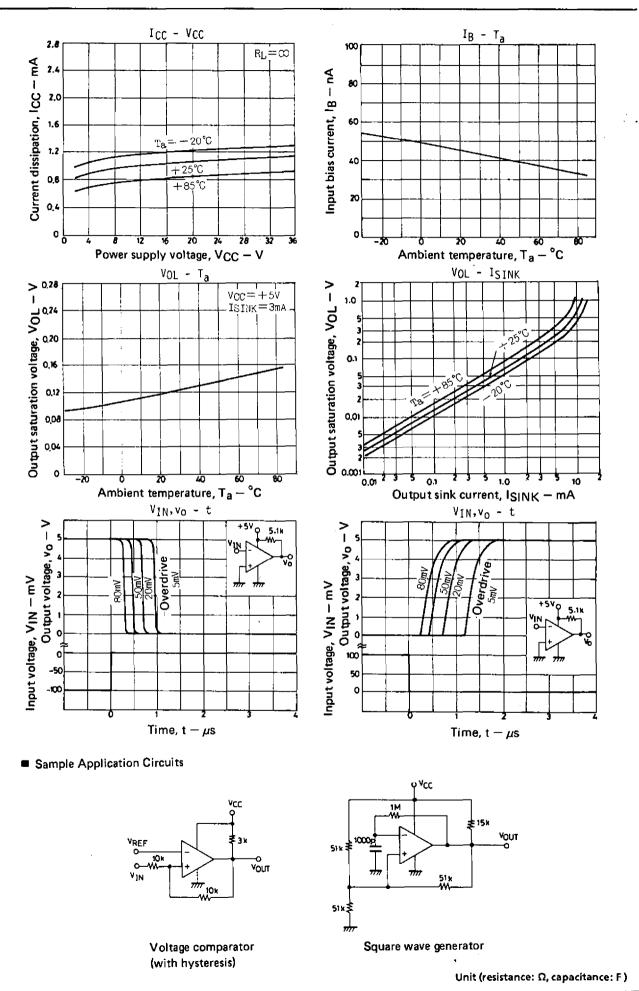
7. Output sink current





# 9. Output leak current





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