



# Low-Voltage/Low-Power Compander IC

### Overview

The LA8637M is a compander IC that was developed to improve audio quality in transceiver systems such as cordless telephones by expanding the dynamic range of the audio signal and suppressing noise. In addition to including both a compressor circuit that compresses with a compression ratio of 1/2 (logarithmic) and an expander with an expansion factor of 2 (logarithmic), the LA8637M also integrates the following functions on the same chip: an ALC preamplifier, a BTL amplifier, a data shaper for received data, a muting function and a standby function. Thus the LA8637M is optimal as the compander/system IC in cordless telephone products.

# **Applications**

• Cordless telephones

### **Functions**

Compressor

ALC preamplifier, preemphasis amplifier, limiter, transmission data input analog switch, filter buffer amplifier

- Expander
  - Filter buffer amplifier, de-emphasis amplifier, mute, BTL amplifier (100  $\Omega$  load)
- Level following data shaper (with hysteresis)
- · Standby mode

#### **Features**

- Easy implementation of transmission system and reception system base band signal processing
- Built-in BTL amplifier that supports mobile unit handsets
- · Standby function to support battery saving
- Low voltage operation:  $V_{CC\ OP} = 1.8$  to 6 V

### **Specifications**

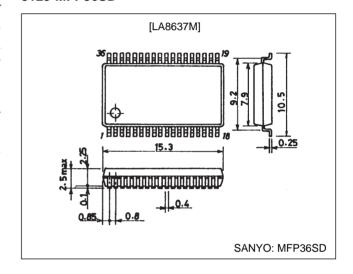
Maximum Ratings at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		7	V
Allowable power dissipation	Pd max	Ta ≤ 75°C	250	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

# **Package Dimensions**

unit: mm

#### 3129-MFP36SD



# LA8637M

# Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		3	V
Operating supply voltage	V <sub>CC OP</sub>		1.8 to 6	V

# Electrical Characteristics at $Ta=25^{\circ}C,\,V_{CC}$ = 3 $V,\,f$ = 1 kHz

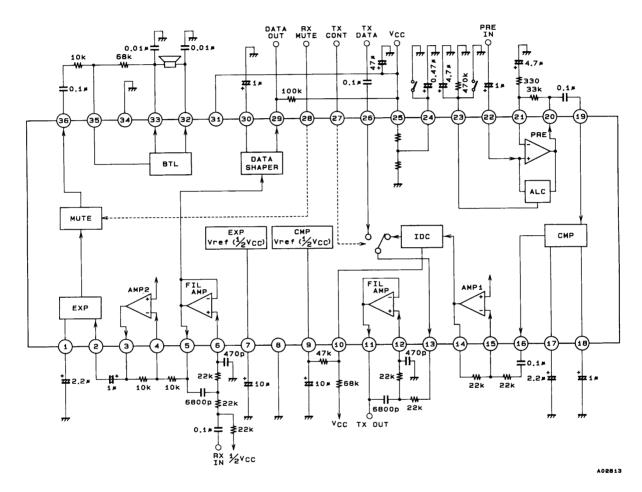
Parameter	Symbol	Conditions	min	typ	max	Unit	
Quiescent current	Icco	No signal	5	8	12	mA	
Standby current	I <sub>STBY</sub>	No signal, standby mode (pin 24: low)	0.8	1	1.2	mA	
[Preamplifier]							
Voltage gain	$V_{GP}$	Vi = -60 dBV	37	39	41	dB	
Maximum voltage gain	V <sub>GP</sub> max	Vi = -60 dBV		50		dB	
Total harmonic distortion	THD	Vi = -40 dBV, ALC: ON		0.3	1.0	%	
Input conversion noise voltage	V <sub>NI</sub>	$Rg = 0 \Omega$		1.5	5	μVrms	
ALC level	V <sub>ALC</sub>	Vi = -40 dBV, ALC: ON	350	420	490	mVrms	
ALC range	ALC	Until the THD from the ALC circuit becomes 1%	35	40		dB	
[Compressor] Vinrefc = -20 dBV =	0 dB, output:	pin 16				1	
Input impedance	rį			30		kΩ	
Output voltage	Voc	Vin = Vinrefc = 0 dB	-22	-20	-18	dBV	
Gain error (1)	Gec1	Vin = -20 dB	-0.5	0	+0.5	dB	
Gain error (2)	Gec2	Vin = -40 dB	-1.0	0	+1.0	dB	
Total harmonic distortion	THD	Vin = 0 dB		0.25	1.0	%	
Output noise voltage	V <sub>NOC</sub>	Rg = $620 \Omega$ , f = $20 \text{ Hz}$ to $20 \text{ kHz}$		0.15	1.0	mVrms	
Crosstalk	СТ <sub>С</sub>	RX-Vin = -20 dBV, 1 kHz BPF		-75	-60	dB	
[Analog Switch]						1	
Muting attenuation	ATT <sub>C</sub>	Vin = -20 dB, 1 kHz BPF	60	75		dB	
[Expander] Vinrefe = -20 dBV = 0	dB					1	
Output voltage	Voe	Vin = Vinrefe = 0 dB	-22	-20	-18	dBV	
Gain error (1)	Gee1	Vin = -20 dB	-1.0	0	+1.0	dB	
Gain error (2)	Gee2	Vin = -30 dB	-1.5	0	+1.5	dB	
Total harmonic distortion	THD	Vin = 0 dB		0.3	1.0	%	
Output noise voltage	V <sub>NO</sub> e	Rg = $620 \Omega$ , f = $20 \text{ Hz}$ to $20 \text{ kHz}$		13	80	μVrms	
Muting attenuation	A <sub>TT</sub> e	Vin = 0 dB, 1 kHz BPF	60	75		dB	
Crosstalk	CTe	PRE AMP-Vin = -60 dBV, 1 kHz BPF		-95	-80	dB	
Maximum output voltage	V <sub>O</sub> max	THD = 10%, $R_L$ = 10 kΩ	0.7	1.0		Vrms	
[Limiter]	•					•	
Limiting voltage	$V_{L}$	$\Delta V = 0.6 \text{ V}$ (voltage between pin 9 and pin 10)	0.27	0.3	0.33	Vp-p	
[BTL Amplifier] Gain = 30 dB	•					•	
Voltage Gain	V <sub>PWR</sub>	Vin = $-40$ dBV, R <sub>L</sub> = $100 \Omega$	27.5	29.5	31.5	dB	
Total harmonic distortion	THD	Vin = $-40$ dBV, R <sub>L</sub> = $100 \Omega$		0.5	1.0	%	
Maximum output power	P <sub>O</sub> max	THD = 10%, R <sub>L</sub> = 100 Ω	15	30		mW	
Maximum output voltage	V <sub>O</sub> max	THD = 10%, R <sub>L</sub> = 620 Ω	4.0	5.5		Vp-p	
Output noise voltage	V <sub>NO</sub>	$Rg = 0 \Omega$ , $R_L = 100 \Omega$		120	800	μVrms	
[Compressor Low-Pass Filter]	•					•	
Maximum output voltage	V <sub>O</sub> max	THD = 1%, $R_L$ = 10 kΩ	450	550		mVrms	
[Expander Low-Pass Filter] V <sub>B</sub> = 1	.5 V (V <sub>B</sub> : low-	pass filter bias voltage)				•	
Maximum output voltage	V <sub>O</sub> max	THD = 1%, $R_L$ = 10 $k\Omega$	400	500		mVrms	
[Data Shaper]							
Duty	D <sub>UTY</sub>	Vin = −15 dBV	45	50	55	%	
Hysteresis	W <sub>HYS</sub>		45	70	100	mV	
Output high level voltage	V <sub>OH</sub>	$R_L = 100 \text{ k}\Omega$	2.8			V	
Output low level voltage	V <sub>OL</sub>	$R_L = 100 \text{ k}\Omega$			0.3	V	
[Standby]							
Standby voltage	V <sub>ST</sub>	Pin 24			0.7	V	
Standby current	I <sub>ST</sub>	Pin 24 outflow current			30	μA	

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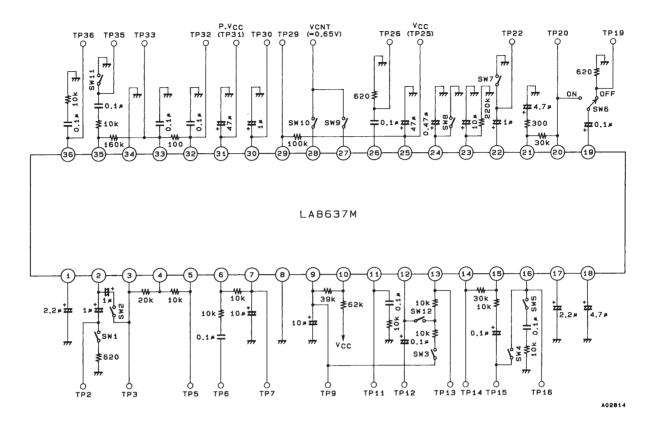
Parameter	Symbol	Conditions	min	typ	max	Unit
[Digital Input Characteristics]						
Input low level voltage	V <sub>IL</sub>	Pins 27 and 28			0.65	V
Input high level voltage	V <sub>IH</sub>	Pins 27 and 28	0.6 V <sub>CC</sub>			V
Input low level current	I <sub>IL</sub>	Pins 27 and 28, V <sub>I</sub> = 0.2 V			100	μA
Input high level current	I <sub>IH</sub>	Pins 27 and 28, $V_I = 2 V$			5	μA

# **Internal Equivalent Circuit Block Diagram**



Unit (resistance :  $\Omega$ , capacitance : F)

### **AC Test Circuit**



Unit (resistance :  $\Omega$ , capacitance : F)

### **Control Mode**

Pin No.	Symbol	State	Audio	Data
27	TX CONT	High	0	_
21	TA CONT	Low	_	0
28	RX MUTE	High	0	
20		Low	Mute	

# **Pin Functions**

Pin No.	Symbol	Internal equivalent circuit	Protection V <sub>CC</sub> side	ve diode Ground side
1 2	EXP.V <sub>REC</sub> EXP.IN	2 32k 0 VCC  Bi  E.VREF  M  10k 0  A02815	0	0
3 4	OP OUT1 OP IN1	E.VREF 20k0	0	0
5 6	FIL.OUT1 FIL.IN1	6 + 5 A02817	0	0
7 9 24	EXP.V <sub>REF</sub> CMP.V <sub>REF</sub> STAND-BY	VCC a x000 x y v v v v v v v v v v v v v v v v v v	0 0 0	0 0 0
10	IDC.ADJ	10 VCC 10 ₹20kΩ 10 C.VREF	0	0

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# LA8637M

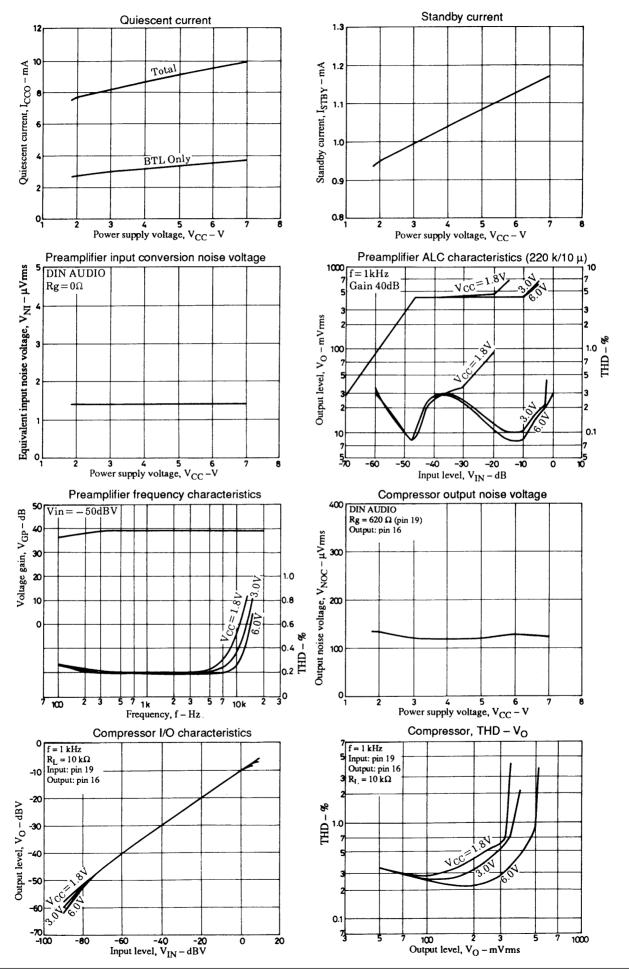
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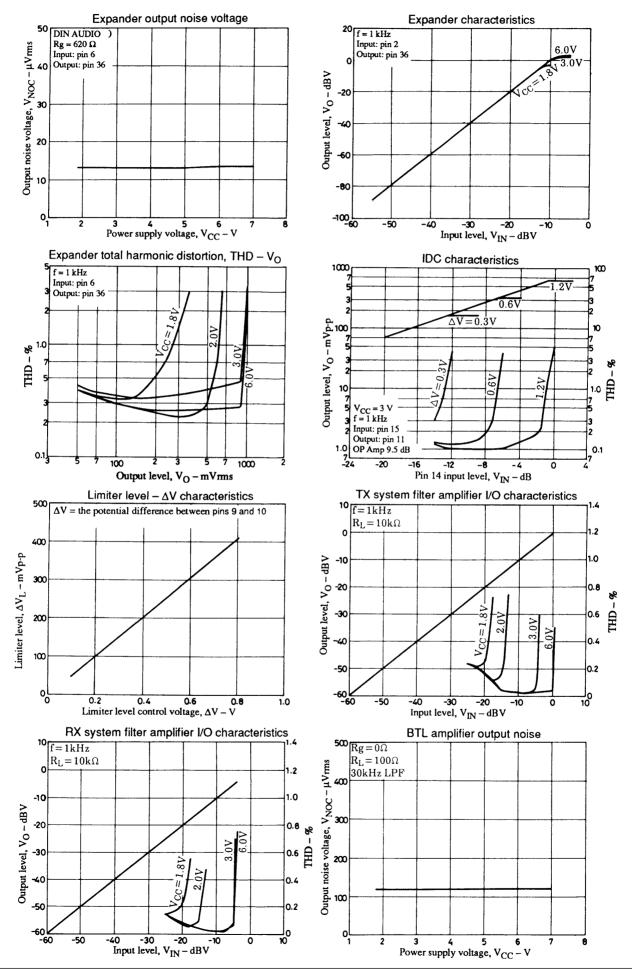
Pin No.	Symbol	Internal equivalent circuit		ve diode
	5,11501	mornal oquivalent encut	V <sub>CC</sub> side	Ground side
11 12	FIL.OUT2 FIL.IN2	12 + 11 A02820	0	0
13 26	TX.OUT DATA IN	C.VREF 50KQ 777	0	0
14 15	OP OUT2 OP IN2	C.VREF  20k0  A02822	0	0
16 17 18 19	CMP.OUT CMP.V <sub>REC</sub> CMP.NF CMP.IN	C.VREF  19 30ka 30ka 30ka  18  0 VCC  19 32ka  18  10ka  10ka  10ka  10ka	0 0 0	0 0 0
20 21 22	PRE OUT PRE NF PRE IN	22 W VCC 20 20 A02824	0 - -	0 0

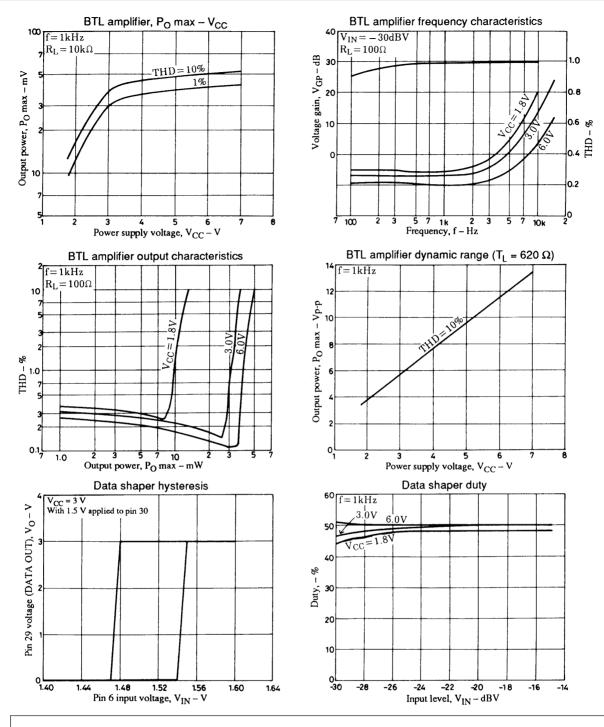
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Pin No.	Symbol	Internal equivalent circuit	Protecti		
	Symbol	mornal equivalent encult	V <sub>CC</sub> side	Ground side	
23	ALC.CT	23 A02825	0	0	
27 28	TX.CONT RX.MUTE	C.VREF ○ W 27	0	0	
29 30	DATA OUT V.HOLD	IN	-0	0	
32 33 35	BTL OUT2 BTL OUT1 BTL IN	E.VREF  10k0  10k0  10k0  P.VCC  33  E.VREF  A02828	- - -	0 0	
36	EXP.OUT	IN ₩ 20kΩ + 36  E.VREF 20kΩ A02829	0	0	

Note: All  $V_{CC}$  side diodes are connected to  $V_{CC}$  at pin 25.







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