### Ordering number : EN 1042F



The LB1211 series are general-purpose transistor arrays containing 7 channels (5 channels : LB1217 only). They are especially suited for driving LEDs, lamps, small-sized relays, etc. The transistors can be standardized.

# Features

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r eatures		N D 1 0 1 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0			
<ul> <li>Common-emitter 7 channels.</li> </ul>		LB1211,1212,1213,1214			
· Common-collector 7 channels.		LB1215,1216			
<ul> <li>Independent 5 channels</li> </ul>		LB1217			
· Built-in base current limiting	resistors.	LB1212,1213,1214,1216			
· Built-in Zener diodes for level	shift.	LB1212			
· Canable of being direct driven	with TTL. (	CMOS_PMOS_etc.			
• Wide operating voltage and te	mperature r	anges			
Absolute Maximum Ratings at	$Ta = 25^{\circ}C$				unit
Output Supply Voltage	VOUT	LB1212/13/14 only	-0.5 to -	+ 50	v
Collector to Emitter Voltage	VCEO	LB1211/15/16/17 only		35	v
Collector to Base Voltage	VCEO	LB1211/15/16/17 only		50	v
Output Current	Loren	BB1211,10,10,11 only		200	mÅ
Input Valtage	1001 Waa-1	LB1212/13/14 only	0.5 to -	+ 30	v
input voltage		ID1212 to 14 only	-0.5 to	± 15	v
	VINZ	LD1210 0my	-0.5 to -	05	··· A
Input Current	IIN	LB1211/15/17 only		20 800	
GND Pin Current	IGND			500	
Allowable Power Dissipation	Pd max			960	mw
Operating Temperature	Topr		-20 to -	+75	°C
Storage Temperature	Tstg		40 to +	150	°C
Electrical Characteristics at Ta	$=25^{\circ}C$		min	typ	max
Output Voltage	V <sub>OUT</sub> 1	$I_{IN} = 1 m A, I_{OUT} = 10 m A$			0.2
	V <sub>OUT</sub> 2	$I_{IN} = 2mA, I_{OUT} = 100mA$			0.8
		LB1212/13/14 only			~ ~
	V <sub>OUT</sub> 3	$I_{IN} = 3mA, I_{OUT} = 100mA$ LB1211/15/16/17 only			0.8
Output Leakage Current	IOFF	$V_{IN} = 0V, V_{OUT} = 25V$			10
Output Sustain Voltage	VOUT (sus)	) $I_{OUT} = 100 \text{mA}$	35		
DC Current Gain	$h_{FE}1$	$V_{OUT} = 10V, I_{OUT} = 10mA$	50		500
	~ ~~	LB1212/13/14 only			
	$h_{FE}2$	$V_{OUT} = 10V, I_{OUT} = 10mA$ LB1211/15/16/17 only	70		500
		DD1211/10/10/11 0my			



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SANYO Electric Co., Ltd. Semiconductor Business Headquarters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

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N010TS/4050YT/4078TA/7067KI/3295KI/7072KI,TS No.1042-1/4

LB1211,1212,1213,1214,1215;1216,1217							
Continued from preceding pa	ge.	······			_		
Input Voltage Turn-ON Time	V <sub>IN(on)</sub> l <sub>IN</sub> LB t <sub>ON</sub> Ref	=1mA,I <sub>OUT</sub> =10mA 1211/15/16/17 only er to Test Circuit.		min 0.4	typ 50	max	unit V ns
Turn-OFF Time	t <sub>OFF</sub> Ref	Refer to Test Circuit.					ns
Equivalent Circuit							
LB1211 OUT1		OUT4 OUT5 013 012 013 012 012 012 012 012 012 012 012 012 012	OUT6 011 011 011 011 011 011 011 011 011 01		8 —0 GN 9 pin : N	D	
		OUTA OUTS 013 012 012 012 012 012 012 012 012			8 	D	
		OUT4 933 912 12 12 12 12 12 12 12 12 12 12 12 12 1			8 	ND	
LB1214 OUTI 1 <i>6</i>	ουτ2 ουτ3 Υ 15 Υ 14	OUT4 OUTS የ13 የ12	оцте Р 11	0017 10			







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   Not impose any responsibility for any fault or negligence which may be cited in any such claim or
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# LB1211/1212/1213/1214/1215/1216/1217

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## No.1042-4/4