



# **Three-Phase Brushless Motor Driver**

## Overview

The LB1981V is a three-phase brushless motor driver especially suited for use mainly with drum motors of portable VCRs.

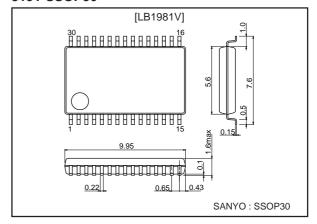
### **Features**

- Three-phase full-wave drive brushless sensorless motor drive
- · Soft switching drive
- Power-saving speed control function due to motor voltage
- Use with 3V power supply possible
- Output residual voltage can be set in 4 ways
- Forward/reverse switching possible
- Built-in standby function (FG and PG amplifier only remain operative)
- · Built-in braking circuit
- · Built-in thermal shutdown circuit
- Built-in FG and PG amplifiers
- Built-in saturation prevention amplifier and midpoint control circuit

# **Package Dimensions**

unit: mm

#### 3191-SSOP30



## **Specifications**

## Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	V <sub>CC</sub> 1 max		7	V
	V <sub>CC</sub> 2 max		12	V
	V <sub>S</sub> max		V <sub>CC</sub> 2	V
Applied output voltage	V <sub>O</sub> max		V <sub>S</sub> +2	V
Applied input voltage	V <sub>IN</sub> 1 max	Control circuits	-0.3 to V <sub>CC</sub> 1+0.3	V
	V <sub>IN</sub> 2 max	U, V, W, COM	V <sub>S</sub> +2	V
Output current	I <sub>O</sub> max		1.0	Α
Allowable power dissipation	Pd max		0.5	W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

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# Allowable Operating Ranges at Ta = 25°C

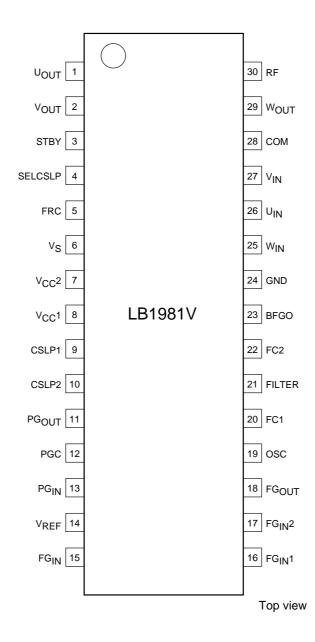
Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	V <sub>CC</sub> 1		2.7 to 6.0	V
	V <sub>CC</sub> 2		2.7 to 10.0	V
	V <sub>S</sub>		0 to V <sub>CC</sub> 2	V

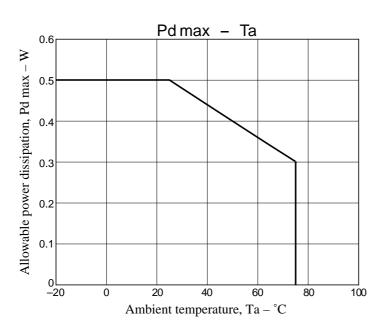
# Electrical Characteristics at Ta = 25°C, $V_{CC}1$ = 3V, $V_{CC}2$ = 4.75V, $V_S$ = 3V

Davassatas	C. mah al	Conditions		Ratings		
Parameter	Symbol	Conditions	min	typ	max	Unit
Power supply current	I <sub>CC</sub> 1	I <sub>O</sub> = 76 mA		5	7.5	mA
	I <sub>CC</sub> 2	I <sub>O</sub> = 76 mA		1.2	2.5	mA
Output idle current	I <sub>CC</sub> 10Q	V <sub>STBY</sub> = 0V		1.0	1.5	mA
	I <sub>CC</sub> 20Q	V <sub>STBY</sub> = 0V			10	μΑ
	I <sub>S30</sub> Q	V <sub>STBY</sub> = 0V		60	120	μΑ
Output saturation voltage, upper side 1	V <sub>OU</sub> 1	I <sub>O</sub> = 0.1A		0.2	0.5	V
Output saturation voltage, lower side 1	V <sub>OD</sub> 1	I <sub>O</sub> = 0.1A		0.2	0.5	V
Output saturation voltage, upper side 2	V <sub>OU</sub> 2	I <sub>O</sub> = 0.4A		0.4	0.8	V
Output saturation voltage, lower side 2	V <sub>OD</sub> 2	I <sub>O</sub> = 0.4A		0.4	0.8	V
COM pin common mode input voltage	V <sub>IC</sub>		0.3		V <sub>CC</sub> 2-0.9	V
range						
Standby pin High level voltage	V <sub>STBYH</sub>		2		V <sub>CC</sub> 1	V
Standby pin Low level voltage	V <sub>STBYL</sub>		-0.2		+0.7	V
Standby pin input current	I <sub>STBYH</sub>	V <sub>STBY</sub> = 3V			50	μΑ
Standby pin leakage current	I <sub>STBYL</sub>	V <sub>STBY</sub> = 0V	-10			μΑ
FRC pin High level voltage	V <sub>FRCH</sub>	SID!	2		V <sub>CC</sub> 1	V
FRC pin Low level voltage	VFRCL		-0.2		+0.7	V
FRC pin input current		V <sub>FRC</sub> = 3V	0.2		50	μΑ
FRC pin leakage current	I <sub>FRCI</sub>	V <sub>FRC</sub> = 0V	-10		00	μΑ
Slope pin source current ratio	I <sub>FRCL</sub>	I <sub>CSLP</sub> 1 source/I <sub>CSLP</sub> 2 source	-12		+12	- μΛ %
Slope pin sink current ratio	R <sub>SOURCE</sub>	I <sub>CSLP</sub> 1 sink/I <sub>CSLP</sub> 2 sink	-12		+12	%
CSLP1 source - sink current ratio	R <sub>SINK</sub>	I <sub>CSLP</sub> 1 sink1 <sub>CSLP</sub> 2 sink	-35		+15	%
CSLP2 sink - source current ratio	R <sub>CSLP</sub> 1		-35 -35		+15	%
Startup frequency	R <sub>CSLP</sub> 2 Freq	I <sub>CSLP</sub> 2 source/I <sub>CSLP</sub> 2 sink	-33	11.5	+13	Hz
	Dwidth	C <sub>OSC</sub> = 0.1 μF, OSC frequency *1		30		
Phase delay width		*1	450		040	deg °C
Thermal shutdown operating	T <sub>TSD</sub>	1	150	180	210	C
temperature	A.T.	*1		4.5		00
Thermal shutdown hysteresis	ΔT <sub>TSD</sub>	"1		15		°C
SELCSLP pin High level voltage	V <sub>SEL</sub> H		2		V <sub>CC</sub>	V
SELCSLP pin Low level voltage	V <sub>SEL</sub> L		-0.2		+0.7	V
SELCSLP input current	I <sub>SEL</sub> H	V <sub>SELCSLP</sub> = 3V			50	μΑ
SELCSLP pin leakage current	I <sub>SEL</sub> L	V <sub>SELCSLP</sub> = 0V	-10			μΑ
[FG amplifier]						
Input offset voltage	V <sub>IO</sub>	*1		±1	±5	mV
Input bias current	I <sub>BIN</sub> -				250	nA
Common mode input voltage range	V <sub>ICOM</sub>	*1	1		2	V
Open loop gain	G <sub>VFG</sub>	f = 1 kHz *1		55		dB
Input ON voltage	V <sub>OL</sub>	$I_O = 10 \mu\text{A}$			0.4	V
Input OFF voltage	V <sub>OH</sub>	I <sub>O</sub> = 10 μA	V <sub>CC</sub> 1-0.5			V
Schmitt amplifier hysteresis width	V <sub>SHIS</sub>	*1		20		mV
Output duty	Duty	f = 720 Hz, V <sub>IN</sub> = 20 mVp-p	30		70	%
Reference voltage	V <sub>REF</sub>		1.15	1.30	1.45	V
[PG amplifier]						
Input offset voltage	V <sub>IO</sub>	*1		±1	±5	mV
Input bias voltage	I <sub>BIN</sub> -	*1			250	nA
Common mode input voltage range	V <sub>ICOM</sub>	*1	1		2	V
Open loop gain	G <sub>VPG</sub>	f = 1 kHz *1		55		dB
Output ON voltage	V <sub>OL</sub>	I <sub>O</sub> = 10 μA			0.4	V
Output OFF voltage	V <sub>OH</sub>	I <sub>O</sub> = 10 μA	V <sub>CC</sub> 1-0.5			V
Schmitt amplifier hysteresis width	V <sub>SHIS</sub>	*1	- 30	50		mV

Note: Items shown to be "\*1" are not measured.

# **Pin Assignment**





A10857

# **Pin Descriptions**

Pin Desc Pin number		Pin voltage	Equivalent circuit	Pin function
3	STBY	V <sub>CC</sub> 1 max -0.2V min	V <sub>CC</sub> 1	When this pin is at 0.7V or less or when it is open, only the FG/PG amplifier operates. In the motor drive state, the pin should be at 2V or higher.
			3 100kΩ\$ A10858	pin should be at 2 v or higher.
4	SEL CSLP	V <sub>CC</sub> 1 max –0.2V min	VCC1  100kΩ  100kΩ  A10859	CSLP pin charge/discharge current switching pin. Setting this pin to 2V or higher switches the triangular wave slope that determines soft switching.
5	FRC	V <sub>CC</sub> 1 max -0.2V min	50kΩ 20μA 50kΩ A10860	Motor forward/reverse switching pin. Low: reverse (-0.2V to +0.7V or open)  High: forward (2V to V <sub>CC</sub> 1)
6	V <sub>S</sub>	0V to V <sub>CC</sub> 2		Power supply pin for determining output amplitude by supplying motor voltage. Must be lower than V <sub>CC</sub> 2 voltage.
7	V <sub>CC</sub> 2	2.7V to 10V		Power supply pin for supplying source side predriver voltage and coil waveform detect comparator voltage.
8	V <sub>CC</sub> 1	2.7V to 6V		Power supply pin for circuits except motor voltage, source side predriver voltage, and coil waveform detect comparator voltage.
9	CLSP1		Vcc1	Pins for connecting triangular
10	CLSP2		8μA Ψ 4μA Ψ 20μA	wave oscillator capacitor. This triangular wave coil output performs waveform soft switching.
			(9 10) A10861	Continued on payt page

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Continued from Pin number		g page Pin voltage	Equivalent circuit	Pin function
11	PG <sub>OUT</sub>	· iii vollago	V <sub>CC</sub> 1 30μA \$30kΩ (11)	PG amplifier output pin.
			A10862	
12	PGC		10μA 10μA 10μA 200Ω A10863	PG amplifier peak hold capacitor connection pin.
13	PGIN	2.0V max 1.0V min (V <sub>CC</sub> = 3V)	13 500Ω VCC1 500Ω VCC1 1.3V A10864	PG amplifier input pin. Connect PG coil between this pin and V <sub>REF</sub> .
14	V <sub>REF</sub>		V <sub>CC</sub> 1 14 35kΩ 70kΩ  A10865	Internal 1.3V reference voltage. Used as reference voltage for FG and PG amplifiers.
15	FG <sub>IN</sub>	2.0V max  1.0V min $(V_{CC}1 = 3V)$	V <sub>CC</sub> 1 6µA	FG amplifier input pin. Connect FG coil between this pin and V <sub>REF</sub> .
16	FG <sub>IN</sub> 1		10kΩ 200Ω 9kΩ 1kΩ W W W W W W W W W W W W W W W W W W W	FG amplifier input signal noise filter capacitor connection pin.
17	FG <sub>IN</sub> 2		77 (16 (15) A10866	FG amplifier input signal noise filter capacitor pin.

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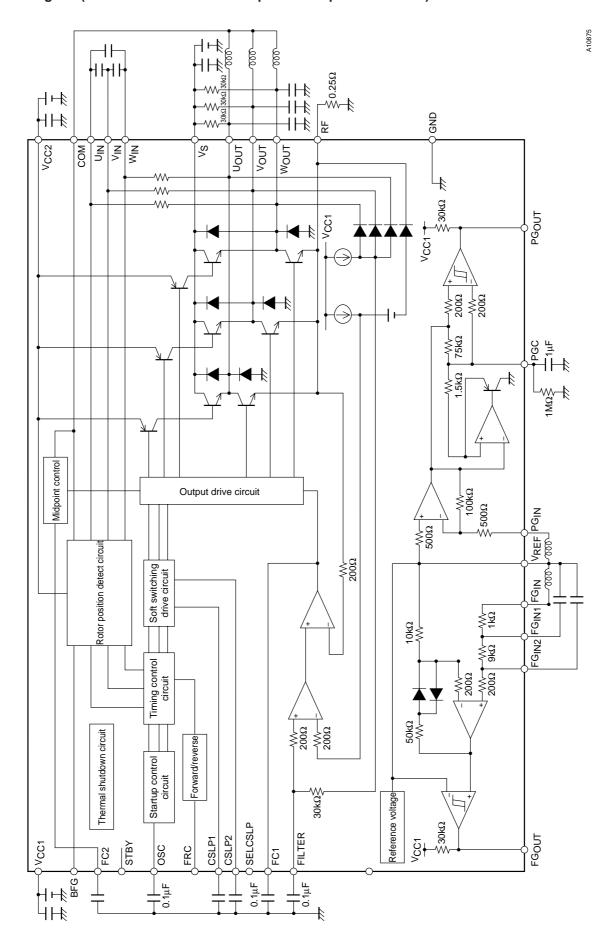
Pin number	Pin name	Pin voltage	Equivalent circuit	Pin function
18	PG <sub>OUT</sub>	-	VCC <sup>1</sup> 30μA 30kΩ  18  A10867	FG amplifier output pin.
19	OSC		5μA (V) 2.5μA (V) 10μA (V) 10	Pin for connecting triangular wave oscillator capacitor. Serves for forced startup waveform generation.
20	FC1		V <sub>CC</sub> 1  2kΩ \$  2kΩ \$  10kΩ \$  A10869	Frequency characteristics pin. Connecting a capacitor between this pin and ground serves to prevent closed-loop oscillation in the current control circuitry. (Lower-side output transistor drive current)
22	FC2		VCC1 (22) FC2 A10870	Frequency characteristics pin. Connecting a capacitor between this pin and ground serves to prevent closed-loop oscillation in the current control circuitry. (Lower-side output transistor drive current)
21	FILTER		V <sub>CC</sub> 1 25μΑ 1kΩ 1kΩ 1kΩ 22 29 1 2 21 A10871	Connecting a capacitor between this pin and ground activates the coil output saturation prevention function. In this condition, the VS pin is controlled for motor voltage control.  By adjusting the external capacitor, torque ripple compensation can be varied.

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Pin number		Pin voltage	Equivalent circuit	Pin function
23	BFG0		V <sub>CC</sub> 1 50μA √ 50μA ≥ 30kΩ 23 A10872	Motor counterelectromotive voltage FG pulse pin. Outputs a pulse using W phase counterelectromotive voltage as FG. Connect to ground if not used.
24	GND			Ground for all circuits except output.
25	W <sub>IN</sub>			Coil waveform detect comparator input pins.
26	U <sub>IN</sub>		V <sub>CC</sub> 2 (ψ) 10μA	harman harbara
27	V <sub>IN</sub>		$\begin{array}{c c} 25 \\ 26 \\ 27 \\ \hline \\ 2k\Omega \end{array}$	
28	СОМ		A10873	Motor coil midpoint input pin. Using this voltage as a reference, the coil voltage waveform is detected.
29	W <sub>OUT</sub>			W phase coil output pin.
1	U <sub>OUT</sub>		V <sub>S</sub>	U phase coil output pin.
2	V <sub>OUT</sub>		3.9Ω (29) 1 (2) (29) 1 (2) (10) (10) (10) (10) (10) (10) (10) (10	V phase coil output pin.
30	RF		30 A10874	Output transistor ground. Constant current drive is performed by detecting the voltage at this pin.

# Block Diagram (Constants for external components depend on motor.)



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