

LCD Dot Matrix Common Driver for STN Displays

Preliminary

Overview

The LC4131C is a common driver for large-scale dot matrix LCD panels. It includes a 120-bit bidirectional shift register and 4-level LCD driver circuits. The number of bits can be further increased by using the provided input and output pins to connect multiple LC4131Cs in cascade. The LC4131C and LC4104C form a large-screen LCD panel driver chip set.

Features

- Fabricated in a CMOS (P-sub) high-voltage process.
- LCD drive voltage: 36 V
- Logic system power-supply voltage: 2.7 to 5.5 V
- fcp max: 2.5 MHz
- Slim chip (output pads are concentrated on one of the longer sides)
- Bidirectional shift register
- The shift register can be split into two 60-bit registers. (Two screens drivable)
- DISPOFF function that locks the drive voltages output to the LCD at fixed levels.
- Display duty: 1/160 to 1/480
- Appropriate for COG (chip on glass) mounting. (A gold bump structure is adopted in the pad areas.)

Specifications

The electrical characteristics values shown below are for devices encapsulated in the Sanyo standard PGA-208 package.

Absolute Maximum Ratings at $V_{SS} = 0 \text{ V}$

Doromotor	Cumbal	Cumbal Applicable nine		Ratings				
Parameter	Symbol	Applicable pins	min	typ	max	Unit		
	V _{DD} max	V_{DD}	-0.3		7.0	V		
Supply voltage	V _{EE} max	V _{EE}	-0.3		40.0	V		
	V _{SSH} max	V _{SSH}	-0.3		0.3	V		
	V _{IN}	*1	-0.3		V _{DD} + 0.3	V		
Input voltage	V0, V1	V0, V1 *2	V _{EE} - 7.0		V _{EE} + 0.3	V		
Input voltage	V4	V4 *2	-0.3		V _{SS} + 7.0	V		
	V5	V5 *2	-0.3		+0.3	V		
Operating temperature	Topr		-20		+75	°C		
Storage temperature	Tstg		-55		+125	°C		

Note: 1. LOAD, RS/LS, DISP, DF, DIO1, DIO120, DMIN and MODE

Allowable Operating Ranges at $V_{SS} = 0 \text{ V}$, $Ta = -20 \text{ to } +75^{\circ}\text{C}$

Parameter	Symbol	Applicable pine		Unit			
Farameter	Symbol	Applicable pins	min	typ	max	Offic	
	V_{DD}	V _{DD}	2.7		5.5	V	
Supply voltage	V _{EE}	VEE	14		36	V	
	V _{SSH}	V _{SSH}		0		V	
Input high-level voltage	V _{IH}	*1	$0.8 \times V_{DD}$		V_{DD}	V	
Input low-level voltage	V _{IL}	*1	0		$0.2 \times V_{DD}$	V	
	V0, V1	V0, V1 *2	V _{EE} - 7.0		V _{EE}	V	
Input voltage	V4	V4 *2	0		V _{SSH} + 7.0	V	
	V5	V5 *2		0		V	

Note: 1. LOAD, RS/LS, DISP, DF, DIO1, DIO120, DMIN and MODE

When turning on the power supplies, first turn on the logic system power supply and then turn on the high-voltage system power supply; alternatively, turn both on at the same time.

When turning off the power supplies, first turn off the high-voltage system power supply and then turn off the logic system power supply; alternatively, turn both off at the same time.

^{2.} The voltages V0, V1, V4, and V5 must obey the relationships $V_{EE} \ge V0 \ge V1 \ge V_{EE} - 7 \text{ V}$, and $7 \text{ V} \ge V4 \ge V5 \ge V_{SSH}$.

^{2.} The voltages V0, V1, V4, and V5 must obey the relationships $V_{EE} \ge V0 \ge V1 \ge V_{EE} - 7$ V, and 7 V $\ge V4 \ge V5 \ge V_{SSH}$.

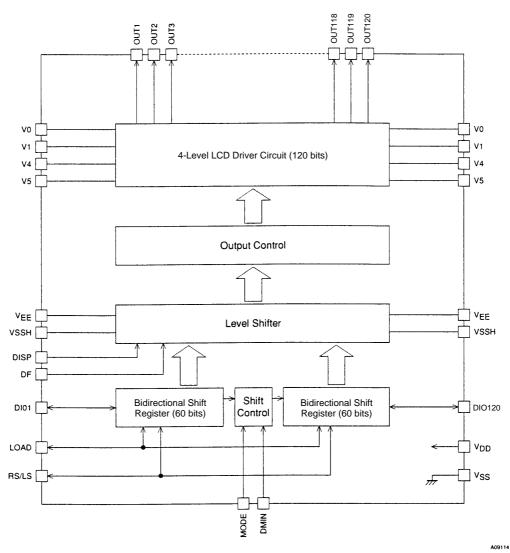
Electrical Characteristics at Ta = -20 to +75°C, V_{DD} = 2.7 to 5.5 V, V_{SS} = 0 V

Parameter	Cumahad	Conditions		Unit		
Parameter	Symbol	Conditions	min	typ	max	Offic
Input high-level current	I _{IH}	$V_{IN} = V_{DD}^{*1}$			1	
Input low-level current	I _{IL}	$V_{IN} = V_{SS}^{*1}$	-1			μA
Output high-level voltage	V _{OH}	I _{OH} = -0.4 mA, DIO1, DIO120	$0.8 \times V_{DD}$		V_{DD}	V
Output low-level voltage	V _{OL}	I _{OL} = 0.4 mA, DIO1, DIO120	V _{SS}		$0.2 \times V_{DD}$	V
	RON0	V _{OUT} = V0 – 0.5 V *2, OUT1 to 120			1000	
Output on registance	RON1	V _{OUT} = V1 – 0.5 V *2, OUT1 to 120			1000	Ω
Output on resistance	RON4	V _{OUT} = V4 + 0.5 V *2, OUT1 to 120			1000	22
	RON5	V _{OUT} = V5 + 0.5 V *2, OUT1 to 120			1000	
Current drain 1	I _{DD}	V _{DD} *3			200	
Current drain 2	I _{DDH}	V _{DD} *4			500	μA

Note: 1. LOAD, RS/LS, DISP, DF, DIO1, DIO120, DMIN and MODE

- 2. V_{OUT} is the voltage applied to on-state outputs. V_{OUT} is the voltage applied to on-state outputs.
- 3. $V_{DD} = 2.7 \text{ to } 5.5 \text{ V}, f_{CP} = 50 \text{ kHz}$

Block Diagram

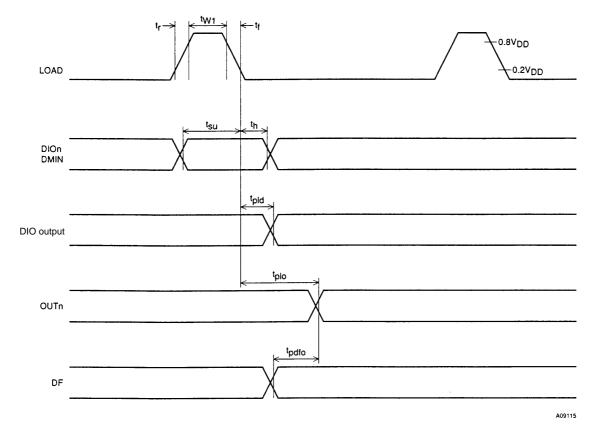


^{4.} f_{DF} = 100 Hz, with no output load, V_{EE} = 36 V, for a single data shift * This IC must be handled carefully enough because the ESD Is not so firm.

Switching Characteristics at V_{DD} = 2.7 to 5.5 $V,\,Ta$ = –20 to +75°C, V_{SS} = 0 V

Parameter	Cumahad	Conditions		Ratings			
	Symbol	Conditions	min	typ	max	Unit	
Clock frequency	fload	LOAD			2.5	MHz	
High-level clock pulse width	twl	LOAD	100				
Input setup time	tsu	LOAD, DIOn, DMIN	100				
Input hold time	th	LOAD, DIOn, DMIN	30]	
LOAD rise time	tr	LOAD			30	ns	
LOAD fall time	tf	LOAD			30		
DIO output delay time	tpld	LOAD, DIOn: 30 pF capacitance load			200		
LOAD-on delay time	tplo	LOAD, OUTn: 100 pF capacitance load			1.0		
DF-on delay time	tpdfo	DF, OUTn: 100 pF capacitance load			1.0	μs	

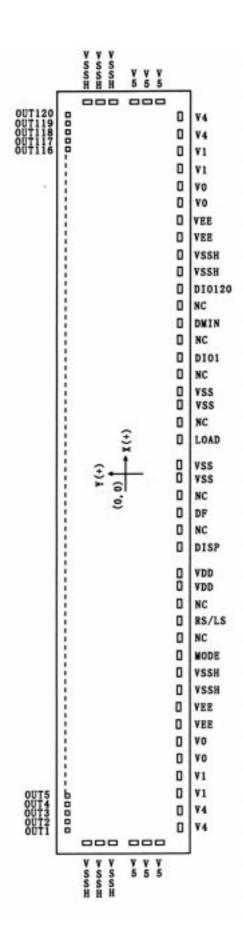
Switching Characteristics



Pin Functions

Symbol	I/O		Function							
		LCD drive of	LCD drive outputs							
		 		DIOD 1		1				
		DF L	Data H	DISP H	OUTn V0	-				
OUT1 to		H	 	Н	V0 V1	-				
OUT120	0	L	L	н	V4	-				
		Н	Н	Н	V5	-				
		*	*	L	V5	*: don't care				
						. don't bare				
VO	ı	V0 level dri	ve voltage	input						
V1	ı	V1 level dri	ve voltage	input						
V4	1	V4 level dri	ve voltage	input						
V5	I	V5 level dri	ve voltage	input						
V _{EE}	_	High-voltag	je block po	wer supply						
V _{SSH}	-	High-voltag	je block gr	ound						
DISP	I	All outputs	will be held	d at a fixed	V5 level wh	nen this pin is low.				
DF	I	Alternation	input							
LOAD	ı	Data shift p	ulse input	(falling edge	e)					
MODE	ı	Data shift d	lirection sp	ecification i	nput					
RS/LS	I	MODE	RS/LS		Shift dire	ection	DIO1	DIO160	DMIN]
		L	L		OUT120 -		OUT	IN	*	
		L	Н		OUT1 → C	OUT120	IN	OUT	*	
DIO1	I/O	н	L		OUT120 OUT60		OUT	IN	IN	
DMIN DIO120	I/O	н	OUT1 -> OUT60							
		*: don't care (Must be fixed at low or high.)								
V _{DD}	_	Logic system power supply								
V _{SS}	_	Logic syste	m ground							

Pad Assignment



Chip size: 12.11 x 2.00 mm

LC4131C Pad Coordinates

Pin	X coordinate	Y coordinate	Pad type	Pin	X coordinate	Y coordinate	Pad type
OUT1	-5652.9	718.5	В	OUT51	-902.9	718.5	В
OUT2	-5557.9	718.5	В	OUT52	-807.9	718.5	В
OUT3	-5462.9	718.5	В	OUT53	-712.9	718.5	В
OUT4	-5367.9	718.5	В	OUT54	-617.9	718.5	В
OUT5	-5272.9	718.5	В	OUT55	-522.9	718.5	В
OUT6	-5177.9	718.5	В	OUT56	-427.9	718.5	В
OUT7	-5082.9	718.5	В	OUT57	-332.9	718.5	В
OUT8	-4987.9	718.5	В	OUT58	-237.9	718.5	В
OUT9	-4892.9	718.5	В	OUT59	-142.9	718.5	В
OUT10	-4797.9	718.5	В	OUT60	-47.9	718.5	В
OUT11	-4702.9	718.5	В	OUT61	47.1	718.5	В
OUT12	-4607.9	718.5	В	OUT62	142.1	718.5	В
OUT13	-4512.9	718.5	В	OUT63	237.1	718.5	В
OUT14	-4417.9	718.5	В	OUT64	332.1	718.5	В
OUT15	-4322.9	718.5	В	OUT65	427.1	718.5	В
OUT16	-4227.9	718.5	В	OUT66	522.1	718.5	В
OUT17	-4132.9	718.5	В	OUT67	617.1	718.5	В
OUT18	-4037.9	718.5	В	OUT68	712.1	718.5	В
OUT19	-3942.9	718.5	В	OUT69	807.1	718.5	В
OUT20	-3847.9	718.5	В	OUT70	902.1	718.5	В
OUT21	-3752.9	718.5	В	OUT71	997.1	718.5	В
OUT22	-3657.9	718.5	В	OUT72	1092.1	718.5	В
OUT23	-3562.9	718.5	В	OUT73	1187.1	718.5	В
OUT24	-3467.9	718.5	В	OUT74	1282.1	718.5	В
OUT25	-3372.9	718.5	В	OUT75	1377.1	718.5	В
OUT26	-3277.9	718.5	В	OUT76	1472.1	718.5	В
OUT27	-3182.9	718.5	В	OUT77	1567.1	718.5	В
OUT28	-3087.9	718.5	В	OUT78	1622.1	718.5	В
OUT29	-2992.9	718.5	В	OUT79	1757.1	718.5	В
OUT30	-2897.9	718.5	В	OUT80	1852.1	718.5	В
OUT31	-2802.9	718.5	В	OUT81	1947.1	718.5	В
OUT32	-2707.9	718.5	В	OUT82	2042.1	718.5	В
OUT33	-2612.9	718.5	В	OUT83	2137.1	718.5	В
OUT34	-2517.9	718.5	В	OUT84	2232.1	718.5	В
OUT35	-2422.9	718.5	В	OUT85	2327.1	718.5	В
OUT36	-2327.9	718.5	В	OUT86	2422.1	718.5	В
OUT37	-2232.9	718.5	В	OUT87	2517.1	718.5	В
OUT38	-2137.9	718.5	В	OUT88	2612.1	718.5	В
OUT39	-2042.9	718.5	В	OUT89	2707.1	718.5	В
OUT40	-1947.9	718.5	В	OUT90	2802.1	718.5	В
OUT41	-1852.9	718.5	В	OUT91	2897.1	718.5	В
OUT42	-1757.9	718.5	В	OUT92	2992.1	718.5	В
OUT43	-1662.9	718.5	В	OUT93	3087.1	718.5	В
OUT44	-1567.9	718.5	В	OUT94	3182.1	718.5	В
OUT45	-1472.9	718.5	В	OUT95	3277.1	718.5	В
OUT46	-1377.9	718.5	В	OUT96	3372.1	718.5	В
OUT47	-1282.9	718.5	В	OUT97	3467.1	718.5	В
OUT48	-1187.9	718.5	В	OUT98	3562.1	718.5	В
OUT49	-1092.9	718.5	В	OUT99	3657.1	718.5	В
OUT50	-997.9	718.5	В	OUT100	3752.1	718.5	В
00100	-551.8	1 10.0	ט	II 301100	3132.1	1 10.3	ט

Pad type	Size
Α	120 μm × 60 μm
В	75 μm × 60 μm
С	100 μm × 60 μm

Continued on next page.

Continued from preceding page.

Pin	X coordinate	Y coordinate	Pad type	Pin	X coordinate	Y coordinate	Pad type
OUT101	3847.1	718.5	В	V _{SS}	88.6	-804.0	Α
OUT102	3942.1	718.5	В	LOAD	475.2	-804.0	Α
OUT103	4037.1	718.5	В	Dammy	757.0	-804.0	Α
OUT104	4132.1	718.5	В	Dammy	1502.6	-804.0	Α
OUT105	4227.1	718.5	В	DIO1	1785.4	-804.0	Α
OUT106	4322.1	718.5	В	Dammy	2065.4	-804.0	Α
OUT107	4417.1	718.5	В	DMIN	2348.2	-804.0	Α
OUT108	4512.1	718.5	В	Dammy	2631.0	-804.0	Α
OUT109	4607.1	718.5	В	DIO120	2913.8	-804.0	Α
OUT110	4702.1	718.5	В	V _{SSH}	3196.6	-803.8	Α
OUT111	4797.1	718.5	В	V _{SSH}	3476.6	-803.8	Α
OUT112	4892.1	718.5	В	V _{EE}	3756.6	-823.8	Α
OUT113	4987.1	718.5	В	V _{EE}	4036.6	-823.8	Α
OUT114	5082.1	718.5	В	V0	4316.6	-813.8	Α
OUT115	5177.1	718.5	В	V0	4596.6	-813.8	Α
OUT116	5272.1	718.5	В	V1	4876.6	-791.0	Α
OUT117	5367.1	718.5	В	V1	5156.6	-791.0	Α
OUT118	5462.1	718.5	В	V4	5436.6	-790.1	Α
OUT119	5557.1	718.5	В	V4	5716.6	-790.1	Α
OUT120	5652.1	718.5	В	V _{SS}	1039.8	-783.6	Α
V4	-5713.8	-790.8	A	V _{SS}	1219.8	-783.6	Α
V4	-5433.8	-790.8	A	V1	-5153.8	-786.9	Α
V_{EE}	-4033.8	-813.8	A	V1	-4873.8	-786.9	Α
V _{EE}	-3753.8	-813.8	A	V0	-4593.8	-786.9	Α
V_{SSH}	-3473.8	-811.2	A	V0	-4313.8	-786.9	Α
V_{SSH}	-3193.8	-811.2	A	V5	5895.0	-462.5	С
MODE	-2911.0	-804.0	A	V5	-5891.7	-459.5	С
Dammy	-2631.0	-804.0	Α	V5	5895.0	-322.5	С
RSLS	-2347.2	-804.0	Α	V5	-5891.7	-319.5	С
Dammy	-2065.4	-804.0	Α	V5	5895.0	-182.5	С
V_{DD}	-1782.6	-804.0	Α	V5	-5891.7	-179.5	С
V_{DD}	-1605.4	-804.0	A	V _{SSH}	-5885.7	87.5	С
DISP	-1219.8	-804.0	A	V _{SSH}	5885.6	87.5	С
Dammy	-939.8	-804.0	A	V _{SSH}	-5885.7	227.5	С
DF	-654.2	-804.0	А	V _{SSH}	5885.6	227.5	С
Dammy	-374.2	-804.0	А	V _{SSH}	-5885.7	367.5	С
V _{SS}	-91.4	-804.0	A	V _{SSH}	5885.6	367.5	С

- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
 - ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
 - ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of April, 1998. Specifications and information herein are subject to change without notice.