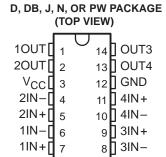
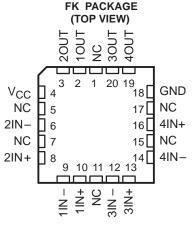
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage 2 V to 36 V
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ
- Low Input Bias Current . . . 25 nA Typ
- Low Input Offset Current . . . 3 nA Typ (LM139)
- Low Input Offset Voltage . . . 2 mV Typ
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output-Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

description

These devices consist of four independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies is also possible as long as the difference between the two supplies is 2 V to 36 V and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wire-AND relationships.

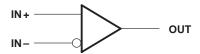
The LM139 and LM139A are characterized for operation from -55° C to 125° C. The LM239 and LM239A are characterized for operation from -25° C to 125° C. The LM339 and LM339A are characterized for operation from 0° C to 70° C. The LM2901 and LM2901Q are characterized for operation from -40° C to 125° C.





NC - No internal connection

symbol (each comparator)





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



LM139, LM139A, LM239, LM239A, LM339 LM339A, LM339Y, LM2901, LM2901Q QUAD DIFFERENTIAL COMPARATORS

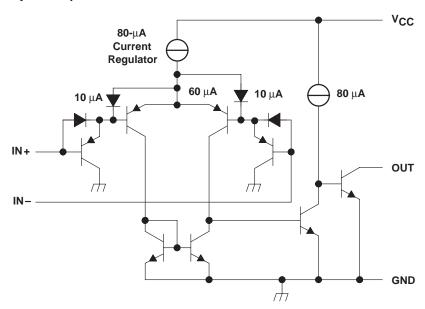
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AVAILABLE OPTIONS

| | | | PACKAGED DEVICES | | | | | | | |
|----------------|--------------------------------|--------------------------|------------------|-------------------------|-----------------------|-----------------------|-----------------|-------------------|--|--|
| TA | V _{IO} max at 25°C | SMALL OUTLINE (D)† | SSOP (DB)‡ | CHIP CARRIER (FK) | CERAMIC DIP (J) | PLASTIC DIP (N) | TSSOP (PW)‡ | CHIP FORM (Y)§ | | |
| 0°C to 70°C | 5 mV 2 mV | LM339D LM339AD | LM339DBLE — | _ | _ | LM339N LM339AN | LM339PWLE — | LM339Y | | |
| -25°C to 85°C | 5 mV 2 mV | LM239D LM239AD | _ | _ | _ | LM239N LM239AN | _ | _ | | |
| -40°C to 125°C | 7 mV | LM2901D LM2901QD | LM2901DBLE — | _ | _ | LM2901QN | LM2901PWLE — | _ | | |
| -55°C to 125°C | 5 mV 2 mV | LM139D LM139AD | _ | LM139AFK | LM139J LM139AJ | LM139N LM139AN | _ | _ | | |

[†] The D package is available taped and reeled. Add the suffix R to the device type (e.g., LM339DR).

schematic (each comparator)



All current values shown are nominal.



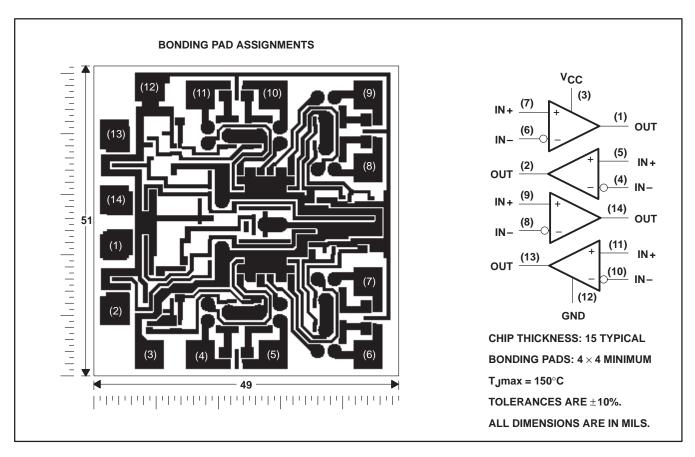
[‡]The DB and PW packages are only available left-end taped and reeled.

[§] Chips are tested at 25°C (see electrical characteristics).

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LM339Y chip information

This chip, when properly assembled, displays characteristics similar to the LM339. Thermal compression or ultrasonic bonding can be used on the doped-aluminum bonding pads. Chips can be mounted with conductive epoxy or a gold-silicon preform.



LM139, LM139A, LM239, LM239A, LM339 LM339A, LM339Y, LM2901, LM2901Q QUAD DIFFERENTIAL COMPARATORS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

| Supply voltage, V _{CC} (see Note 1) | |
|--|--------------------------------|
| Differential input voltage, V _{ID} (see Note 2) | ±36 V |
| Input voltage range, V _I (either input) | |
| Output voltage, VO | 36 V |
| Output current, IO | |
| Duration of output short circuit to ground (see Note 3) | unlimited |
| Continuous total dissipation | . See Dissipation Rating Table |
| Operating free-air temperature range, T _A : LM139, LM139A | –55°C to 125°C |
| LM239, LM239A | –25°C to 85°C |
| LM339, LM339A | 0°C to 70°C |
| LM2901, LM2901Q | |
| Storage temperature range | |
| Case temperature for 60 seconds: FK package | 260°C |
| Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D, DB, N, or | PW package 260°C |
| Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package | 300°C |

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to network ground.
 - 2. Differential voltages are at IN+ with respect to IN-.
 - 3. Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.

DISSIPATION RATING TABLE

| PACKAGE | $T_A \le 25^{\circ}C$ POWER RATING | DERATING FACTOR | DERATE ABOVE T _A | T _A = 70°C POWER RATING | T _A = 85°C POWER RATING | T _A = 125°C POWER RATING |
|---------|------------------------------------|--------------------|--------------------------------|---------------------------------------|---------------------------------------|--|
| D | 900 mW | 7.6 mW/°C | 31°C | 608 mW | 494 mW | _ |
| DB | 775 mW | 6.2 mW/°C | 25°C | 496 mW | 403 mW | 155 mW |
| FK | 900 mW | 11.0 mW/°C | 68°C | 880 mW | 715 mW | 275 mW |
| J | 900 mW | 11.0 mW/°C | 68°C | 880 mW | 715 mW | 275 mW |
| N | 900 mW | 9.2 mW/°C | 52°C | 736 mW | 598 mW | _ |
| PW | 700 mW | 5.6 mW/°C | 25°C | 448 mW | 364 mW | 140 mW |



electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

| | PARAMETER | TEST CONDITIONS | | T + | L | M139 | | LM139A | | | UNIT |
|--|---|---|---|------------------|----------------------|------|------|----------------------|-----|------|------|
| | PARAMETER | | | T _A † | MIN | TYP | MAX | MIN | TYP | MAX | UNII |
| V | Input offset voltage | $V_{CC} = 5 \text{ V to } 30 \text{ V},$ | V _{IC} = V _{ICR} min, | 25°C | | 2 | 5 | | 1 | 2 | mV |
| VIO | input onset voltage | V _O = 1.4 V | | −55°C to 125°C | | | 9 | | | 4 | IIIV |
| lio. | Input offset current | V _O = 1.4 V | | | | 3 | 25 | | 3 | 25 | nA |
| lio | input onset current | VO = 1.4 V | | −55°C to 125°C | | | 100 | | | 100 | IIA |
| lun. | Input higo ourrent | Vo - 1.4.V | | 25°C | | -25 | -100 | | -25 | -100 | nA |
| IB | Input bias current | V _O = 1.4 V | | -55°C to 125°C | | | -300 | | | -300 | IIA |
| V _{ICR} Cor | | | | 25°C | 0 to | | | 0 to | | | |
| | Common-mode input voltage range | | | 20 0 | V _{CC} -1.5 | | | V _{CC} -1.5 | | | V |
| 1 ICK | gogo | | | -55°C to 125°C | 0 to | | | 0 to | | | |
| | | | | | V _{CC} -2 | | | V _{CC} -2 | | | |
| A _{VD} | Large-signal differential voltage amplification | $V_{CC} \pm = \pm 7.5 \text{ V},$ | $V_O = -5 \text{ V to } 5 \text{ V}$ | 25°C | | 200 | | 50 | 200 | | V/mV |
| 10 | Ligh level output ourrent | V 4.V | V _{OH} = 5 V | 25°C | | 0.1 | | | 0.1 | | nA |
| ЮН | High-level output current | V _{ID} = 1 V | V _{OH} = 30 V | -55°C to 125°C | | | 1 | | | 1 | μΑ |
| \/-· | Low level output valte as | V 4 V | l 4 σσ.Λ | 25°C | | 150 | 400 | | 150 | 400 | mV |
| V _{OL} Low-level output voltage | | $V_{ID} = -1 V$, $I_{OL} = 4 \text{ mA}$ | | -55°C to 125°C | | | 700 | | | 700 | IIIV |
| lOL | Low-level output current | $V_{ID} = -1 V$, | V _{OL} = 1.5 V | 25°C | 6 | 16 | | 6 | 16 | | mA |
| Icc | Supply current (four comparators) | V _O = 2.5 V, | No load | 25°C | | 0.8 | 2 | | 0.8 | 2 | mA |

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

| PARAMETER | | TEST C | ONDITIONS | LM13 | UNIT | | |
|---------------|-----------|--|---------------------------------------|------|------|-----|------|
| | FARAMETER | | | MIN | TYP | MAX | UNII |
| Response time | | R _L connected to 5 V through 5.1 k Ω , | 100-mV input step with 5-mV overdrive | 1.3 | | | |
| | | $C_L = 15 \text{ pF}^{\ddagger}$, See Note 4 | TTL-level input step | 0.3 | | | μs |

‡C_L includes probe and jig capacitance.

NOTE 4: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

| | DADAMETED | TEST OF | NOTIONS | T _A † | LM239 | 9, LM339 | 9 | LM239A | ۱, LM33 | 9A | LM2901, LM2901Q | | | |
|------|---|---|-------------------------|------------------|------------------------------|----------|------|------------------------------|---------|------|------------------------------|-----|------|------|
| l | PARAMETER | IEST CO | TEST CONDITIONS | | MIN | TYP | MAX | MIN | TYP | MAX | MIN | TYP | MAX | UNIT |
| Vio | Input offset voltage | V _{CC} = 5 V to 3 | | 25°C | | 2 | 5 | | 1 | 3 | | 2 | 7 | mV |
| VIO | Input onset voltage | V _O = 1.4 V | | Full range | | | 9 | | | 4 | | | 15 | IIIV |
| | Input offset current | V _O = 1.4 V | , | 25°C | | 5 | 50 | | 5 | 50 | | 5 | 50 | nA |
| lio | Input onset outrent | VO = 1.4 V | | Full range | | | 150 | | | 150 | | | 200 | 11/2 |
| lin. | Input bias current | V _O = 1.4 V | | 25°C | | -25 | -250 | | -25 | -250 | | -25 | -250 | nA |
| IB | Input bias current | VO = 1.4 V | VO = 1.4 V | | | | -400 | | | -400 | | | -500 | 11/5 |
| \/ | ,, Common-mode input | · • | | 25°C | 0 to V _{CC} -1.5 | | | 0 to V _{CC} -1.5 | | | 0 to V _{CC} -1.5 | | | |
| VICR | voltage range | | | Full range | 0 to V _{CC} -2 | | | 0 to V _{CC} -2 | | | 0 to V _{CC} -2 | | | V |
| AVD | Large-signal differential voltage amplification | $V_{CC} = 15 \text{ V},$ $V_{O} = 1.4 \text{ V to}$ $R_{L} \ge 15 \text{ k}\Omega \text{ to}$ | | 25°C | 50 | 200 | | 50 | 200 | | 25 | 100 | | V/mV |
| 1 | Librah Jawal autaut augrant | V:= -1 V | V _{OH} = 5 V | 25°C | | 0.1 | 50 | | 0.1 | 50 | | 0.1 | 50 | nA |
| ЮН | High-level output current | V _{ID} = 1 V | V _{OH} = 30 V | Full range | | | 1 | | | 1 | | | 1 | μΑ |
| V2. | Less lovel output voltage | V:= = 1 V | 1 4 mA | 25°C | | 150 | 400 | | 150 | 400 | | 150 | 500 | mV |
| VOL | V _{OL} Low-level output voltage | output voltage $V_{ID} = -1 \text{ V}, I_{OL} = 4 \text{ m}$ | I _{OL} = 4 mA | Full range | | | 700 | | | 700 | | | 700 | 1117 |
| lOL | Low-level output current | $V_{ID} = -1 V$, | V _{OL} = 1.5 V | 25°C | 6 | 16 | | 6 | 16 | | 6 | 16 | | mA |
| | Comple agreent | $V_0 = 2.5 V$, | No load | <u>'</u> | | 0.8 | 2 | | 0.8 | 2 | | 0.8 | 2 | |
| Icc | Supply current (four comparators) | V _{CC} = 30 V, No load | $V_0 = 2.5 V$, | 25°C | | | | | | | | 1 | 2.5 | mA |

[†] Full range (MIN to MAX) for LM239 and LM239A is -25°C to 85°C, for LM339 and LM339A is 0°C to 70°C, and for LM2901 is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

| PARAMETER | TEST C | TEST CONDITIONS | | | | |
|---------------|--|---------------------------------------|-----|-----|--|----|
| | | MIN | TYP | MAX | | |
| Response time | R _L connected to 5 V through 5.1 k Ω , | 100-mV input step with 5-mV overdrive | | 1.3 | | |
| Response une | $C_L = 15 \text{ pF}^{\ddagger}$, See Note 4 | TTL-level input step | 0.3 | | | μs |

[‡]C_L includes probe and jig capacitance.

NOTE 4: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

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electrical characteristics at V_{CC} = 5 V, T_A = 25°C (unless otherwise noted)

| | PARAMETER | TEST 00 | NOITIONST | LN | //339Y | | UNIT |
|-----------------|---|--|---|------------------------------|--------|------|------|
| | PARAMETER | IEST CO | TEST CONDITIONS† | | | MAX | UNIT |
| V _{IO} | Input offset voltage | $V_{CC} = 5 \text{ V to } 30 \text{ V},$ $V_{O} = 1.4 \text{ V}$ | $V_{IC} = V_{ICR}min,$ | | 2 | 5 | mV |
| lio | Input offset current | V _O = 1.4 V | | | 5 | 50 | nA |
| I _{IB} | Input bias current | V _O = 1.4 V | | | -25 | -250 | nA |
| VICR | Common-mode input voltage range | | | 0 to V _{CC} -1.5 | | | V |
| A _{VD} | Large-signal differential voltage amplification | $V_{CC} = 15 \text{ V},$ $R_L \ge 15 \text{ k}\Omega \text{ to } V_{CC}$ | $V_0 = 1.4 \text{ V to } 11.4 \text{ V},$ | 25 | 100 | | V/mV |
| loh | High-level output current | V _{ID} = 1 V, | V _{OH} = 5 V | | 0.1 | 50 | nA |
| VOL | Low-level output voltage | $V_{ID} = -1 V$ | I _{OL} = 4 mA | | 150 | 400 | mV |
| loL | Low-level output current | $V_{ID} = -1 V$, | V _{OL} = 1.5 V | 6 | 16 | | mA |
| | | V _O = 2.5 V, | No load | | 0.8 | 2 | |
| Icc | Supply current (four comparators) | V _O = 30 V, No load | V _O = 15 V, | | 1 | 2.5 | mA |

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

| PARAMETER | TEST CO | ANDITIONS | L | .M339Y | | UNIT |
|---------------|--|---------------------------------------|---|--------|-----|------|
| PARAMETER | TEST CC | TEST CONDITIONS | | TYP | MAX | UNIT |
| Poononoo timo | R _L connected to 5 V through 5.1 k Ω , | 100-mV input step with 5-mV overdrive | | 1.3 | | |
| Response time | $C_L = 15 \text{ pF}^{\ddagger}$, See Note 4 | TTL-level input step | | 0.3 | | μs |

‡ C_L includes probe and jig capacitance.

NOTE 4: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



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