

## LP2982 Micropower 50 mA Ultra Low-Dropout Regulator in SOT-23 Package

Check for Samples: [LP2982](#)

### FEATURES

- Ultra low dropout voltage
- Guaranteed 50 mA output current
- Typical dropout voltage 180 mV @ 80 mA
- Requires minimum external components
- < 1  $\mu$ A quiescent current when shutdown
- Low ground pin current at all loads
- Output voltage accuracy 1.0% (A Grade)
- High peak current capability (150 mA typical)
- Wide supply voltage range (16V max)

- Low  $Z_{OUT}$  0.3 $\Omega$  typical (10 Hz to 1 MHz)
- Over-Temperature/Over-Current protection
- $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  junction temperature range

### APPLICATIONS

- Cellular Phone
- Palmtop/Laptop Computer
- Personal Digital Assistant (PDA)
- Camcorder, Personal Stereo, Camera

### DESCRIPTION

The LP2982 is a 50 mA, fixed-output voltage regulator designed to provide ultra low dropout and lower noise in battery powered applications.

Using an optimized VIP™ (Vertically Integrated PNP) process, the LP2982 delivers unequalled performance in all specifications critical to battery-powered designs:

**Dropout Voltage:** Typically 120 mV @ 50 mA load, and 7 mV @ 1 mA load.

**Ground Pin Current:** Typically 375  $\mu$ A @ 50 mA load, and 80  $\mu$ A @ 1 mA load.

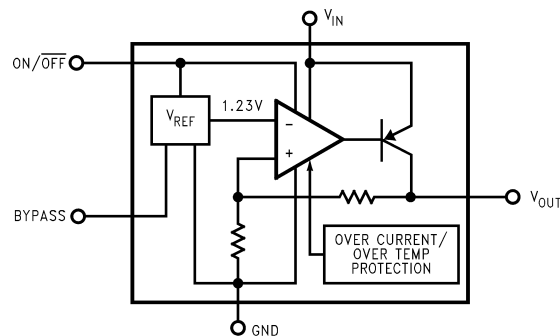
**Sleep Mode:** Less than 1  $\mu$ A quiescent current when on/off pin is pulled low.

**Precision Output:** 1.0% tolerance output voltages available (A grade).

**Low Noise:** By adding an external bypass capacitor, output noise can be reduced to 30  $\mu$ V (typical).

Four output voltage versions, from 3.0V to 5.0V, are available as standard products.

### Block Diagram



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

VIP is a trademark of Texas Instruments.

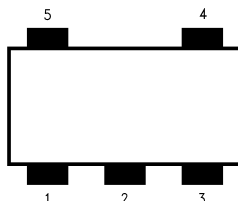
All other trademarks are the property of their respective owners.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of the Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

Copyright © 2004–2009, Texas Instruments Incorporated

## Connection Diagram

### 5-Lead Small Outline Package (SOT-23)



**Figure 1. Top View**  
See NS Package Number MF05A

## Pin Functions

### Pin Descriptions

Name	Pin Number	Function
V <sub>IN</sub>	1	Input Voltage
GND	2	Common Ground (device substrate)
ON/OFF	3	Logic high enable input
BYPASS	4	Bypass capacitor for low noise operation
V <sub>OUT</sub>	5	Regulated output voltage



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

## Absolute Maximum Ratings <sup>(1)</sup>

Storage Temperature Range	–65°C to +150°C
Operating Junction Temperature Range	–40°C to +125°C
Lead Temperature (Soldering, 5 sec.)	260°C
ESD Rating <sup>(2)</sup>	2 kV
Power Dissipation <sup>(3)</sup>	Internally Limited
Input Supply Voltage (Survival)	–0.3V to +16V
Input Supply Voltage (Operating)	2.1V to +16V
Shutdown Input Voltage (Survival)	–0.3V to +16V
Output Voltage (Survival, <sup>(4)</sup> )	–0.3V to +9V
I <sub>OUT</sub> (Survival)	Short Circuit Protected
Input-Output Voltage (Survival, <sup>(5)</sup> )	–0.3V to +16V

(1) Absolute maximum ratings indicate limits beyond which damage to the component may occur. Electrical specifications do not apply when operating the device outside of its rated operating conditions.

(2) The ESD rating of pins 3 and 4 for the SOT-23 package, or pins 5 and 2 for the micro SMD package, is 1 kV.

(3) The maximum allowable power dissipation is a function of the maximum junction temperature, T<sub>J(MAX)</sub>, the junction-to-ambient thermal resistance, θ<sub>JA</sub>, and the ambient temperature, T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is calculated

$$P_{(MAX)} = \frac{T_{J(MAX)} - T_A}{\theta_{JA}}$$

using: The value of θ<sub>JA</sub> for the SOT-23 package is 220°C/W. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown.

(4) If used in a dual-supply system where the regulator load is returned to a negative supply, the LP2982 output must be diode-clamped to ground.

(5) The output PNP structure contains a diode between the V<sub>IN</sub> and V<sub>OUT</sub> terminals that is normally reverse-biased. Reversing the polarity from V<sub>IN</sub> to V<sub>OUT</sub> will turn on this diode. (See [REVERSE CURRENT PATH](#).)

## Electrical Characteristics

Limits in standard typeface are for  $T_J = 25^\circ\text{C}$ , and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified:  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1\text{ mA}$ ,  $C_{IN} = 1\text{ }\mu\text{F}$ ,  $C_{OUT} = 4.7\text{ }\mu\text{F}$ ,  $V_{ON/OFF} = 2\text{V}$ .

Symbol	Parameter	Conditions	Typ	LP2982AI-X.X		LP2982I-X.X		Units
				(1)		(1)		
				Min	Max	Min	Max	
$\Delta V_O$	Output Voltage Tolerance	$I_L = 1\text{ mA}$		-1.0	+1.0	-1.5	+1.5	% $V_{NOM}$
		$1\text{ mA} < I_L < 50\text{ mA}$		-1.5	+1.5	-2.0	+2.0	
				-2.0	+2.0	-3.5	+3.5	
$\frac{\Delta V_O}{\Delta V_{IN}}$ (1)	Output Voltage Line Regulation	$V_{O(NOM)} + 1V \leq V_{IN} \leq 16V$	0.007		0.014		0.014	%/ $V$
					0.032		0.032	
$V_{IN}-V_O$	Dropout Voltage	$I_L = 0$	1		3		3	mV
	(2)				5		5	
		$I_L = 1\text{ mA}$	7		10		10	
					15		15	
		$I_L = 10\text{ mA}$	40		60		60	
					90		90	
		$I_L = 50\text{ mA}$	120		150		150	
					225		225	
$I_{GND}$	Ground Pin Current	$I_L = 0$	65		95		95	$\mu A$
					125		125	
		$I_L = 1\text{ mA}$	80		110		110	
					170		170	
		$I_L = 10\text{ mA}$	140		220		220	
					460		460	
		$I_L = 50\text{ mA}$	375		600		600	
					1200		1200	
		$V_{ON/OFF} < 0.3V$	0.01		0.8		0.8	
		$V_{ON/OFF} < 0.15V$	0.10		2.0		2.0	
$V_{ON/OFF}$	ON/OFF Input Voltage	High = O/P ON	1.4	1.6		1.6		V
	(3)	Low = O/P OFF	0.55		0.15		0.15	
$I_{ON/OFF}$	ON/OFF Input Current	$V_{ON/OFF} = 0$	0.01		-2		-2	$\mu A$
		$V_{ON/OFF} = 5V$	5		15		15	
$I_{O(PK)}$	Peak Output Current	$V_{OUT} \geq V_{O(NOM)} - 5\%$	150	100		100		mA
$V_{IN}-V_O$	Dropout Voltage	$I_L = 80\text{ mA}$	180		225		225	mV
					325		325	
$I_{GND}$	Ground Pin Current	$I_L = 80\text{ mA}$	525		750		750	$\mu A$
					1400		1400	
$e_n$	Output Noise Voltage (RMS)	BW = 300 Hz–50 kHz, $C_{OUT} = 10\text{ }\mu F$ $C_{BYPASS} = 0.01\text{ }\mu F$	30					$\mu V$
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$ (2)	Ripple Rejection	f = 1 kHz $C_{OUT} = 10\text{ }\mu F$	45					dB

- (1) Temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Average Outgoing Quality Level (AOQL).
- (2) Dropout voltage is defined as the input to output differential at which the output voltage drops 100 mV below the value measured with a 1V differential.
- (3) The ON/OFF inputs must be properly driven to prevent possible misoperation. For details, refer to [Application Hints](#).

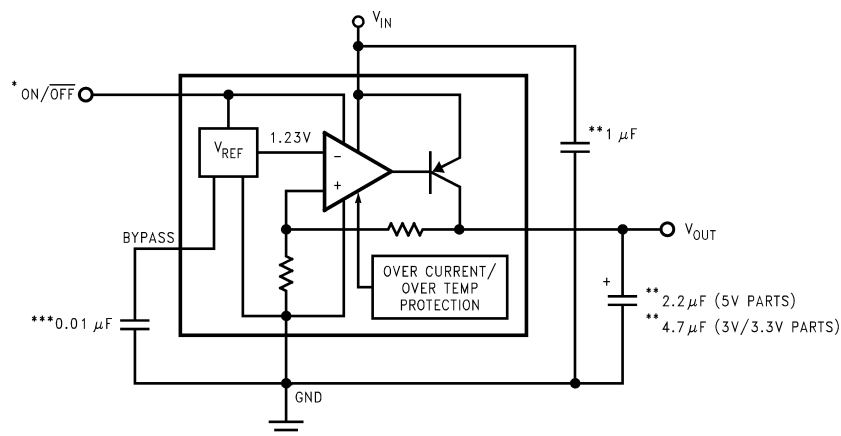
## Electrical Characteristics (continued)

Limits in standard typeface are for  $T_J = 25^\circ\text{C}$ , and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified:  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $I_L = 1\text{ mA}$ ,  $C_{IN} = 1\text{ }\mu\text{F}$ ,  $C_{OUT} = 4.7\text{ }\mu\text{F}$ ,  $V_{ON/OFF} = 2\text{V}$ .

Symbol	Parameter	Conditions	Typ	LP2982AI-X.X		LP2982I-X.X		Units
				(1)		(1)		
				Min	Max	Min	Max	
I <sub>O(MAX)</sub>	Short Circuit Current	R <sub>L</sub> = 0 (Steady State) (4)	150					mA

(4) See *Typical Performance Characteristics* curve(s).

## Typical Application Circuit



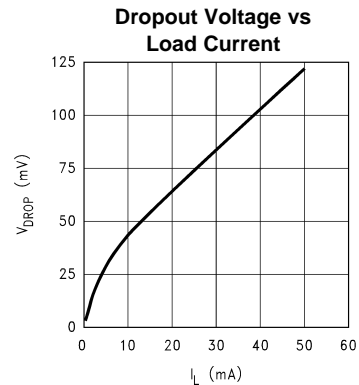
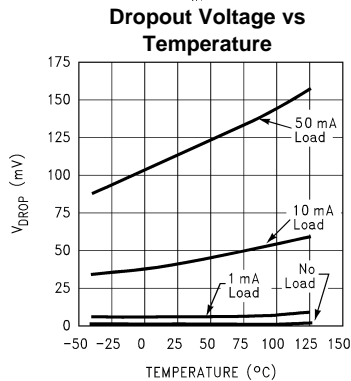
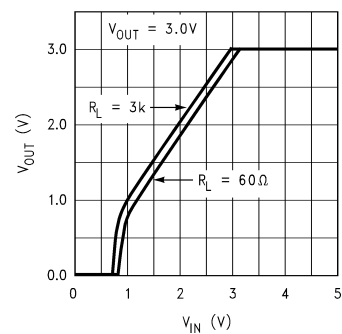
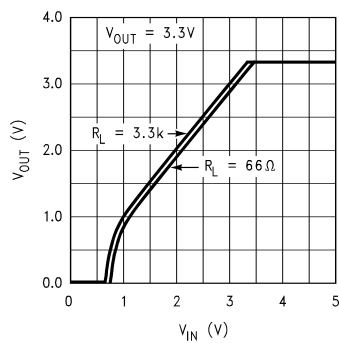
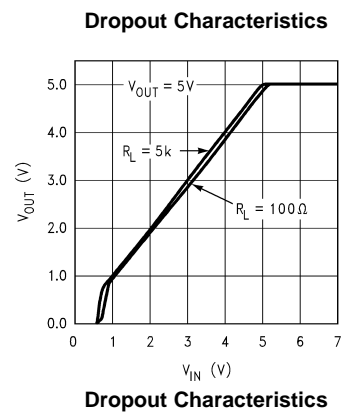
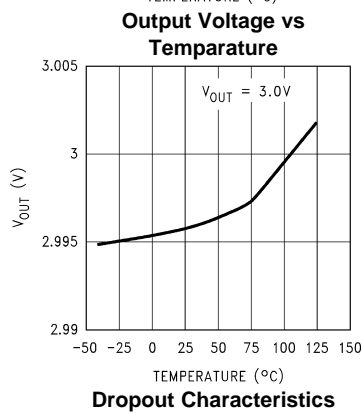
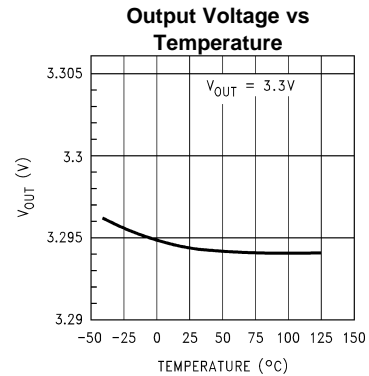
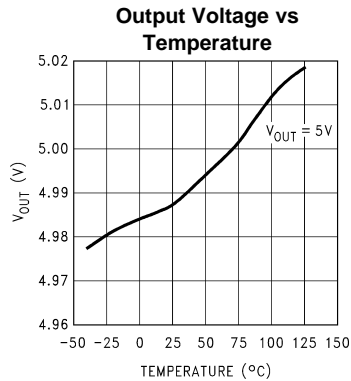
\* $\overline{\text{ON/OFF}}$  input must be actively terminated. Tie to  $V_{IN}$  if this function is not to be used.

\*\*Minimum capacitance is shown to insure stability over full load current range. More capacitance provides superior dynamic performance (see Application Hints).

\*\*\*See Application Hints.

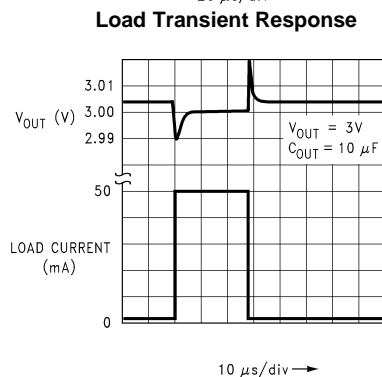
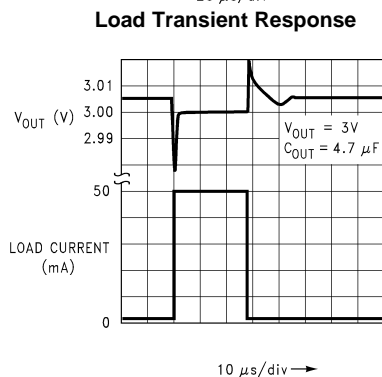
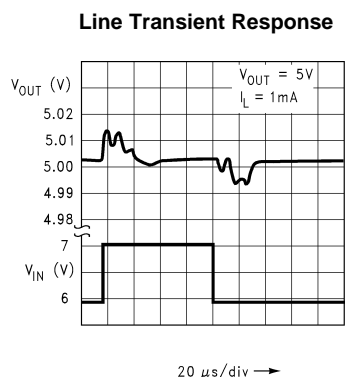
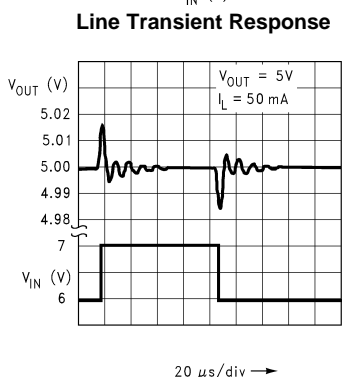
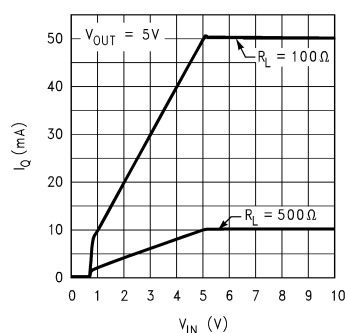
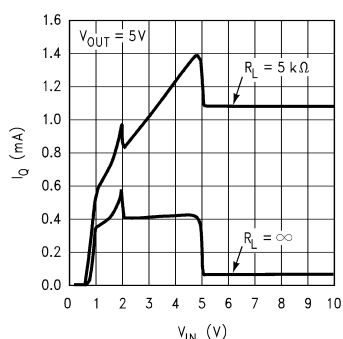
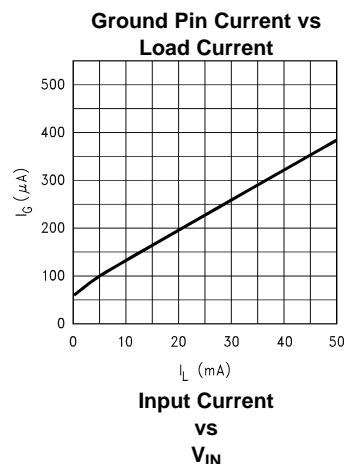
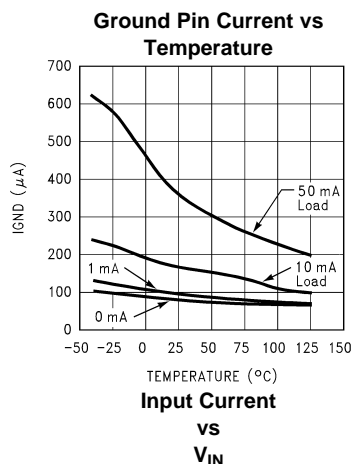
## Typical Performance Characteristics

Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $C_{OUT} = 4.7\text{ }\mu\text{F}$ ,  $C_{IN} = 1\text{ }\mu\text{F}$ , all voltage options, ON/OFF pin tied to  $V_{IN}$ .



### Typical Performance Characteristics (continued)

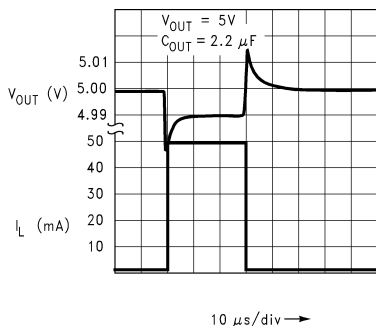
Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 1\ \mu\text{F}$ , all voltage options, ON/OFF pin tied to  $V_{IN}$ .



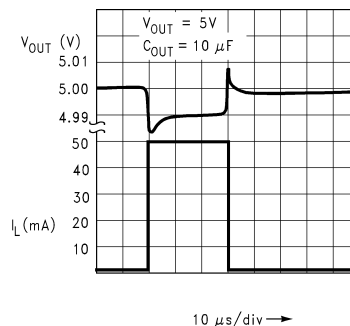
## Typical Performance Characteristics (continued)

Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 1\ \mu\text{F}$ , all voltage options, ON/OFF pin tied to  $V_{IN}$ .

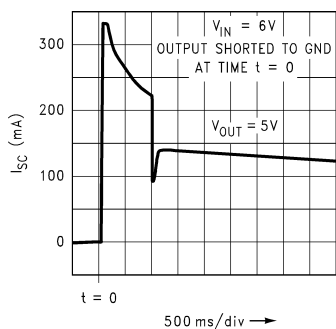
**Load Transient Response**



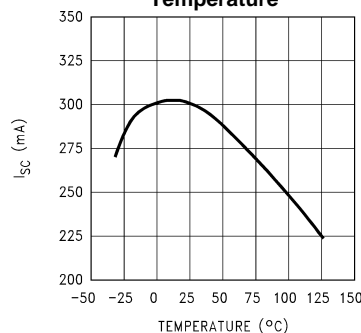
**Load Transient Response**



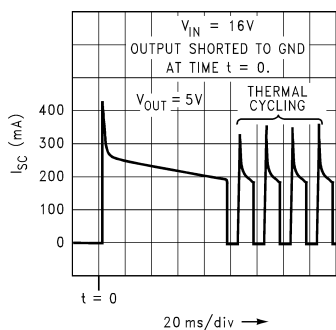
**Short Circuit Current**



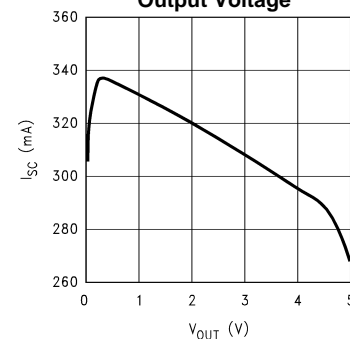
**Instantaneous Short Circuit  
Current  
vs  
Temperature**



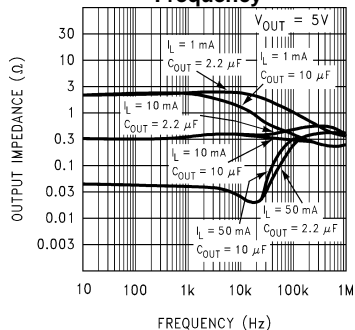
**Short Circuit Current**



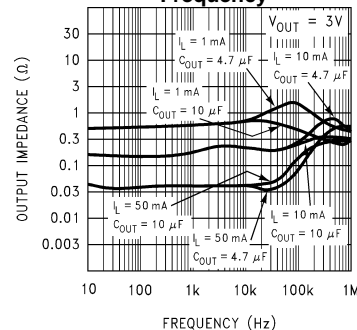
**Instantaneous Short Circuit  
Current  
vs  
Output Voltage**



**Output Impedance vs  
Frequency**



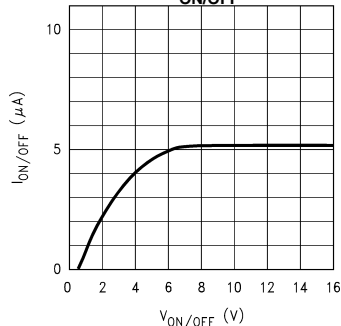
**Output Impedance vs  
Frequency**



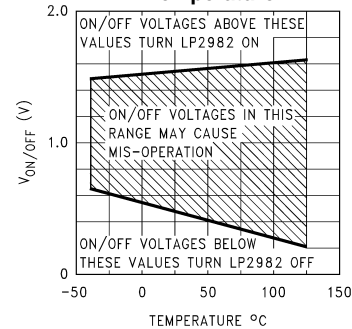
## Typical Performance Characteristics (continued)

Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $C_{IN} = 1\ \mu\text{F}$ , all voltage options, ON/OFF pin tied to  $V_{IN}$ .

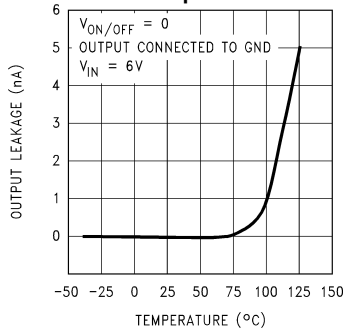
**ON/OFF Pin Current vs  
 $V_{ON/OFF}$**



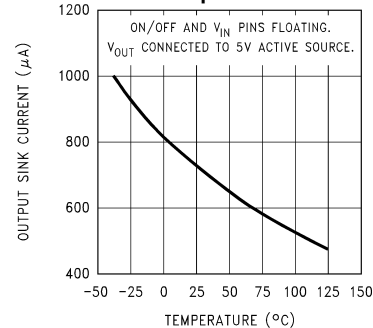
**ON/OFF Threshold vs  
Temperature**



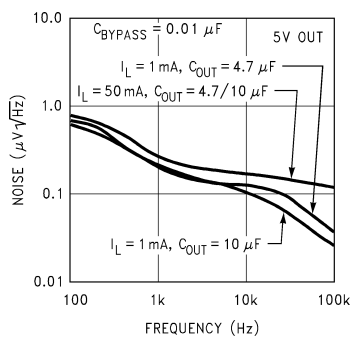
**Input to Output Leakage vs  
Temperature**



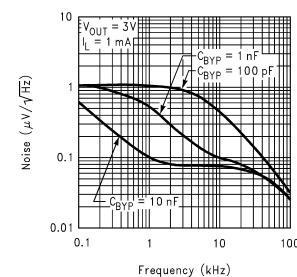
**Output Reverse Leakage vs  
Temperature**



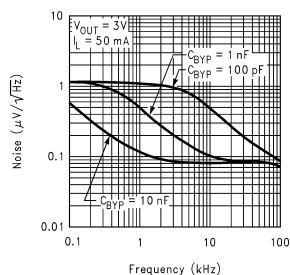
**Output Noise Density**



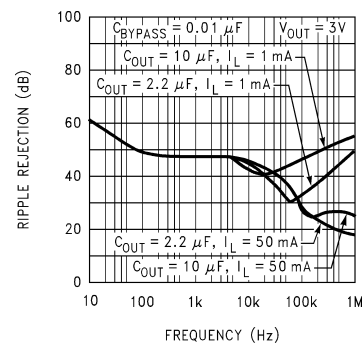
**Output Noise Density**



**Output Noise Density**



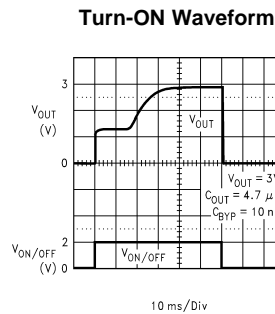
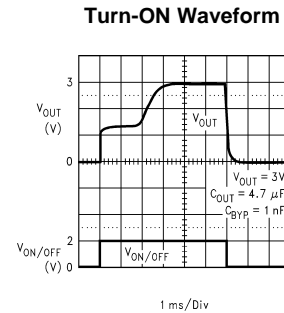
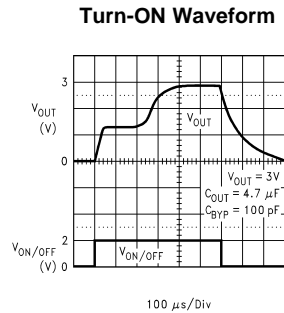
**Ripple Rejection**





## Typical Performance Characteristics (continued)

Unless otherwise specified:  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{O(NOM)} + 1\text{V}$ ,  $C_{OUT} = 4.7\text{ }\mu\text{F}$ ,  $C_{IN} = 1\text{ }\mu\text{F}$ , all voltage options, ON/OFF pin tied to  $V_{IN}$ .



## Application Hints

### EXTERNAL CAPACITORS

Like any low-dropout regulator, the external capacitors used with the LP2982 must be carefully selected to assure regulator loop stability.

**Input Capacitor:** An input capacitor whose value is  $\geq 1\text{ }\mu\text{F}$  is required with the LP2982 (amount of capacitance can be increased without limit).

This capacitor must be located a distance of not more than 0.5" from the input pin of the LP2982 and returned to a clean analog ground. Any good quality ceramic or tantalum can be used for this capacitor.

**Output Capacitor:** The output capacitor must meet both the requirement for minimum amount of capacitance and E.S.R. (equivalent series resistance) value. Curves are provided which show the allowable ESR range as a function of load current for various output voltages and capacitor values (refer to [Figure 2](#), [Figure 3](#)).

**Important:** The output capacitor must maintain its ESR in the stable region over the full operating temperature to assure stability. Also, capacitor tolerance and variation with temperature must be considered to assure the minimum amount of capacitance is provided at all times.

This capacitor should be located not more than 0.5" from the output pin of the LP2982 and returned to a clean analog ground.

**Low-current Operation:** In applications where the load current is  $< 1\text{ mA}$ , special consideration must be given to the output capacitor.

Circuitry inside the LP2982 is specially designed to reduce operating (quiescent) current at light loads down to about 65  $\mu\text{A}$ .

The mode of operation which yields this very low quiescent current also means that the output capacitor ESR is critical.

For optimum stability and minimum output noise, it is recommended that a 10 $\Omega$  resistor be placed in series with the output capacitor in any applications where  $I_L < 1\text{ mA}$ .

## CAPACITOR CHARACTERISTICS

**Tantalum:** Tantalum capacitors are the best choice for use with the LP2982. Most good quality tantalum can be used with the LP2982, but check the manufacturer's data sheet to be sure the ESR is in range.

It is important to remember that ESR increases sharply at lower temperatures ( $< 10^{\circ}\text{C}$ ) and a capacitor that is near the upper limit for stability at room temperature can cause instability when it gets cold.

In applications which must operate at very low temperatures, it may be necessary to parallel the output tantalum capacitor with a ceramic capacitor to prevent the ESR from going up too high (see next section for important information on ceramic capacitors).

**Ceramic:** Ceramic capacitors are not recommended for use at the output of the LP2982. This is because the ESR of a ceramic can be low enough to go below the minimum stable value for the LP2982. A good  $2.2\ \mu\text{F}$  ceramic was measured and found to have an ESR of about  $15\ \text{m}\Omega$ , which is low enough to cause oscillations.

If a ceramic capacitor is used on the output, a  $1\ \Omega$  resistor should be placed in series with the capacitor.

**Aluminum:** Because of large physical size, aluminum electrolytic are not typically used with the LP2982. They must meet the same ESR requirements over the operating temperature range, which is more difficult because of their large increase in ESR at cold temperature.

An aluminum electrolytic can exhibit an ESR increase of as much as 50X when going from  $20^{\circ}\text{C}$  to  $-40^{\circ}\text{C}$ . Also, some aluminum electrolytic are not operational below  $-25^{\circ}\text{C}$  because the electrolyte can freeze.

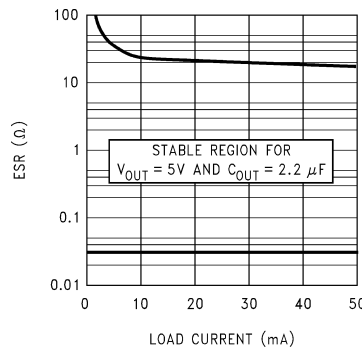


Figure 2. 5V/2.2  $\mu\text{F}$  ESR Curves

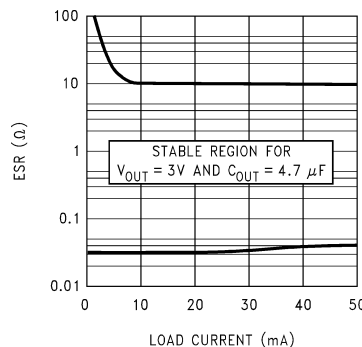


Figure 3. 3V/4.7  $\mu\text{F}$  ESR Curves

## BYPASS CAPACITOR

The  $0.01\ \mu\text{F}$  capacitor connected to the bypass pin to reduce noise must have very low leakage.

The current flowing out of the bypass pin comes from the bandgap reference, which is used to set the output voltage.

This capacitor leakage current causes the output voltage to decline by an amount proportional to the current. Typical values are  $-0.015\%/n\text{A}$  @  $-40^{\circ}\text{C}$ ,  $-0.021\%/n\text{A}$  @  $25^{\circ}\text{C}$ , and  $-0.035\%/n\text{A}$  @  $+125^{\circ}\text{C}$ .

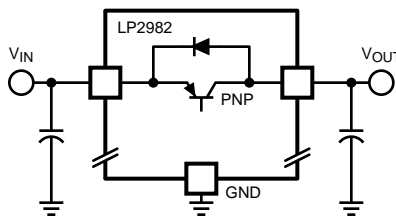
This data is valid up to a maximum leakage current of about 500 nA, beyond which the bandgap is so severely loaded that it can not function.

Care must be taken to ensure that the capacitor selected will not have excessive leakage current over the operating temperature range of the application.

A high quality ceramic capacitor which uses either NPO or COG type dielectric material will typically have very low leakage. Small surface mount polypropylene or polycarbonate film capacitors also have extremely low leakage, but are slightly larger than ceramics.

## REVERSE CURRENT PATH

The internal PNP power transistor used as the pass element in the LP2982 has an inherent diode connected between the regulator output and input. During normal operation (where the input voltage is higher than the output) this diode is reverse biased (See [Figure 4](#)).



**Figure 4. LP2982 Reverse Current Path**

However, if the input voltage is more than a  $V_{BE}$  below the output voltage, this diode will turn ON and current will flow into the regulator output. In such cases, a parasitic SCR can latch which will allow a high current to flow into the  $V_{IN}$  pin and out the ground pin, which can damage the part.

The internal diode can also be turned on if the input voltage is abruptly stepped down to a voltage which is a  $V_{BE}$  below the output voltage.

In any application where the output voltage may be higher than the input voltage, an external Schottky diode must be connected from  $V_{IN}$  to  $V_{OUT}$  (cathode on  $V_{IN}$ , anode on  $V_{OUT}$ . See [Figure 5](#)), to limit the reverse voltage across the LP2982 to 0.3V (see *Absolute Maximum Ratings*).

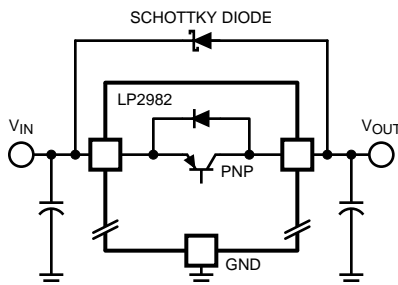


Figure 5. Adding External Schottky Diode Protection

### ON/OFF INPUT OPERATION

The LP2982 is shut off by pulling the ON/OFF input low, and turned on by driving the input high. If this feature is not to be used, the ON/OFF input should be tied to  $V_{IN}$  to keep the regulator on at all times (the ON/OFF input must **not** be left floating).

To ensure proper operation, the signal source used to drive the ON/OFF input must be able to swing above and below the specified turn-on/turn-off voltage thresholds which guarantee an ON or OFF state (see Electrical Characteristics).

The ON/OFF signal may come from either a totem-pole output, or an open-collector output with pull-up resistor to the LP2982 input voltage or another logic supply. The high-level voltage may exceed the LP2982 input voltage, but must remain within the Absolute Maximum Ratings for the ON/OFF pin.

It is also important that the turn-on/turn-off voltage signals applied to the ON/OFF input have a slew rate which is greater than 40 mV/ $\mu$ s.

**IMPORTANT:** the regulator shutdown function will not operate correctly if a slow-moving signal is applied to the ON/OFF input.

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
LP2982AIM5-3.0	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L20A	<a href="#">Samples</a>
LP2982AIM5-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L20A	<a href="#">Samples</a>
LP2982AIM5-3.3	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L19A	<a href="#">Samples</a>
LP2982AIM5-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L19A	<a href="#">Samples</a>
LP2982AIM5-5.0	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L18A	<a href="#">Samples</a>
LP2982AIM5-5.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L18A	<a href="#">Samples</a>
LP2982AIM5X-3.0	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L20A	<a href="#">Samples</a>
LP2982AIM5X-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L20A	<a href="#">Samples</a>
LP2982AIM5X-3.3	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L19A	<a href="#">Samples</a>
LP2982AIM5X-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L19A	<a href="#">Samples</a>
LP2982AIM5X-5.0	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L18A	<a href="#">Samples</a>
LP2982AIM5X-5.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L18A	<a href="#">Samples</a>
LP2982IM5-3.0	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L20B	<a href="#">Samples</a>
LP2982IM5-3.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L20B	<a href="#">Samples</a>
LP2982IM5-3.3	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L19B	<a href="#">Samples</a>
LP2982IM5-3.3/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L19B	<a href="#">Samples</a>
LP2982IM5-5.0	ACTIVE	SOT-23	DBV	5	1000	TBD	Call TI	Call TI	-40 to 125	L18B	<a href="#">Samples</a>
LP2982IM5-5.0/NOPB	ACTIVE	SOT-23	DBV	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L18B	<a href="#">Samples</a>
LP2982IM5X-3.0	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L20B	<a href="#">Samples</a>

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
LP2982IM5X-3.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L20B	<a href="#">Samples</a>
LP2982IM5X-3.3	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L19B	<a href="#">Samples</a>
LP2982IM5X-3.3/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L19B	<a href="#">Samples</a>
LP2982IM5X-5.0	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125	L18B	<a href="#">Samples</a>
LP2982IM5X-5.0/NOPB	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	-40 to 125	L18B	<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) Only one of markings shown within the brackets will appear on the physical device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

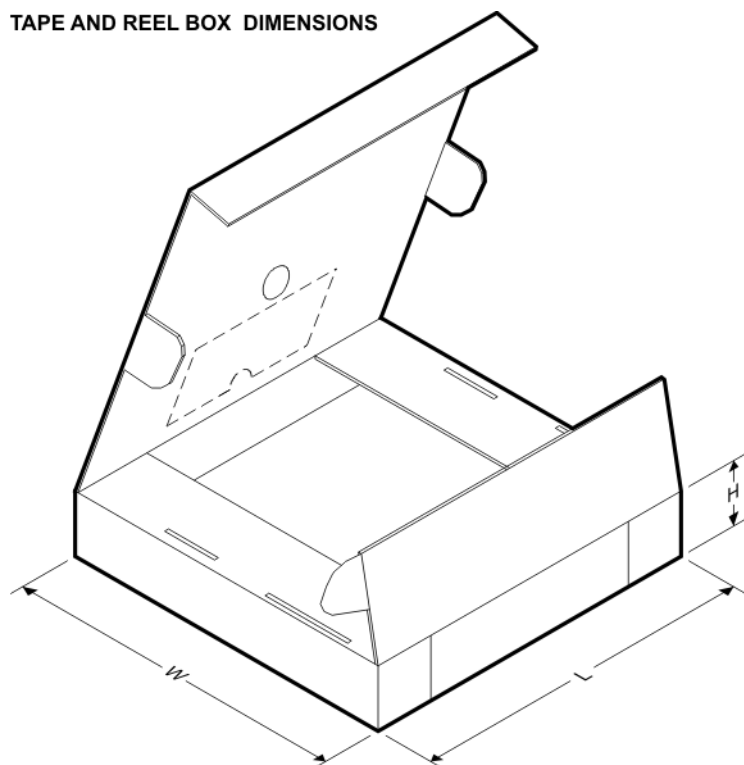
**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LP2982AIM5-3.0	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5-3.0/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5-3.3	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5-3.3/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5-5.0	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5-5.0/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-3.0	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-3.0/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-3.3	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-3.3/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-5.0	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982AIM5X-5.0/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-3.0	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-3.0/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-3.3	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-3.3/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-5.0	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5-5.0/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LP2982IM5X-3.0	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5X-3.0/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5X-3.3	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5X-3.3/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5X-5.0	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LP2982IM5X-5.0/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

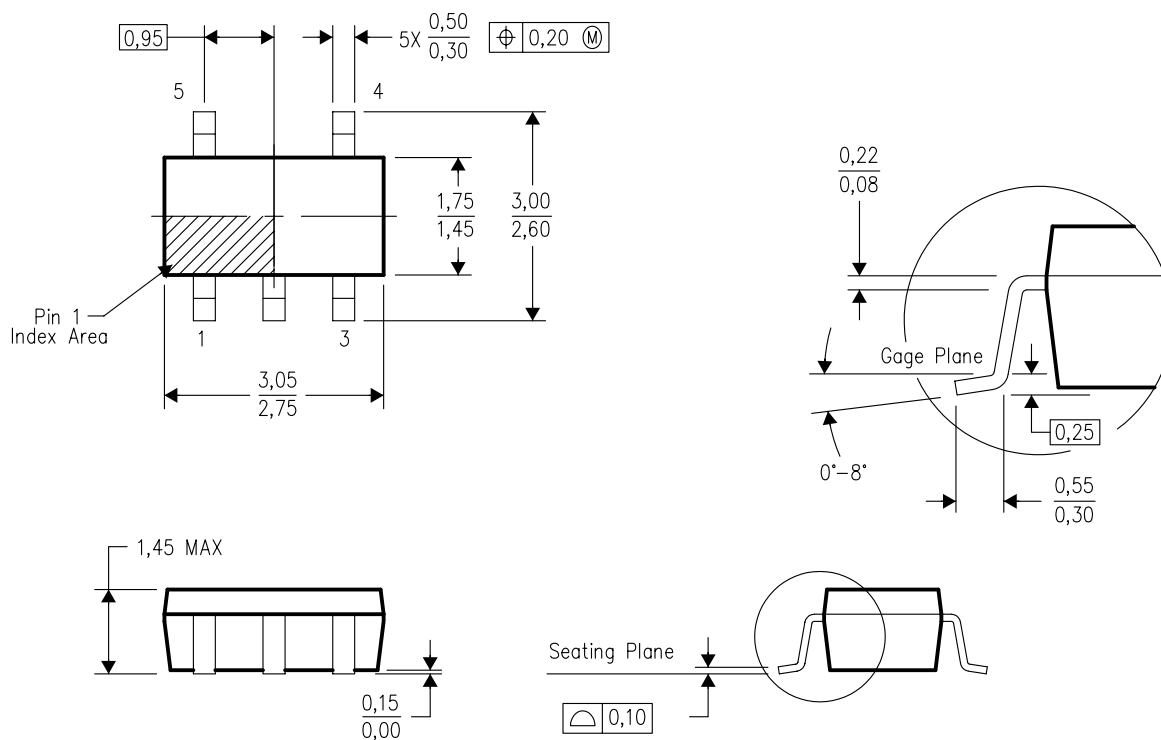
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LP2982AIM5-3.0	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5-3.0/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5-3.3	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5-3.3/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5-5.0	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5-5.0/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982AIM5X-3.0	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982AIM5X-3.0/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982AIM5X-3.3	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982AIM5X-3.3/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982AIM5X-5.0	SOT-23	DBV	5	3000	206.0	191.0	90.0



Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LP2982AIM5X-5.0/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5-3.0	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5-3.0/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5-3.3	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5-3.3/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5-5.0	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5-5.0/NOPB	SOT-23	DBV	5	1000	203.0	190.0	41.0
LP2982IM5X-3.0	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5X-3.0/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5X-3.3	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5X-3.3/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5X-5.0	SOT-23	DBV	5	3000	206.0	191.0	90.0
LP2982IM5X-5.0/NOPB	SOT-23	DBV	5	3000	206.0	191.0	90.0

## DBV (R-PDSO-G5)

## PLASTIC SMALL-OUTLINE PACKAGE



4073253-4/K 03/2006

- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - Falls within JEDEC MO-178 Variation AA.

DBV (R-PDSO-G5)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
  - D. Publication IPC-7351 is recommended for alternate designs.
  - E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

### Products

Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Applications Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Automotive and Transportation	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Communications and Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
Computers and Peripherals	<a href="http://www.ti.com/computers">www.ti.com/computers</a>
Consumer Electronics	<a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>
Energy and Lighting	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
Industrial	<a href="http://www.ti.com/industrial">www.ti.com/industrial</a>
Medical	<a href="http://www.ti.com/medical">www.ti.com/medical</a>
Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Space, Avionics and Defense	<a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a>
Video and Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>

### TI E2E Community

[e2e.ti.com](http://e2e.ti.com)