OKI semiconductor

Preliminary

MSM53256AS/RS

32,768 WORD X 8 BIT MASK ROM

GENERAL DESCRIPTION

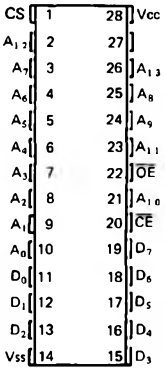
MSM53256AS/RS is a silicon gate C-MOS device ROM with a 32,768 words x 8 bit capacity. It operates on a 5 V single power supply and all inputs and outputs can be directly connected to the TTL. The adoption of an asynchronous system in the circuit requires no external clock assuring extremely easy operation. The availability of power down mode contributes to the low power dissipation which is as low as 50 μA (max) when the chip is not selected. The application of a byte system is most suitable for use as a large-capacity fixed memory for micro-computers and data terminals.

Since it provides $\overline{CE}$, CS and $\overline{OE}$ signals, the connection of output terminals of other chips with the wired OR is possible ensuring an easy expand operation of memory and bus line control.

FEATURES

- 32,768 words x 8 bits
- 5V single power supply
- Access time: 250 ns MAX
- Input/output TTL compatible
- 3-state output
- Standby current 50 μA MAX
- 28-pin DIP

PIN CONFIGURATION



$\overline{OE}$	:	Output enable
Vcc, Vss	:	Power supply voltage
$A_0 \sim A_{13}$	:	Address input
$D_0 \sim D_7$	:	Data output
$\overline{CE}$	:	Chip enable
CS	:	Chip select

Note: The $\overline{CS}$ active level is specified by customer.

FUNCTIONAL BLOCK DIAGRAM