Features

- **Current-controlled Output Current Source with 5 Input Channels**
- **2 Selectable Outputs for Grounded Laser Diodes**
- **Output Current per Channel up to 200 mA**
- **Total Output Current up to 250 mA**
- **Rise Time 1.0 ns / Fall Time 1.1 ns**
- **On-chip RF Oscillator**
- **Control of 2 Different Frequencies and Swings by Use of 4 External Resistors**
- **Oscillator Frequency Range from 200 MHz to 600 MHz**
- **Oscillator Swing to 100 mA**
- **Single 5-V Power Supply**
- **Common Enable/Disable Input**
- **TTL/CMOS Control Signals**
- **Small SSO24 Package and HP-VFQFP-N28 Package**

Applications

- **Combo Drives (DVD + CD-RW)**
- **DVD-RAM with CD-RW Capability**
- **DVD-RW with CD-RW Capability**

Description

The T0800 is a laser diode driver for the operation of two different, grounded laser diodes for DVD-RAM (650 nm) and CD-RW (780 nm). It includes five channels for five different optical power levels which are controlled by a separate IC. The read channel generates a continuous output level. The channels 2 to 5 are provided as write channels with very fast switching speeds. When a 'low' signal is applied to the NE pins, write current pulses are enabled. All channels are summed together and switched to one of the two outputs IOUTA or IOUTB by the select input SELA. Each channel can contribute up to 200 mA to the total output current of up to 250 mA. A total gain of 100 is provided between each reference current input and the selected output. Although the reference inputs are current inputs, voltage control is possible by using external resistors. An on-chip RF oscillator reduces laser mode hopping noise during read mode. Frequency and swing can be set independently for the two selectable outputs with two pairs of resistors. Oscillation is enabled by a 'high' signal at the ENOSC pin. Complete output current and oscillator switch-off is achieved by a 'low' signal at the ENABLE input.

5-Channel Laser Driver with RF Oscillator and 2 Outputs

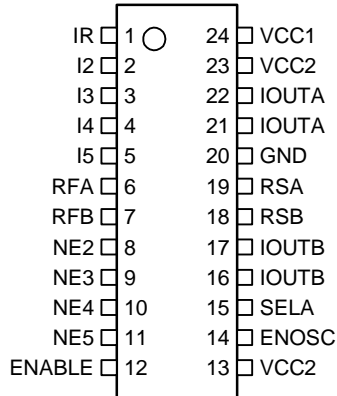# T0800

Preliminary

Pin Configuration

Figure 1. Pinning SSO24

IR	1	24	VCC1
I2	2	23	VCC2
I3	3	22	IOUTA
I4	4	21	IOUTA
I5	5	20	GND
RFA	6	19	RSA
RFB	7	18	RSB
NE2	8	17	IOUTB
NE3	9	16	IOUTB
NE4	10	15	SELA
NE5	11	14	ENOSC
ENABLE	12	13	VCC2

Pin Description: SSO24

| Pin	Symbol	Type	Function
1	IR	Analog	Input current, bias voltage approximately GND
2	I2	Analog	Input current, bias voltage approximately GND
3	I3	Analog	Input current, bias voltage approximately GND
4	I4	Analog	Input current, bias voltage approximately GND
5	I5	Analog	Input current, bias voltage approximately GND
6	RFA	Analog	External resistor to GND sets frequency of oscillator A
7	RFB	Analog	External resistor to GND sets frequency of oscillator B
8	NE2	Digital	Digital control of channel 2 (low active)
9	NE3	Digital	Digital control of channel 3 (low active)
10	NE4	Digital	Digital control of channel 4 (low active)
11	NE5	Digital	Digital control of channel 5 (low active)
12	ENABLE	Digital	Enables output current (high active)
13	VCC2	Supply	+5 V power supply for IOUT
14	ENOSC	Digital	Enables RF oscillator (high active)
15	SELA	Digital	High: selects IOUTA, RFA, RSA Low: selects IOUTB, RFB, RSB
16/17	IOUTB	Analog	Output current source B for laser diode
18	RSB	Analog	External resistor to GND sets swing of oscillator B
19	RSA	Analog	External resistor to GND sets swing of oscillator A
20	GND	Supply	Ground
21/22	IOUTA	Analog	Output current source A for laser diode
23	VCC2	Supply	+5 V power supply for IOUT
24	VCC1	Supply	+5 V power supply for circuit

Figure 2. Pinning HP-VFQFP-N28
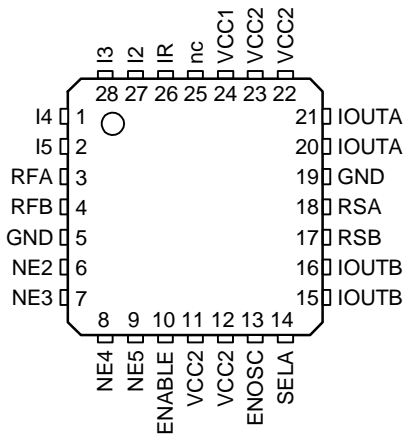
Pin Description: HP-VFQFP-N28

Pin	Symbol	Type	Function
1	I4	Analog	Input current, bias voltage approximately GND
2	I5	Analog	Input current, bias voltage approximately GND
3	RFA	Analog	External resistor to GND sets frequency of oscillator A
4	RFB	Analog	External resistor to GND sets frequency of oscillator B
5	GND	Supply	Ground
6	NE2	Digital	Digital control of channel 2 (low active)
7	NE3	Digital	Digital control of channel 3 (low active)
8	NE4	Digital	Digital control of channel 4 (low active)
9	NE5	Digital	Digital control of channel 5 (low active)
10	ENABLE	Digital	Enables output current (high active)
11, 12	VCC2	Supply	+5 V power supply IOUT
13	ENOSC	Digital	Enables RF oscillator (high active)
14	SELA	Digital	High: selects IOUTA, RFA, RSA Low: selects IOUTB, RFB, RSB
15	IOUTB	Analog	Output current source B for laser diode
16	IOUTB	Analog	Output current source B for laser diode
17	RSB	Analog	External resistor to GND sets swing of oscillator B
18	RSA	Analog	External resistor to GND sets swing of oscillator A
19	GND	Supply	Ground
20	IOUTA	Analog	Output current source A for laser diode
21	IOUTA	Analog	Output current source A for laser diode
22, 23	VCC2	Supply	+5 V power supply IOUT
24	VCC1	Supply	+5 V power supply circuit
25	nc	–	Not connected
26	IR	Analog	Input current, bias voltage approximately GND
27	I2	Analog	Input current, bias voltage approximately GND
28	I3	Analog	Input current, bias voltage approximately GND
Paddle	–	–	Should be conneced with ground

Figure 3. Block Diagram
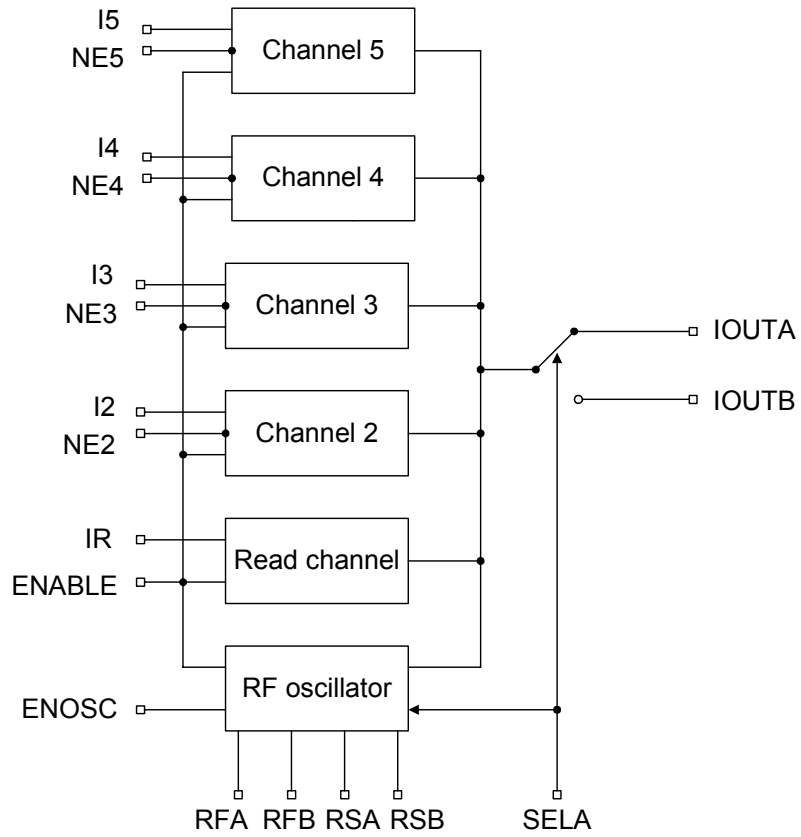
Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply voltage	V_{CC}	-0.5 to +6.0	V
Input voltage at IR, I2, I3, I4, I5	V_{IN1}	-0.5 to + 0.5	V
Input voltage at NE2, NE3, NE4, NE5, ENOSC	V_{IN2}	-0.5 to V_{CC} + 0.5	V
Output voltage	V_{OUT}	-0.5 to V_{CC} −1	V
Power dissipation	P_{tot}	0.7 [1] to 1 [2]	W
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-65 to +125	°C

Notes: 1. $R_{thJA} \leq 115$ K/W, $T_{amb} = 70$°C
2. $R_{thJA} \leq 115$ K/W, $T_{amb} = 25$°C

Thermal Resistance

Parameter	Symbol	Value [1]	Unit
Junction ambient	R_{thJA}	115 (SSO24) 35 (HP-VFQFP-N28)	K/W K/W

Note: 1.   Measured with multi-layer test board (JEDEC standard JESD51-7)

Recommended Operating Conditions

Parameter	Symbol	Value	Unit
Supply voltage range	V_{CC}	4.5 to 5.5	V
Input current	I_{IR}, I_{I2}, I_{I3}, I_{I4}, I_{I5}	< 2.5	mA
External resistor to GND to set oscillator frequency	RFA, RFB	> 3	kΩ
External resistor to GND to set oscillator swing	RSA, RSB	> 100	Ω
Operating temperature range	T_{amb}	0 to +70	°C

Electrical Characteristics

V_{CC} = 5 V, T_{amb} = 25°C, ENABLE = High, NE2 = NE3 = NE4 = NE5 = High, ENOSC = Low, unless otherwise specified

No.	Parameters	Test Conditions	Pin [1]	Symbol	Min.	Typ.	Max.	Unit	Type*
1	**Power Supply**								
1.1	Supply current, power down	ENABLE = Low, NE2 = NE3 = NE4 = NE5 = Low	11,12, 22, 23, 24	ICC_{PD2}		0.5		mA	A
1.2	Supply current, read mode, oscillator disabled	$I_{IR} = I_{I2} = I_{I3} = I_{I4} = I_{I5} = 500$ µA	11,12, 22, 23, 24	ICC_{R1}		115		mA	A
1.3	Supply current, read mode, oscillator enabled, output A selected	$I_{IR} = I_{I2} = I_{I3} = I_{I4} = I_{I5} = 500$ µA, ENOSC = High, RS = 560 Ω, RF = 7.5 kΩ, SELA = High	11,12, 22, 23, 24	ICC_{R2}		120		mA	A
1.4	Supply current, write mode	$I_{IR} = I_{I2} = I_{I3} = I_{I4} = I_{I5} = 500$ µA, NE2 = NE3 = NE4 = NE5 = Low	11,12, 22, 23, 24	ICC_W		320		mA	A
1.5	Supply current, input off	$I_{IR} = I_{I2} = I_{I3} = I_{I4} = I_{I5} = 0$ µA	11,12, 22, 23, 24	ICC_{off}		18		mA	A
2	**Digital Inputs**								
2.1	NE2/NE3/NE4/NE5 low voltage		6, 7, 8, 9	VNE_{LO}			1.1	V	A
2.2	NE2/NE3/NE4/NE5 high voltage		6, 7, 8, 9	VNE_{HI}	2.0			V	A
2.3	SELA low voltage		14	$VSELA_{LO}$			0.5	V	A
2.4	SELA high voltage		14	$VSELA_{HI}$	2.0			V	A
2.5	ENABLE low voltage		10	VEN_{LO}			0.5	V	A
2.6	ENABLE high voltage		10	VEN_{HI}	2.0			V	A
2.7	ENOSC low voltage		13	VEO_{LO}			0.5	V	A
2.8	ENOSC high voltage		13	VEO_{HI}	2.0			V	A
3	**Currents at Digital Inputs**								
3.1	NE2/NE3/NE4/NE5 low current	NE = 0 V	6, 7, 8, 9	INE_{LO}	-300			µA	A
3.2	NE2/NE3/NE4/NE5 high current	NE = 5 V	6, 7, 8, 9	INE_{HI}			800	µA	A
3.3	SELA low current	SELA = 0 V	14	$ISELA_{LO}$	-45			µA	A
3.4	SELA high current	SELA = 5 V	14	$ISELA_{HI}$			150	µA	A
3.5	ENABLE low current	ENABLE = 0 V	10	IEN_{LO}	-150			µA	A
3.6	ENABLE high current	ENABLE = 5 V	10	IEN_{HI}			100	µA	A
3.7	ENOSC low current	ENOSC = 0 V	13	IEO_{LO}	-100			µA	A
3.8	ENOSC high current	ENOSC = 5 V	13	IEO_{HI}			800	µA	A

*) Type means: A = 100% tested, B = 100% correlation tested, C = Characterized on samples, D = Design parameter

Note: 1. Related to HP-VFQFP-N28 Package

Electrical Characteristics: Laser Amplifier

V_{CC} = 5 V, T_{amb} = 25°C, ENABLE = High, unless otherwise specified

No.	Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit	Type*
4	**Outputs IOUTA and IOUTB**								
4.1	Best fit current gain	Any channel [1]	15, 16, 20, 21	GAIN	90	100	130	mA/mA	A
4.2	Best fit current offset	Any channel [1]	15, 16, 20, 21	IOS	-8		+4	mA	A
4.3	Output current linearity	Any channel [1]	15, 16, 20, 21	ILIN	-3		+3	%	A
4.5	Output current per channel	Output is sourcing	15, 16, 20, 21	I_{OUTR}	200			mA	A
4.6	Total output current		15, 16, 20, 21	I_{OUT}	250			mA	A
4.7	I_{IN} input impedance	R_{IN} is to GND	1, 2, 26, 27, 28	R_{IN}	170	220	270	Ω	A
4.8	NE threshold	Temperature stabilized	6, 7, 8, 9	VTH		1.68		V	C
4.9	Output off current 1	ENABLE = Low	15, 16, 20, 21	$IOFF_1$			1	mA	A
4.10	Output off current 2	NE2 = NE3 = NE4 = NE5 = High	15, 16, 20, 21	$IOFF_2$			1	mA	A
4.11	Output off current 3	NE2 = NE3 = NE4 = NE5 = Low, I_{IR} = I_{I2} = I_{I3} = I_{I4} = I_{I5} = 0 µA	15, 16, 20, 21	$IOFF_3$			5	mA	A
4.12	I_{OUT} supply sensitivity, read mode	I_{OUT} = 40 mA, V_{CC} = 5 V ±10%, read-only	15, 16, 20, 21	VSE_R	-5		1	%V	A
4.13	I_{OUT} supply sensitivity, write mode	I_{OUT} = 80 mA, 40 mA read + 40 mA write, V_{CC} = 5 V ±10%	15, 16, 20, 21	VSE_W	-6		0	%V	A
4.14	I_{OUT} current output noise	I_{OUT} = 40 mA, ENOSC = Low	15, 16, 20, 21	INO_O		3		nA/rt-Hz	C
4.15	I_{OUT} temperature sensitivity, read mode	I_{OUT} = 40 mA, read only	15, 16, 20, 21	TSE_R		-100		ppm/°C	C
4.16	I_{OUT} temperature sensitivity, write mode	I_{OUT} = 80 mA, 40 mA read + 40 mA write	15, 16, 20, 21	TSE_W		-300		ppm/°C	C

*) Type means: A = 100% tested, B = 100% correlation tested, C = Characterized on samples, D = Design parameter

Note: 1. Linearity of the amplifier is calculated using a best fit method at three operating points of I_{OUT} at 20 mA, 40 mA, and 60 mA, I_{OUT} = (I_{IN} × GAIN) + I_{OS}

Electrical Characteristics

$V_{CC} = 5$ V, $I_{OUT} = 40$ mA DC with 40-mA pulse, $T_{amb} = 25°C$, unless otherwise specified

No.	Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit	Type*
5	**Outputs IOUTA and IOUTB, AC Performance**								
5.1	Write rise time	$I_{OUT} = 40$ mA (read) +40 mA (10%-90%) [1]	15, 16, 20, 21	t_{RISE}		1.0	3.0	ns	C
5.2	Write fall time	$I_{OUT} = 40$ mA (read) +40 mA (10%-90%) [1]	15, 16, 20, 21	t_{FALL}		1.1	3.0	ns	C
5.3	Output current overshoot	$I_{OUT} = 40$ mA (read) + 40 mA [1]	15, 16, 20, 21	OS		5		%	C
5.4	I_{OUT} ON prop delay	NE 50% High-Low to I_{OUT} at 50% of final value	15, 16, 20, 21	t_{ON}		2.0		ns	C
5.5	I_{OFF} OFF prop delay	NE 50% Low-High to I_{OUT} at 50% of final value	15, 16, 20, 21	t_{OFF}		2.0		ns	C
5.6	Disable time	ENABLE 50% High-Low to I_{OUT} at 50% of final value	15, 16, 20, 21	t_{DIS}		20		ns	C
5.7	Enable time	ENABLE 50% Low-High to I_{OUT} at 50% of final value	15, 16, 20, 21	t_{EN}		20		ns	C
5.8	Disable time oscillator	ENOSC 50% Low-High to I_{OUT} at 50% of final value	15, 16, 20, 21	T_{DISO}		4		ns	C
5.9	Enable time oscillator	ENOSC 50% High-Low to I_{OUT} at 50% of final value	15, 16, 20, 21	T_{ENO}		2		ns	C
5.10	SELA delay	SELA Low-High 50% to I_{OUT} at 50% of final value	15, 16, 20, 21	T_{SAH}		TBD		ns	C
5.11	SELA delay	SELA High-Low 50% to I_{OUT} at 50% of final value	15, 16, 20, 21	T_{SAL}		TBD		ns	C
5.12	Amplifier bandwidth	$I_{OUT} = 50$ mA, all channels, -3 dB value	15, 16, 20, 21	BW_{LCA}		20		MHz	C
6	**Oscillator**								
6.1	Oscillator frequency	RF = 4.7 kΩ	15, 16, 20, 21	F_{OSC}	380	470	560	MHz	A
6.2	Oscillator temperature coefficient	RF = 4.7 kΩ	15, 16, 20, 21	TC_{OSC}		-150		ppm/°C	C
6.3	Disable time oscillator	ENOSC 50% High-Low to I_{OUT} , at 10%/90% of final value	15, 16, 20, 21	T_{DISO}		4		ns	C
6.4	Enable time oscillator	ENOSC 50% Low-High to I_{OUT} , at 10%/90% of final value	15, 16, 20, 21	T_{ENO}		2		ns	C

*) Type means: A = 100% tested, B = 100% correlation tested, C = Characterized on samples, D = Design parameter

Application Information Oscillator

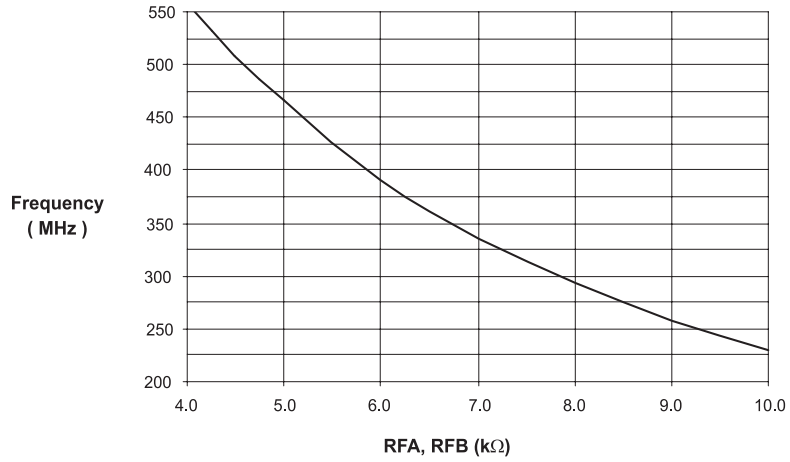**Figure 4.** Frequency vs. Resistor RFA and RFB (RS = 525 Ω)

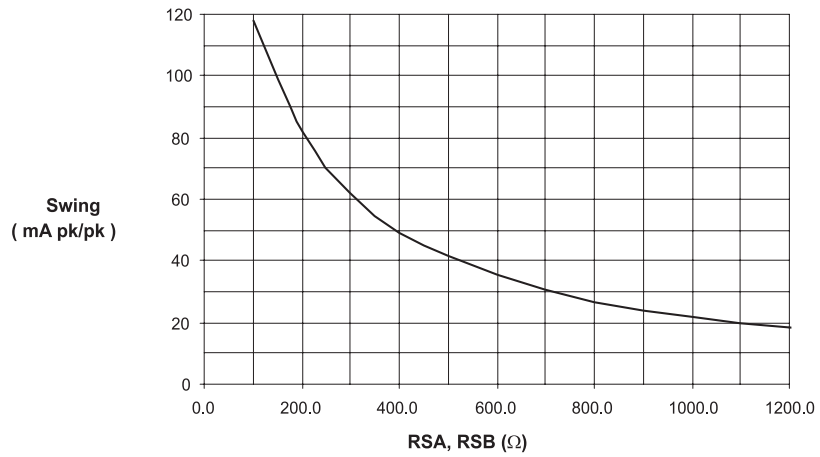**Figure 5.** Swing vs. Resistor RSA and RSB (RF = 7.82 kΩ)
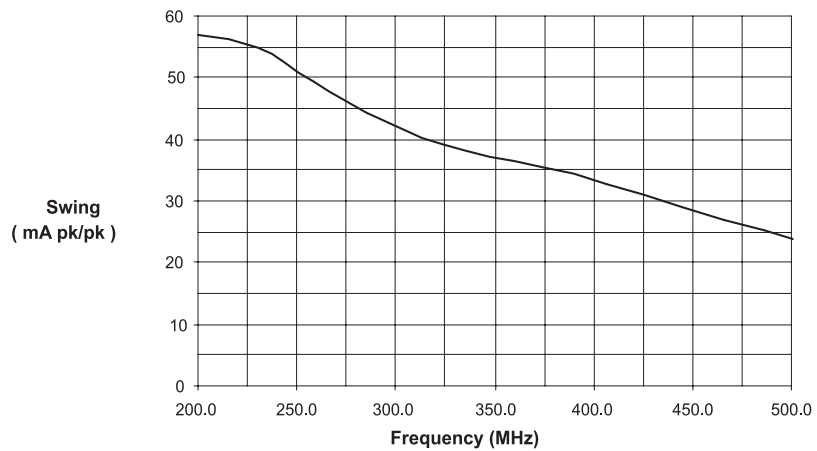

Figure 6. Frequency Dependency of Swing

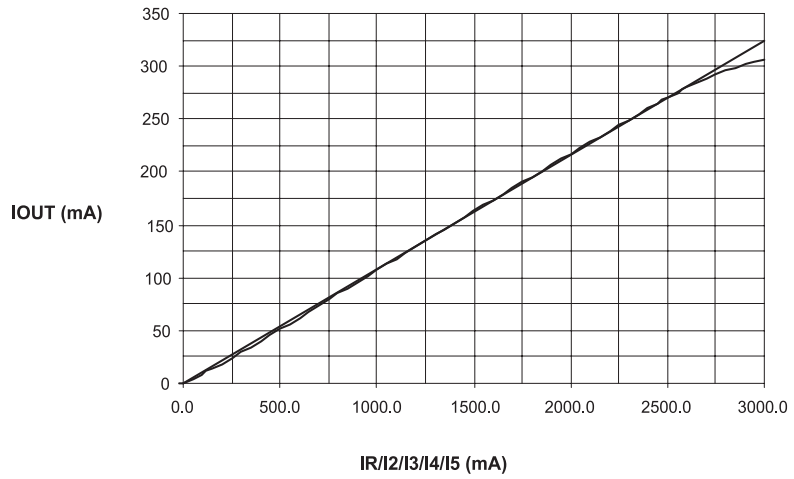**Figure 7.** Transfer Characteristic of all Channels (gain = 111)

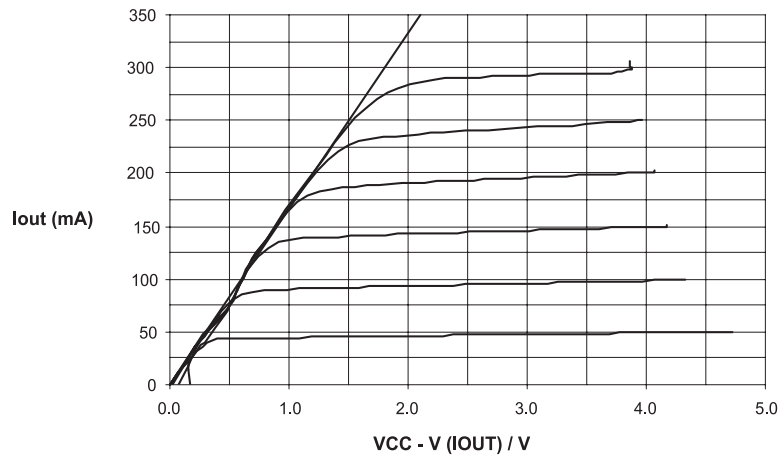**Figure 8.** Output Characteristic (Voltage Compliance) R(IOUT) = 5.8 Ω

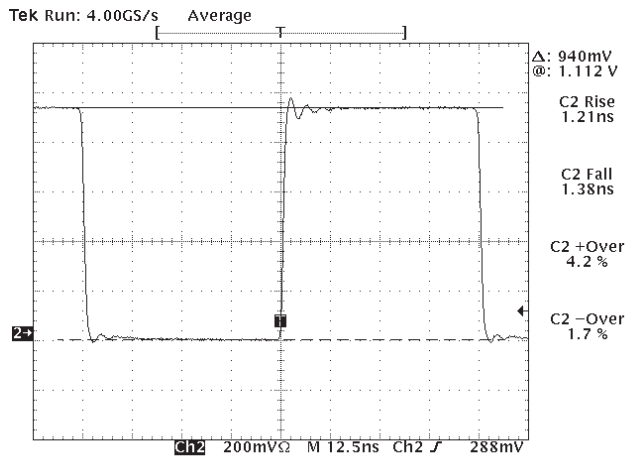**Figure 9.** Output Pulse, Read: 50 mA, Write: 250 mA pk-pk
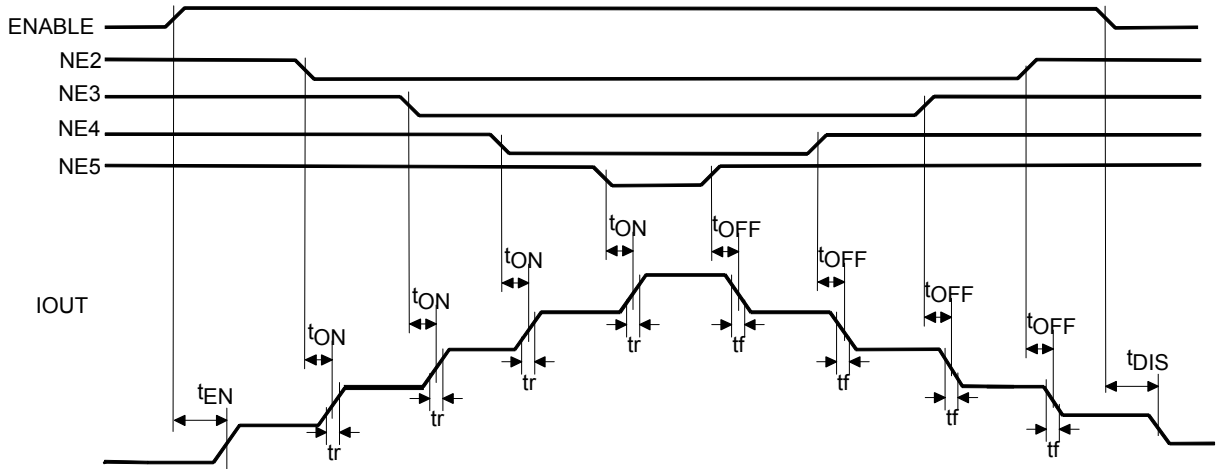
Figure 10. Timing Diagram of IOUT

Figure 11. Application Circuit
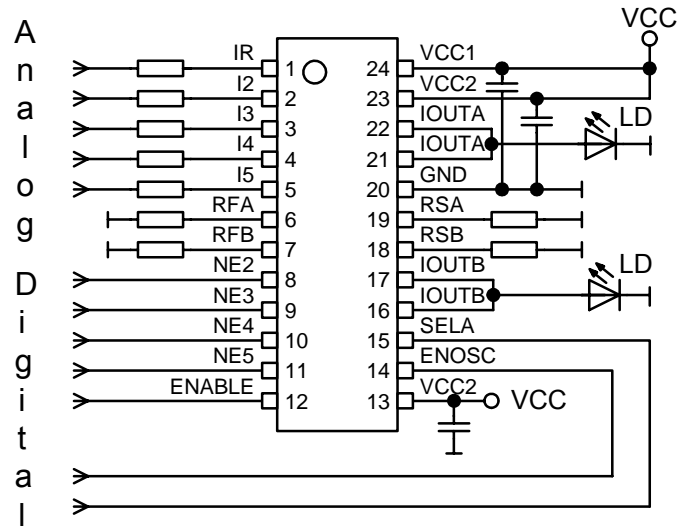
Ordering Information

Extended Type Number	Package	Remarks
T0800-TNQ	SSO24	Taped and reeled
T0800-PJQ	HP-VFQFP-N28	Taped and reeled

Package Information

Package SSO24
Dimensions in mm
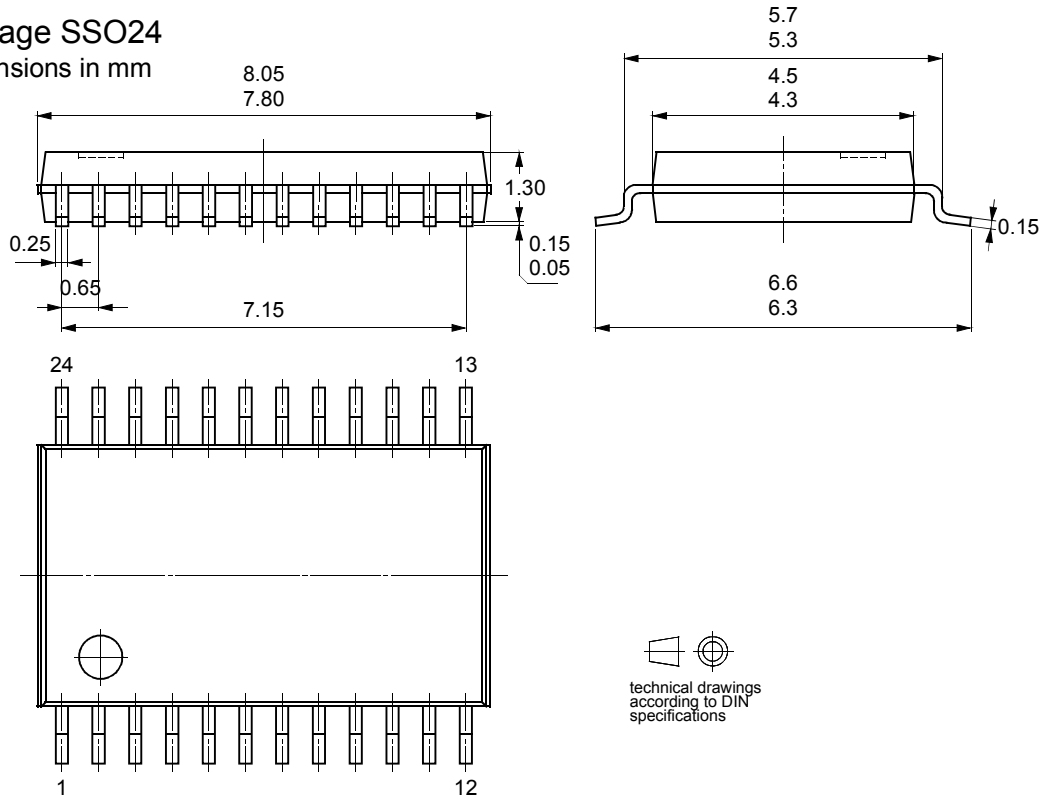
technical drawings
according to DIN
specifications

Package: HP-VFQFP-N28
Exposed pad Var. A
(acc. JEDEC OUTLINE No. MO-220)

Dimensions in mm

$0.85^{+0.15}$

$0.42 \times 45°$

$0.65^{+0.15}$

28

1

7

4.75

$0.01^{+0.04}_{-0.01}$

technical drawings
according to DIN
specifications

$0.23^{+0.07}_{-0.05}$

10:1

$0.6^{+0.15}_{-0.10}$

□ 5

□ $3.1_{±0.15}$

22   28

21   1

15   7

14   8

0.5

□3
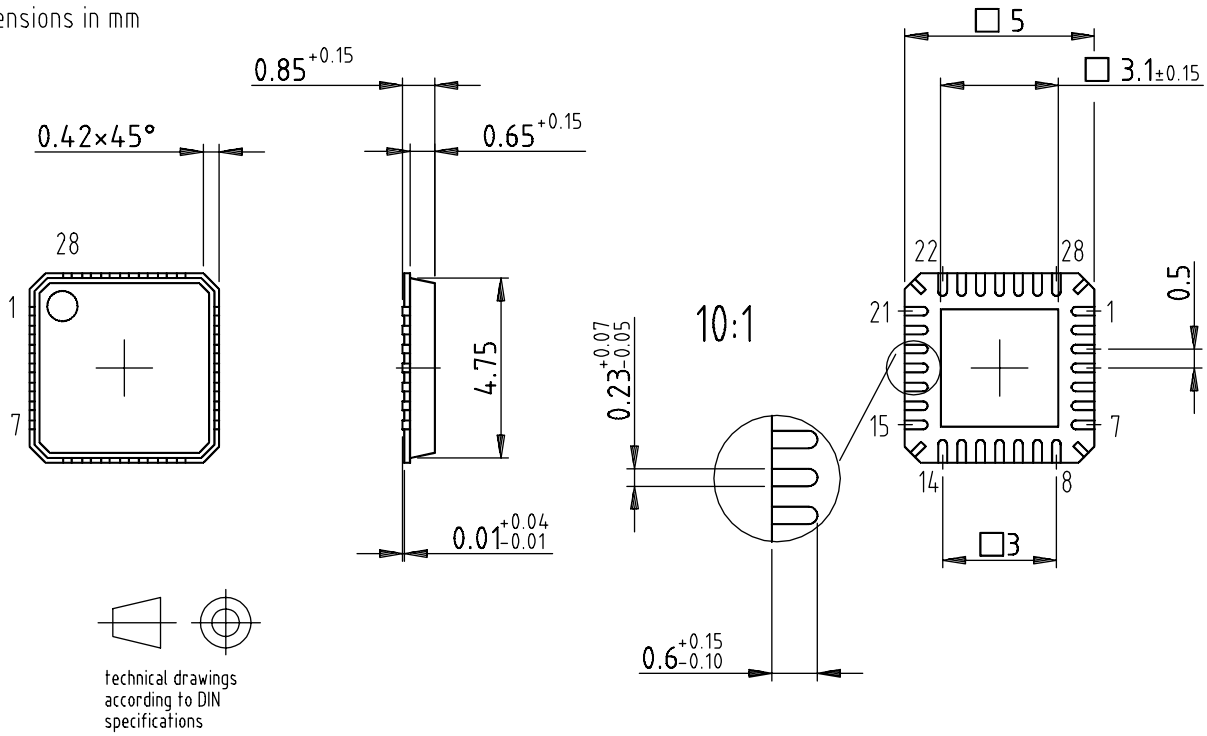
Drawing-No.: 6.543-5081.01-4
Issue: 2; 08.04.02

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