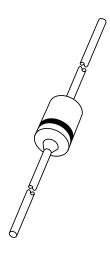
DISCRETE SEMICONDUCTORS

DATA SHEET



BYD73 series Ultra fast low-loss controlled avalanche rectifiers

Product specification Supersedes data of December 1991 File under Discrete Semiconductors, SC01 1996 May 24





Ultra fast low-loss controlled avalanche rectifiers

BYD73 series

FEATURES

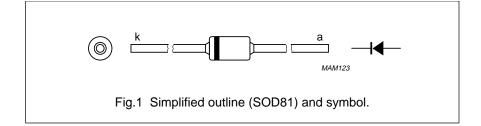
- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

DESCRIPTION

Cavity free cylindrical glass SOD81 package through Implotec^{™(1)} technology. This package is

hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

(1) Implotec is a trademark of Philips.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{RRM}	repetitive peak reverse voltage				
	BYD73A		_	50	V
	BYD73B		_	100	V
	BYD73C		_	150	V
	BYD73D		_	200	V
	BYD73E		_	250	V
	BYD73F		_	300	V
	BYD73G		_	400	V
V _R	continuous reverse voltage				
	BYD73A		_	50	V
	BYD73B		_	100	V
	BYD73C		_	150	V
	BYD73D		_	200	V
	BYD73E		_	250	V
	BYD73F		_	300	V
	BYD73G		_	400	V
I _{F(AV)}	average forward current	T _{tp} = 55 °C; lead length = 10 mm;			
	BYD73A to D	see Figs 2 and 3;	_	1.75	A
	BYD73E to G	averaged over any 20 ms period; see also Figs 10 and 11	_	1.70	А
I _{F(AV)}	average forward current	T _{amb} = 60 °C; PCB mounting (see			
	BYD73A to D	Fig.16); see Figs 4 and 5;	_	1.00	A
	BYD73E to G	averaged over any 20 ms period; see also Figs 10 and 11	_	0.95	А

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SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{FRM}	repetitive peak forward current	T _{tp} = 55 °C; see Figs 6 and 7			
	BYD73A to D		_	14	A
	BYD73E to G		_	15	A
I _{FRM}	repetitive peak forward current	T _{amb} = 60 °C; see Figs 8 and 9			
	BYD73A to D		_	8.5	A
	BYD73E to G		_	9.5	A
I _{FSM}	non-repetitive peak forward current	t = 10 ms half sine wave;	_	25	А
		$T_j = T_{j \text{ max}}$ prior to surge;			
		$V_R = V_{RRMmax}$			
E _{RSM}	non-repetitive peak reverse	$L = 120 \text{ mH}$; $T_j = T_{j \text{ max}}$ prior to	_	10	mJ
	avalanche energy	surge; inductive load switched off			
T _{stg}	storage temperature		-65	+175	°C
Tj	junction temperature		-65	+175	°C

ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	$I_F = 1 A; T_j = T_{j max};$				
	BYD73A to D	see Figs 12 and 13	_	_	0.75	V
	BYD73E to G		_	_	0.83	V
V _F	forward voltage	I _F = 1 A;				
	BYD73A to D	see Figs 12 and 13	_	_	0.98	V
	BYD73E to G		_	_	1.05	V
$V_{(BR)R}$	reverse avalanche breakdown voltage	I _R = 0.1 mA				
	BYD73A		55	_	_	V
	BYD73B		110	_	_	V
	BYD73C		165	_	_	V
	BYD73D		220	_	_	V
	BYD73E		275	_	_	V
	BYD73F		330	_	_	V
	BYD73G		440	_	_	V
I _R	reverse current	V _R = V _{RRMmax} ; see Fig.14	_	_	1	μΑ
		$V_R = V_{RRMmax};$ $T_j = 165 ^{\circ}C;$ see Fig.14	_	_	100	μΑ
t _{rr}	reverse recovery time	when switched from				
	BYD73A to D	$I_F = 0.5 \text{ A to } I_R = 1 \text{ A};$	_	_	25	ns
	BYD73E to G	measured at $I_R = 0.25 A$; see Fig.18	_	_	50	ns

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C _d	diode capacitance	f = 1 MHz; V _R = 0 V;				
	BYD73A to D	see Fig.15	_	50	_	pF
	BYD73E to G		_	40	_	pF
$\frac{ dI_R }{dt}$	maximum slope of reverse recovery current	when switched from $I_F = 1 \text{ A to V}_R \ge 30 \text{ V}$				
Tut	BYD73A to D	and $dI_F/dt = -1 A/\mu s$;	_	_	4	A/μs
	BYD73E to G	see Fig.17	_	_	5	A/μs

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point	lead length = 10 mm	60	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	120	K/W

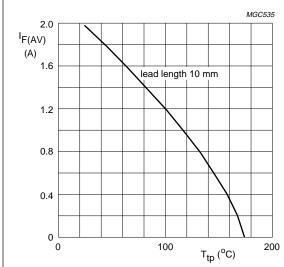
Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer ≥40 μm, see Fig.16. For more information please refer to the *'General Part of Handbook SC01.'*

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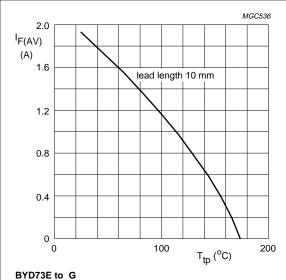
GRAPHICAL DATA



BYD73A to D

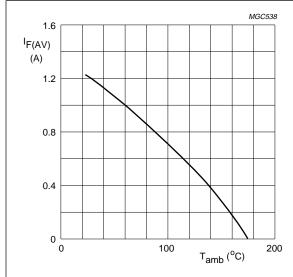
a = 1.42; $V_R = V_{RRMmax}$; $\delta = 0.5$. Switched mode application.

Fig.2 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



a = 1.42; $V_R = V_{RRMmax}$; $\delta = 0.5$. Switched mode application.

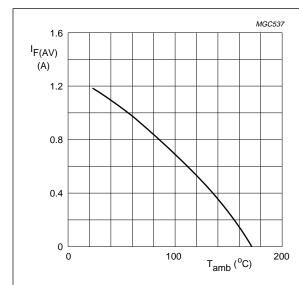
Fig.3 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



BYD73A to D

a = 1.42; $V_R = V_{RRMmax}$; $\delta = 0.5$. Device mounted as shown in Fig.16. Switched mode application.

Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).



BYD73E to G

a = 1.42; $V_R = V_{RRMmax}$; $\delta = 0.5$.

Device mounted as shown in Fig.16.

Switched mode application.

Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).

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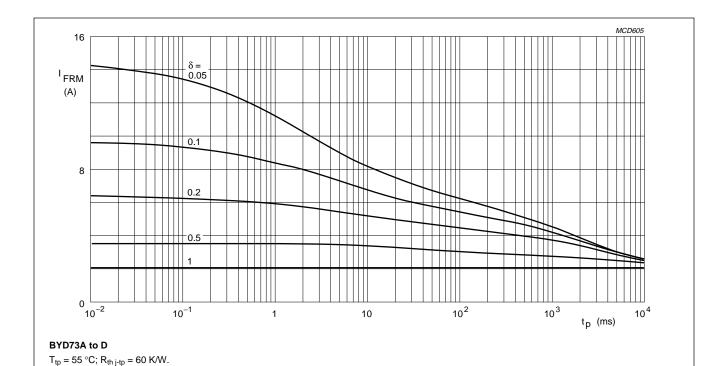
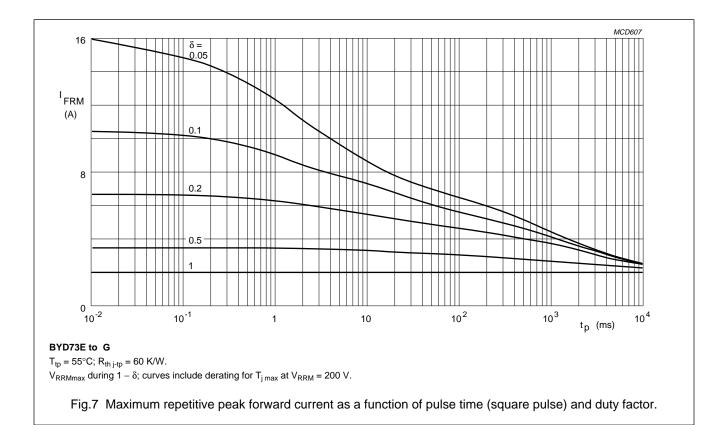


Fig.6 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.



 V_{RRMmax} during 1 – δ ; curves include derating for $T_{j max}$ at V_{RRM} = 200 V.

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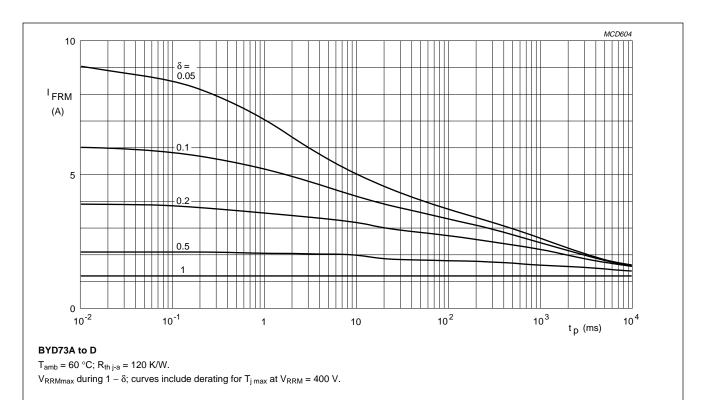
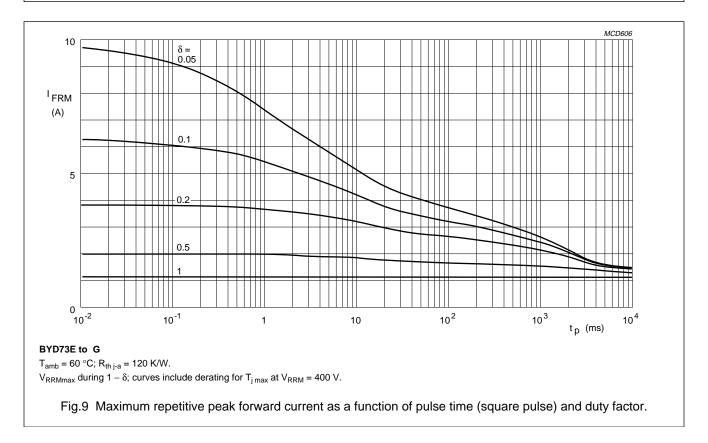


Fig.8 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.



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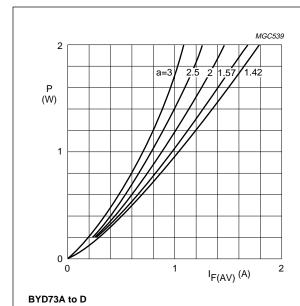


Fig.10 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.

 $a = I_{F(RMS)}/I_{F(AV)}; \ V_R = V_{RRMmax}; \ \delta = 0.5.$

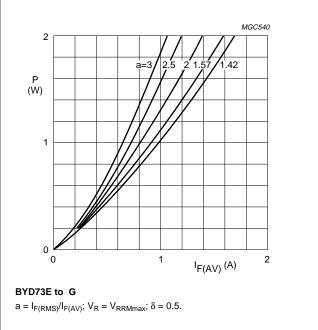
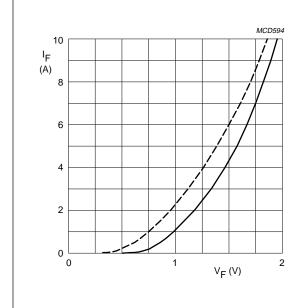


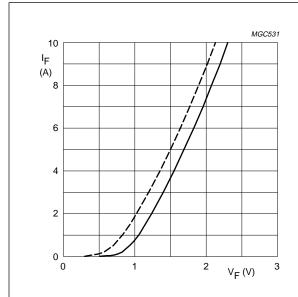
Fig.11 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.



BYD73A to D

Dotted line: $T_j = 175$ °C. Solid line: $T_i = 25$ °C.

Fig.12 Forward current as a function of forward voltage; maximum values.



BYD73E to G

Dotted line: $T_j = 175$ °C. Solid line: $T_j = 25$ °C.

Fig.13 Forward current as a function of forward voltage; maximum values.

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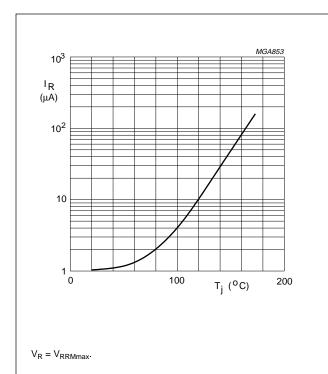


Fig.14 Reverse current as a function of junction temperature; maximum values.

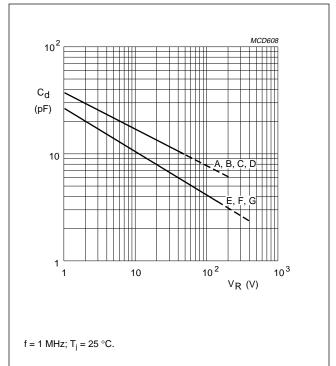
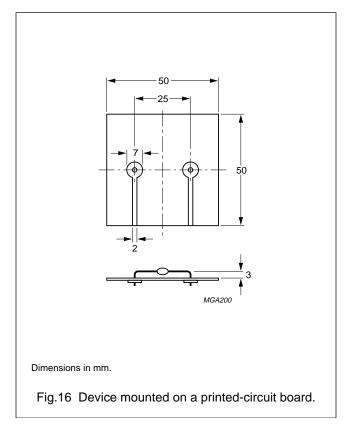
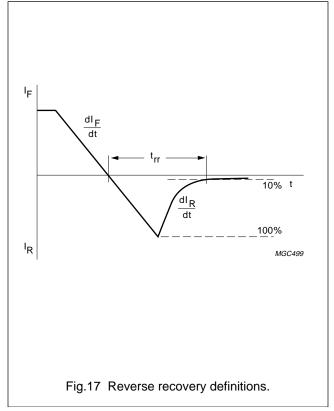


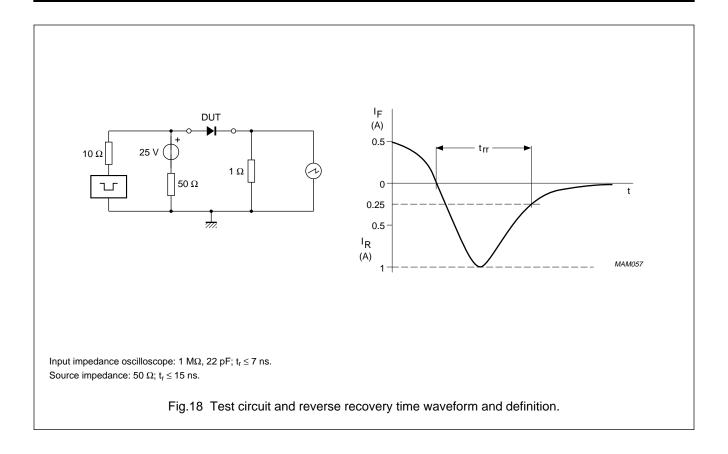
Fig.15 Diode capacitance as a function of reverse voltage; typical values.





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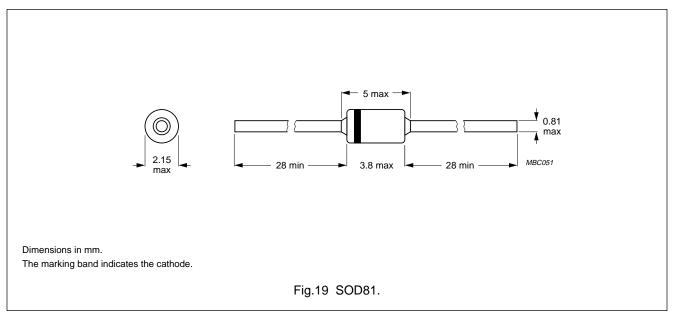
BYD73 series



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PACKAGE OUTLINE



DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.