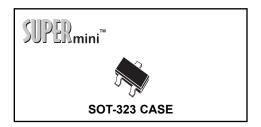
CMSSH-3 CMSSH-3A CMSSH-3C CMSSH-3S SURFACE MOUNT SUPERmini™ SILICON SCHOTTKY DIODES





## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMSSH-3 Series types are Silicon Schottky Diodes, epoxy molded in a SUPERmini™ surface mount package, designed for fast switching applications requiring a low forward voltage drop.

CMSSH-3: SINGLE MARKING CODE: 95D
CMSSH-3A: DUAL, COMMON ANODE
CMSSH-3C: DUAL, COMMON CATHODE
CMSSH-3S: DUAL, IN SERIES

MARKING CODE: B1D
MARKING CODE: B2D
MARKING CODE: A5D

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)				
пилитов. (1 <sub>A</sub> -25 6)	SYMBOL		UNITS	
Peak Repetitive Reverse Voltage	$V_{RRM}$	30	V	
Continuous Forward Current	I <sub>F</sub>	100	mA	
Peak Repetitive Forward Voltage	I <sub>FRM</sub>	350	mA	
Forward Surge Current, tp=10ms	<sup>I</sup> FSM	750	mA	
Power Dissipation	$P_D$	250	mW	
Operating and Storage				
Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C	
Thermal Resistance	$\Theta_{\sf JA}$	500	°C/W	

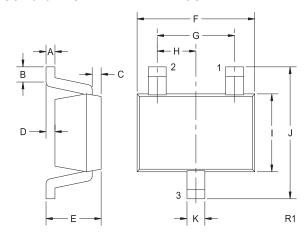
**ELECTRICAL CHARACTERISTICS PER DIODE:** (T<sub>A</sub>=25°C unless otherwise noted) SYMBOL **TEST CONDITIONS** MIN **TYP** MAX **UNITS**  $B_{VR}$ I<sub>R</sub>=100μA 30 ٧  $V_{\mathsf{F}}$ I<sub>E</sub>=2.0mA 0.29 0.33 ٧  $V_{\mathsf{F}}$ I<sub>F</sub>=15mA 0.40 0.45 ٧  $V_{\mathsf{F}}$ I<sub>E</sub>=100mA 0.74 1.00 ٧  $V_R = 25V$ 90 500  $I_R$ nΑ  $V_R = 25V, T_A = 100$ °C 25 100 μΑ  $I_R$ рF  $C_{\mathsf{T}}$  $V_R$ =1.0V, f=1 MHz 7.0  $I_F = I_R = 10$ mA,  $I_{rr} = 1.0$ mA,  $R_L = 100$  $\Omega$ 5.0 t<sub>rr</sub> ns



CMSSH-3 CMSSH-3A CMSSH-3C CMSSH-3S

## SURFACE MOUNT SUPERmini™ SILICON SCHOTTKY DIODES

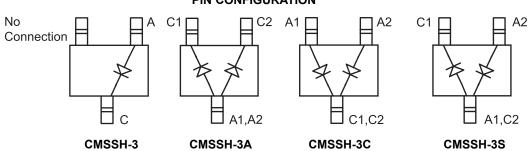
## **SOT-323 CASE - MECHANICAL OUTLINE**



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.004	0.008	0.10	0.20		
В	0.004	-	0.10	-		
С	0.004	0.008	0.10	0.20		
D	-	0.004	_	0.10		
Е	0.031	0.043	0.80	1.10		
F	0.071	0.087	1.80	2.20		
G	0.051		1.30			
Н	0.026		0.65			
	0.045	0.053	1.15	1.35		
J	0.079	0.087	2.00	2.20		
K	0.008	0.016	0.20	0.40		
SOT-323 (REV: R1)						

**MARKING CODE:** 

## PIN CONFIGURATION



95D B1D B2D

MARKING CODE: MARKING CODE:

R1 (13-November 2002)

**MARKING CODE:** 

A5D