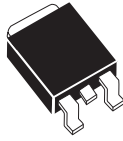


CSHD5-25L

SCHOTTKY RECTIFIER  
SINGLE, 5.0 AMPS, 25 VOLTS  
LOW FORWARD VOLTAGE DROP

LOW  
V<sub>F</sub>



DPAK RECTIFIER CASE

**Central**<sup>TM</sup>  
**Semiconductor Corp.**

**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CSHD5-25L is a Silicon Schottky Rectifier designed for surface mount power applications requiring a low forward voltage drop.

**MARKING CODE: FULL PART NUMBER**

**MAXIMUM RATINGS:** (T<sub>C</sub>=25°C unless otherwise noted)

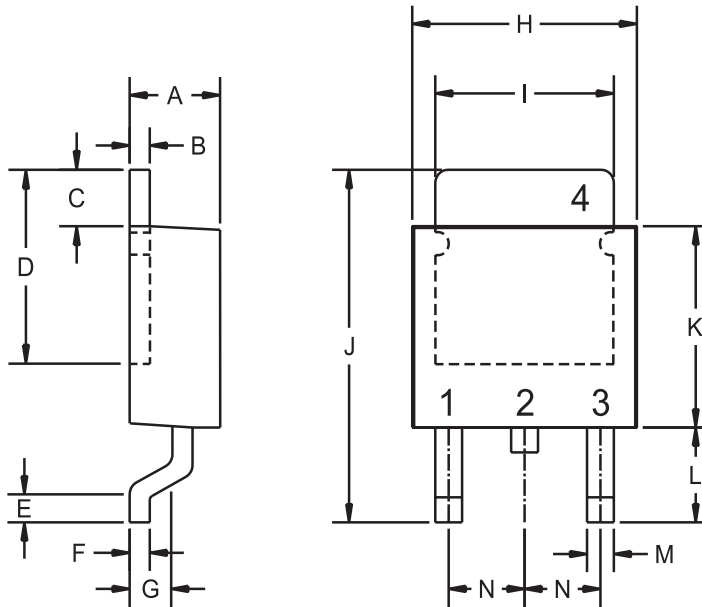
	<b>SYMBOL</b>		<b>UNITS</b>
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	25	V
Average Rectified Forward Current (T <sub>C</sub> =120°C)	I <sub>O</sub>	5.0	A
Peak Forward Surge Current (tp=10ms)	I <sub>FSM</sub>	80	A
Peak Repetitive Reverse Surge Current (tp=2μs)	I <sub>RSM</sub>	1.0	A
Critical Rate of Rise of Reverse Voltage	dv/dt	10,000	V/μs
Operating and Storage			
Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	θ <sub>JC</sub>	2.5	°C/W

**ELECTRICAL CHARACTERISTICS:** (T<sub>C</sub>=25°C unless otherwise noted)

<b>SYMBOL</b>	<b>TEST CONDITIONS</b>	<b>MIN</b>	<b>MAX</b>	<b>UNIT</b>
I <sub>R</sub>	V <sub>R</sub> =25V		500	μA
I <sub>R</sub>	V <sub>R</sub> =25V, T <sub>C</sub> =125°C		200	mA
V <sub>F</sub>	I <sub>F</sub> =5.0A		0.47	V
V <sub>F</sub>	I <sub>F</sub> =5.0A, T <sub>C</sub> =125°C		0.35	V

R2 (26-September 2002)

DPAK RECTIFIER CASE - MECHANICAL OUTLINE



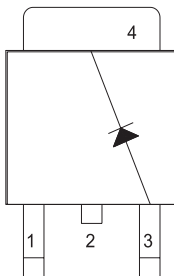
R1

**LEAD CODE:**

- 1) NO CONNECTION
- 2) CATHODE
- 3) ANODE
- 4) CATHODE

PIN 2 IS COMMON TO THE TAB (4).

**MARKING CODE: FULL PART NUMBER**



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.093	0.108	2.35	2.75
B	0.016	0.024	0.40	0.60
C	0.063		1.60	
D	0.203	0.219	5.15	5.55
E	0.012	0.028	0.30	0.70
F	0.020		0.50	
G	0.035	0.051	0.90	1.30
H	0.252	0.268	6.40	6.80
I	0.197	0.213	5.00	5.40
J	-	0.394	-	10.00
K	0.209	0.224	5.30	5.70
L	0.090	0.106	2.30	2.70
M	0.016	0.031	0.40	0.80
N	0.091		2.30	

DPAK RECTIFIER (REV: R1)