TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

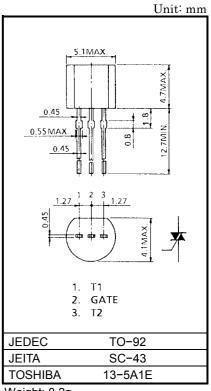
SM08G43

AC POWER CONTROL APPLICATIONS

• Repetitive Peak Off–State Voltage $: V_{DRM} = 400V$ • R.M.S On-State Current $: I_{T (RMS)} = 0.8A$

MAXIMUM RATINGS

_				
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage	V_{DRM}	400	V	
R.M.S On-State Current (Full Sine Waveform Tc = 65°C)	I _{T (RMS)}	0.8	А	
Peak One Cycle Surge On-State	I _{TSM}	6 (50Hz)	Α	
Current (Non-Repetitive)		6.6 (60Hz)	A	
I ² t Limit Value	I ² t	0.18	A ² s	
Peak Gate Power Dissipation	P _{GM}	0.5	W	
Average Gate Power Dissipation	P _{G (AV)}	0.05	W	
Peak Gate Voltage	V_{GM}	5	V	
Peak Gate Current	I _{GM}	0.3	Α	
Junction Temperature	Tj	-40~125	°C	
Storage Temperature Range	T _{stg}	-40~125	°C	

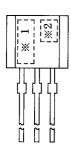


Weight: 0.2g

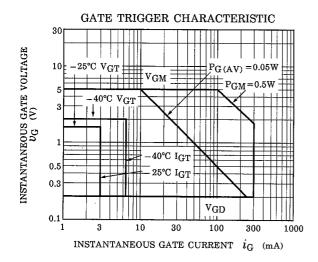
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

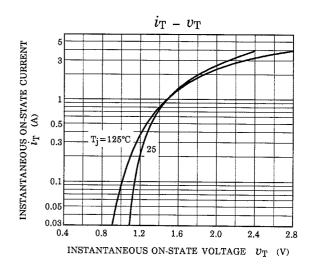
CHARACTERIS	TIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State (Current		I _{DRM}	RM V _{DRM} = Rated		_	_	10	μΑ
Gate Trigger Voltage	I	(1+)	V _{GT}	V _D = 12V, R _L = 20Ω	T2 (+) , Gate (+)	_	_	_	V
	Ш	(1-)			T2 (+) , Gate (-)	_	_	1.5	
	III	(3-)			T2 (-) , Gate (-)	_	_	1.5	
	IV	(3+)			T2 (-) , Gate (+)	_	_	_	
Gate Trigger Current	I	(1+)	lgт		T2 (+), Gate (+)	_	_	_	- mA
	II	(1-)			T2 (+) , Gate (-)	_	_	3	
	III	(3-)			T2 (-) , Gate (-)	_	_	3	
	IV	(3+)			T2 (-) , Gate (+)	_	_	_	
Peak On-State Voltage		V _{TM}	I _{TM} = 1.2A		_	_	1.5	V	
Gate Non-Trigger Voltage		V _{GD}	V _D = Rated, Tc = 125°C		0.2	_	_	V	
Holding Current		lΗ	V _D = 12V, Gate Open		_	_	10	mA	
Thermal Resistance		R _{th (j−c)}	Junction to Case		_	_	50	°C/W	
Thermal Resistance	hermal Resistance R _{th (j-a)} Junction to Ambient		pient	_	_	220	°C/W		

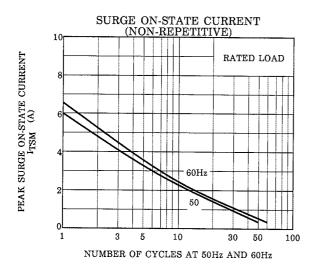
MARKING

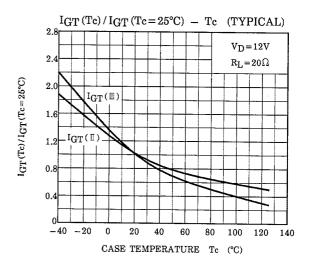


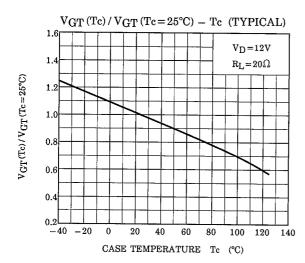
NUMBER	SYMBOL		MARK
*1	TYPE	SM08G43	M08G
*2	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)		Example 8A: January 1998 8B: February 1998 8L: December 1998

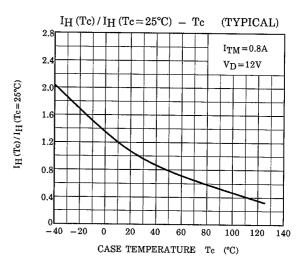




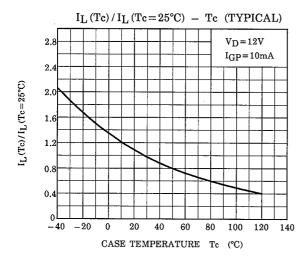


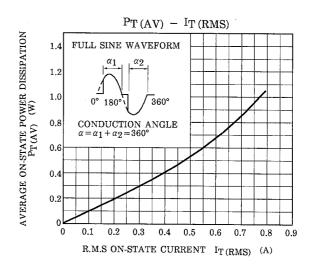


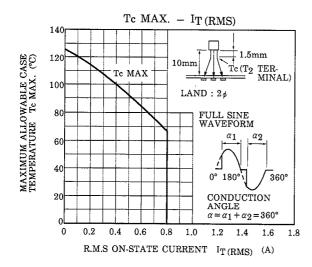


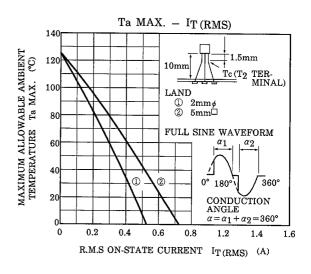


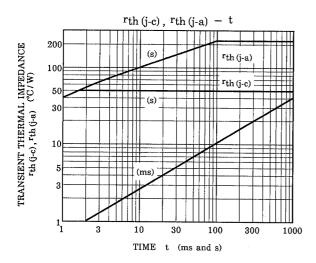
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