TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM3GZ47,SM3JZ47

AC POWER CONTROL APPLICATIONS

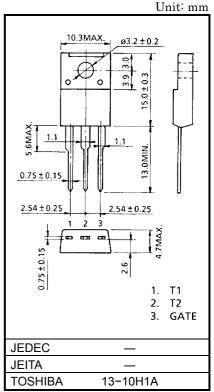
Repetitive Peak Off-State Voltage : V_{DRM} = 400, 600V
 R.M.S ON-State Current : I_T (RMS) = 3A

• High Commutating (dv / dt)

• Isolation Voltage : V_{ISOL} = 1500V AC

MAXIMUM RATINGS

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage	SM3GZ47	V_{DRM}	400	V	
	SM3JZ47	V DRM	600	v	
R.M.S On-State Current (Full Sine Waveform Tc = 110°C)		I _{T (RMS)}	3	А	
Peak One Cycle Surge On-State Current (Non-Repetitive)		l=a	30 (50Hz)	Α	
		ITSM	33 (60Hz)		
I ² t Limit Value (t = 1~10n	ı²t	4.5	A ² s		
Critical Rate of Rise of O Current	di / dt	50	A/μs		
Peak Gate Power Dissipa	ation	P _{GM}	5	W	
Average Gate Power Dissipation		P _{G (AV)}	0.5	W	
Peak Gate Voltage		V_{GM}	10	V	
Peak Gate Current		I _{GM}	2	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Ra	T _{stg}	-40~125	°C		
Isolation Voltage (AC, t =	V _{ISOL}	1500	V		



Weight: 1.7g

Note 1: di / dt test condition $V_{DRM} = 0.5 \times Rated$ $I_{TM} \le 4.5 A$ $t_{gw} \ge 10 \mu s$ $t_{gr} \le 250 ns$

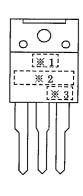
 $i_{gp} = I_{GT} \times 2.0$



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

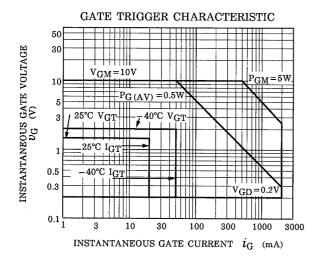
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I _{DRM}	V _{DRM} = Rated		_	_	20	μA
Gate Trigger Voltage	I	V _{GT}	V _D = 12V R _L = 20Ω	T2 (+), Gate (+)	_	_	1.5	V
	II			T2 (+), Gate (-)	_	_	1.5	
	III			T2 (-), Gate (-)	_	_	1.5	
	IV			T2 (-), Gate (+)	_	_	_	
Gate Trigger Current	I	l _{GT}	V _D = 12V R _L = 20Ω	T2 (+), Gate (+)	_	_	20	- mA
	II			T2 (+), Gate (-)	_	_	20	
	III			T2 (-), Gate (-)	_	_	20	
	IV			T2 (-), Gate (+)	_	_	_	
Peak On-State Voltage		V _{TM}	I _{TM} = 4.5A		_	_	1.5	V
Gate Non-Trigger Voltage V _{GD} V _D = Rate		V _D = Rated, To	c = 125°C	0.2	_	_	V	
Holding Current I_H $V_D =$		V _D = 12V, I _{TM} = 1A		_	_	30	mA	
Thermal Resistance	nermal Resistance R _{th (j-c)} Junction to Case, AC		_	_	4.2	°C/W		
Critical Rate of Rise of Off-State Voltage		dv / dt	V _{DRM} = Rated, T _j = 125°C Exponential Rise		_	300	_	V / µs
Critical Rate of Rise of Off-State Voltage at Commutation		(dv / dt) c	$V_{DRM} = 400V, T_j = 125^{\circ}C$ (di /dt) c = -2.0A / ms		10	_	_	V / µs

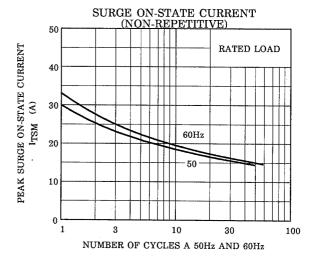
MARKING

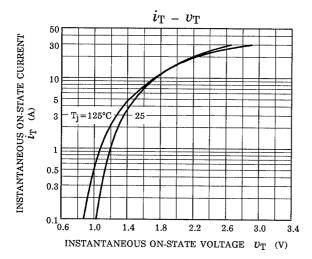


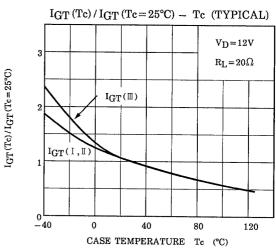
NUMBER		MARK	
* 1	TOSHIBA PRODUC	7	
* 2 TYPE	SM3GZ47	M3GZ47	
	1111 E	SM3JZ47	M3JZ47
* 3	Lot Number Month Year	Example 8A : January 1998 8B : Febrary 1998 8L : December 1998	

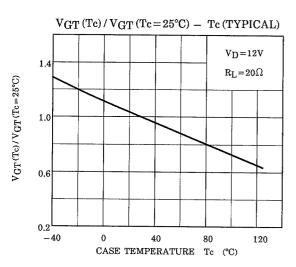
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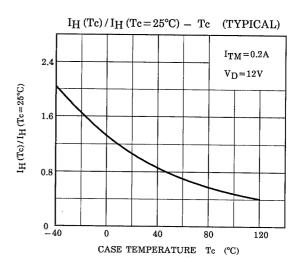




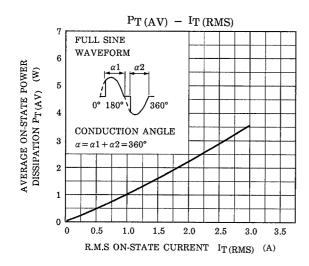


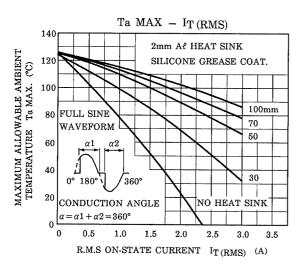


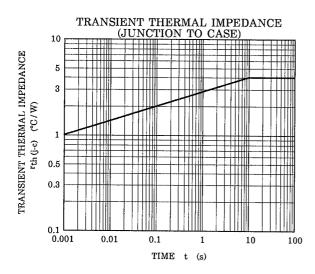


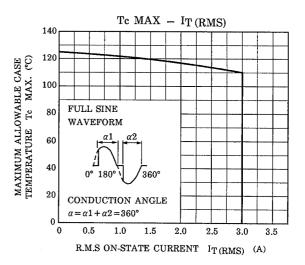


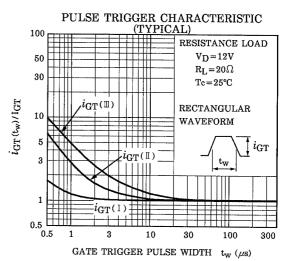
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