TOSHIBA THYRISTOR SILICON PLANAR TYPE

# **SF0R3G42**

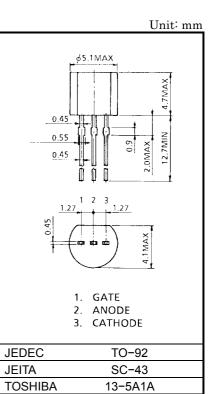
#### LOW POWER SWITCHING AND CONTROL **APPLICATIONS**

- Repetitive Peak Off-State Voltage : VDRM = 400V Repetitive Peak Reverse Voltage  $: V_{RRM} = 400V$
- Average On–State Current

- $: I_{T}(AV) = 300 \text{mA}$
- Plastic Mold Type. •

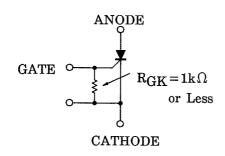
### **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage and Repetitive Peak Reverse Voltage $(R_{GK} = 1k\Omega)$	V <sub>DRM</sub> V <sub>RRM</sub>	400	V	
Non-Repetitive Peak Reverse Voltage (Non-Repetitive<5ms, $R_{GK} = 1k\Omega$ , $T_j = 0 \sim 125^{\circ}C$ )	V <sub>RSM</sub>	500	V	
Average On-State Current (Half Sine Waveform Ta = 45°C)	I <sub>T (AV)</sub>	300	mA	
R.M.S On-State Current	I <sub>T (RMS)</sub>	450	mA	
Peak One Cycle Surge On-State Current (Non-Repetitive)	I <sub>TSM</sub>	9 (50Hz)	A	
		9.9 (60Hz)		
I <sup>2</sup> t Limit Value	l <sup>2</sup> t	0.4	A <sup>2</sup> s	
Peak Gate Power Dissipation	P <sub>GM</sub>	0.1	W	
Average Gate Power Dissipation	P <sub>G (AV)</sub>	0.01	W	
Peak Forward Gate Voltage	V <sub>FGM</sub>	3.5	V	
Peak Reverse Gate Voltage	V <sub>RGM</sub>	-5	V	
Peak Forward Gate Current	I <sub>GM</sub>	125	mA	
Junction Temperature	Tj	-40~125	°C	
Storage Temperature Range	T <sub>stg</sub>	-40~125	°C	



Weight: 0.2g

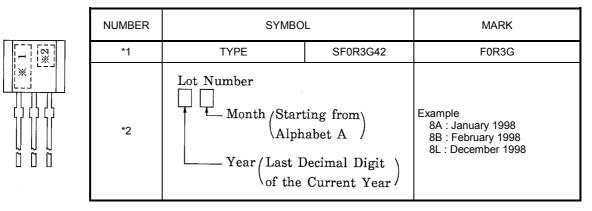
Note: Should be used with gate resistance as follows.

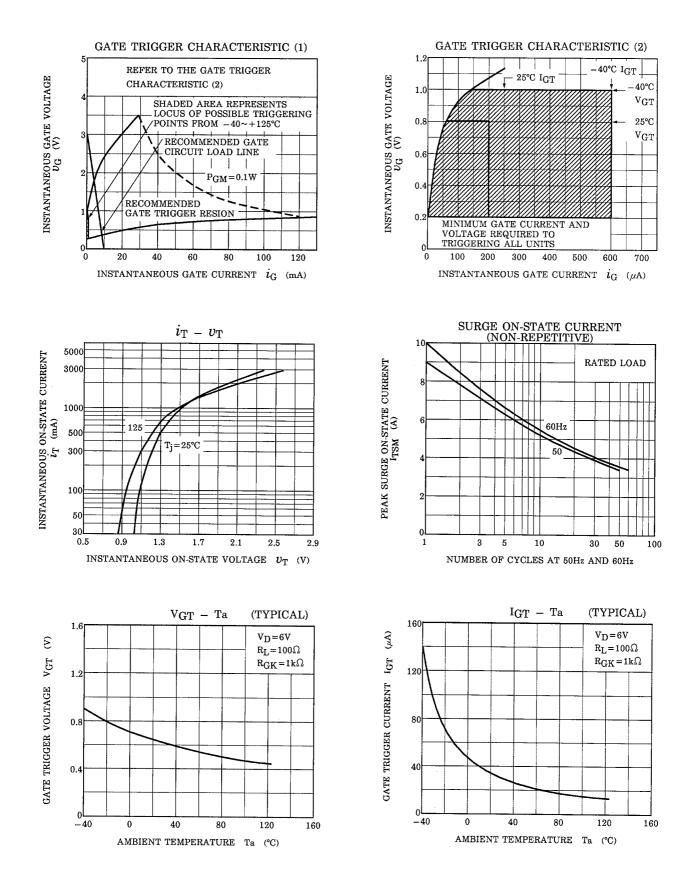


## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

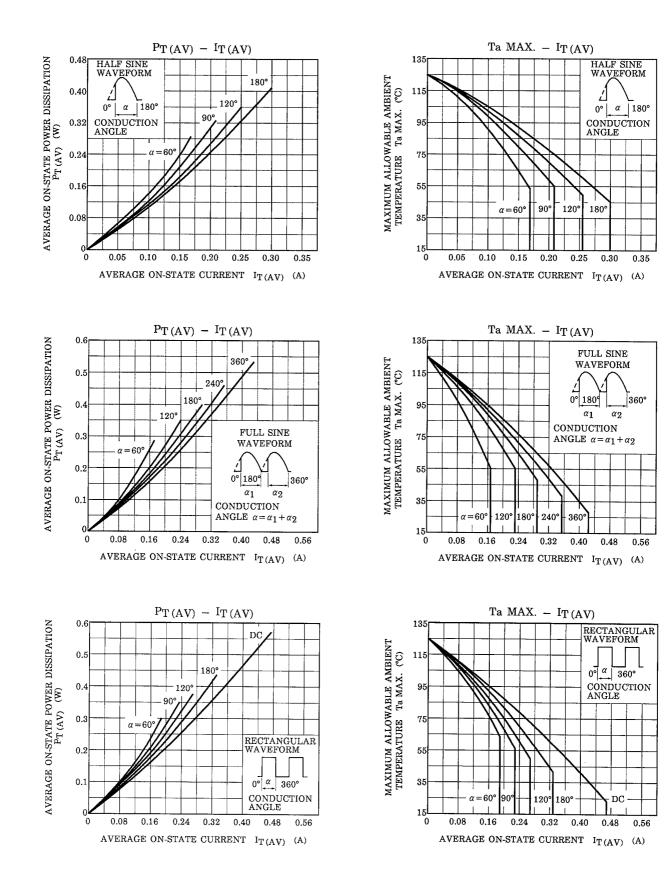
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub> = Rated R <sub>GK</sub> = 1kΩ, T <sub>j</sub> = 125°C	_	_	100	μA
Peak On-State Voltage	V <sub>TM</sub>	I <sub>TM</sub> = 2A	_	_	2.0	V
Gate Trigger Voltage	V <sub>GT</sub>	V <sub>D</sub> = 6V, R <sub>I</sub> = 100Ω, R <sub>GK</sub> = 1kΩ	-	-	0.8	V
Gate Trigger Current	I <sub>GT</sub>	$v_D = 0v, r_L = 10002, r_{GK} = 10022$	_	_	200	μA
Gate Non-Trigger Voltage	V <sub>GD</sub>	V <sub>D</sub> = Rated, R <sub>GK</sub> = 1kΩ, Ta = 125°C	0.2	_	_	V
Holding Current	Ι <sub>Η</sub>	R <sub>L</sub> = 100Ω, R <sub>GK</sub> = 1kΩ	_	4	_	mA
Thermal Resistance	R <sub>th (j−a)</sub>	Junction to Ambient	-	-	250	°C/W

## MARKING

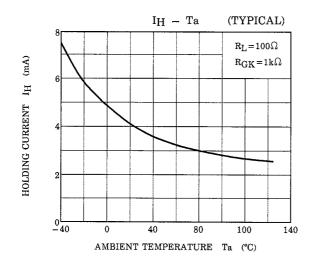


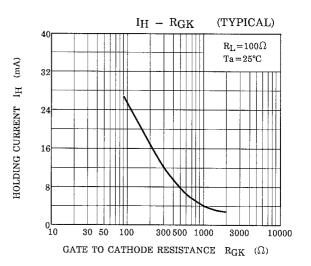


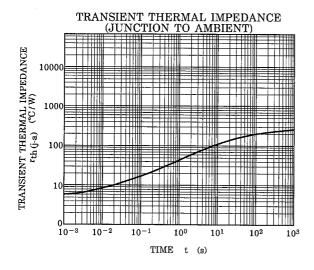
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